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PROJECT REFERENCE NO.	SHEET NO.
R-5816	Sig.1.10

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3 AND 4.

2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```
LOGICAL I/O COMMAND #1  (+/-COMMAND#)
IF  ACTIVE OVERLAP     #6  IS ON
AND RED CLEAR ON OVL   #6  IS ON

            SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT  #39  ON
SET OUTPUT ASSIGNMENT  #40  OFF
```

NOTE: LOGIC FOR OVERLAP F RED CLEAR (HEADS 44 AND 45).

PRESS '+'

```
LOGICAL I/O COMMAND #2  (+/-COMMAND#)
IF  ACTIVE OVERLAP     #6  IS ON

            SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #41  OFF
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" (HEADS 44 AND 45).

PRESS '+'

```
LOGICAL I/O COMMAND #3  (+/-COMMAND#)
IF  YELLOW ON OVERLAP  #6  IS ON

            SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #40 ON
```

NOTE: LOGIC FOR YELLOW ARROW (HEADS 44 AND 45).

PRESS '+'

```
LOGICAL I/O COMMAND #4  (+/-COMMAND#)
IF  OUTPUT ASSIGNMENT #2  IS ON
AND OUTPUT ASSIGNMENT #5  IS OFF

            SCROLL DOWN

THEN:
SET OUTPUT ASSIGNMENT #41 OFF
SET OUTPUT ASSIGNMENT #40 OFF
SET OUTPUT ASSIGNMENT #39 ON
```

NOTE: LOGIC STATEMENT FOR ADVANCE WALK WITH FYAs. TURN FYA HEADS 44 AND 45 OFF DURING PED 4 ADVANCE WALK.

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

OUTPUT 2 = 4 PED WALK
OUTPUT 5 = VEHICLE 4 GREEN
OUTPUT 39 = Overlap D Red
OUTPUT 40 = Overlap D Yellow
OUTPUT 41 = Overlap D Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```
PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:            ¦123456789101112131415 16
VEH OVL PARENTS:¦   X X  X
VEH OVL NOT VEH:¦
VEH OVL NOT PED:¦
VEH OVL GRN EXT:¦
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW    GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

PRESS '+' 3 TIMES

```
PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
PHASE:            ¦123456789101112131415 16
VEH OVL PARENTS:¦   XX   X
VEH OVL NOT VEH:¦
VEH OVL NOT PED:¦
VEH OVL GRN EXT:¦
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

← NOTICE GREEN FLASH

PRESS '+'

```
PAGE 1: VEHICLE OVERLAP 'E' SETTINGS
PHASE:            ¦123456789101112131415 16
VEH OVL PARENTS:¦   X    X
VEH OVL NOT VEH:¦
VEH OVL NOT PED:¦
VEH OVL GRN EXT:¦
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW    GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

PRESS '+'

```
PAGE 1: VEHICLE OVERLAP 'F' SETTINGS
PHASE:            ¦123456789101112131415 16
VEH OVL PARENTS:¦    X   X
VEH OVL NOT VEH:¦
VEH OVL NOT PED:¦
VEH OVL GRN EXT:¦
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW    GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

PHASE SEQUENCE PROGRAMMING DETAIL

(program controller as shown below)

FROM OASIS LOCAL CONTROLLER MAIN MENU
SELECT: 4 PHASE SEQUENCE

```
PHASE SEQUENCE: PAGE 1  NEXT: PAGES)
RNG¦LEAD   BARRIER 1  X-LAG¦LEAD   BARRIER 2  X-LAG¦LEAD   BARRIER 3  X-LAG¦LEAD   BARRIER 4  X-LAG
1  ¦0    2    0    0    ¦0    4    9    0    ¦0    1    0    0    ¦8    0    0    0
2  ¦0    5    0    0    ¦0    0    0    0    ¦0    6    0    0    ¦0    0    0    0
3  ¦0    0    0    0    ¦0    0    0    0    ¦0    0    0    0    ¦0    0    0    0
4  ¦0    0    0    0    ¦0    0    0    0    ¦0    0    0    0    ¦0    0    0    0
```

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 02-0256
DESIGNED: July 2022
SEALED: 7/26/2022
REVISED: N/A

This plan supersedes the plan signed and sealed on 02/26/2019.

ELECTRICAL DETAIL SHEET 2 OF 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:

NC 58 (Fort Macon Road)
at
SR 1182 (Atlantic Beach Cswy)

Division 2 | Carteret County | Atlantic Beach

PLAN DATE:	July 2022	REVIEWED BY:	J.L. Lewis
PREPARED BY:	M.L. Stygles	REVIEWED BY:	J. Ma

REVISIONS	INIT.	DATE

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