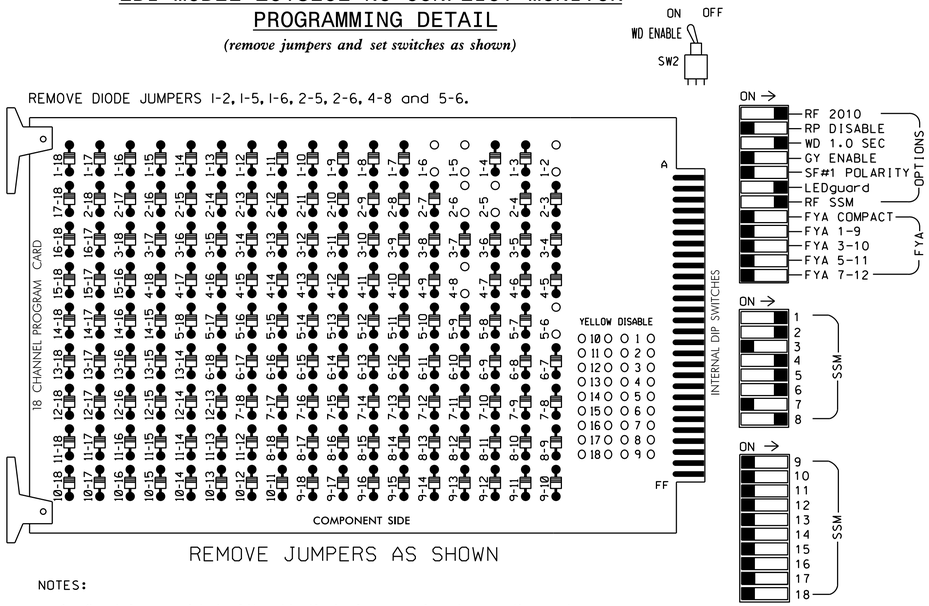
PROJECT REFERENCE NO.	SHEET NO.
U-5518EM	Sig. 1.1

EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all phases.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16
PHASE	OLA	2	2 PED	3	4	4 PED	OLC	6	6 PED	7	8	8 PED
SIGNAL HEAD NO.	63★	21,22	NU	NU	41,42	NU	23★	61,62	NU	NU	81,82	NU
RED		128			101			134			107	
YELLOW		129			102			135			108	
GREEN		130			103			136			109	
RED ARROW	125						131					
YELLOW ARROW	126						132					
FLASHING YELLOW ARROW	127						133					
GREEN ARROW												

NU = Not Used

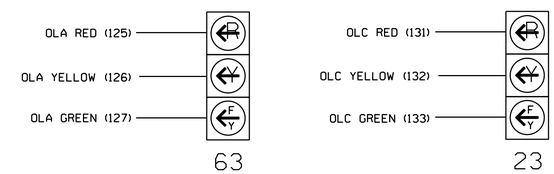
★ See pictorial of head wiring in detail below.

EQUIPMENT INFORMATION

CONTROLLER.....2070L
CABINET.....336
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....POLE
OUTPUT FILE POSITIONS...12
LOAD SWITCHES USED.....S1,S2,S5,S7,S8,S11
PHASES USED.....2,4,6,8
OVERLAP "A".....6
OVERLAP "B".....NOT USED
OVERLAP "C".....2
OVERLAP "D".....NOT USED

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	Ø 6 / 6C	Ø 2 / 2A	SLOT EMPTY	Ø 4 / 4A	SLOT EMPTY	Ø 6 / 6A	SLOT EMPTY	Ø 8 / 8A	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	FS / DC ISOLATOR
L	Ø 2 / 2C	Ø 2 / 2B		Ø 4 / 4B		Ø 6 / 6B		Ø 8 / 8B						ST / DC ISOLATOR

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB21-3,4	I2U	39	1	2	2	Y	Y		1.6	
2B	TB23-3,4	I2L	43	5	12	2	Y	Y			
2C	TB23-1,2	I1L	47	9	22	2	Y	Y			
4A	TB21-7,8	I4U	41	3	4	4	Y	Y			5
4B	TB23-7,8	I4L	45	7	14	4	Y	Y			10
6A	TB21-11,12	I6U	40	2	6	6	Y	Y		1.6	
6B	TB23-11,12	I6L	44	6	16	6	Y	Y			
6C	TB21-1,2	I1U	56	18	1	6	Y	Y			
8A	TB22-1,2	I8U	42	4	8	8	Y	Y			5
8B	TB24-1,2	I8L	46	8	18	8	Y	Y			10

INPUT FILE POSITION LEGEND: I2L
FILE I
SLOT 2
LOWER

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```
PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:            |123456789101112131415 16
VEH OVL PARENTS:  |     X
VEH OVL NOT VEH:  |
VEH OVL NOT PED:  |
VEH OVL GRN EXT:  |
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

← NOTICE GREEN FLASH

PRESS '+' TWICE

```
PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:            |123456789101112131415 16
VEH OVL PARENTS:  | X
VEH OVL NOT VEH:  |
VEH OVL NOT PED:  |
VEH OVL GRN EXT:  |
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 05-0740
DESIGNED: August 2014
SEALED: 8/28/2014
REVISED: N/A

THIS ELECTRICAL DETAIL SUPERSEDES THE DETAIL SEALED ON 04/30/14

Electrical Detail - Sheet 1 of 3

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:
Transportation Mobility and Safety Division
STATE OF NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
Signal Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

NC 97
at
SR 2329 (Marshburn Road/
Lizard Lick Road)

Division 5 | Wake County | Lizard Lick

PLAN DATE: August 2014 | REVIEWED BY: T. Joyce
PREPARED BY: C. Strickland | REVIEWED BY:

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 022013 ENGINEER GEORGE C. BROWN

DocuSigned by: George C. Brown 9/5/2014
F12601EC08E8434 DATE

SIG. INVENTORY NO. 05-0740

05-SEP-2014 07:28
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cestrickland