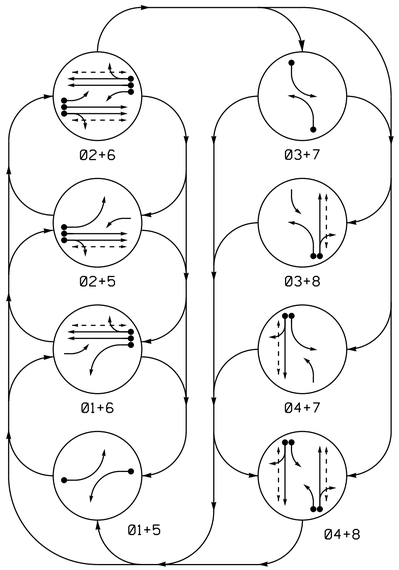
PROJECT REFERENCE NO.	SHEET NO.
B-4138A	Sig.1.0

PHASING DIAGRAM



PHASING DIAGRAM DETECTION LEGEND

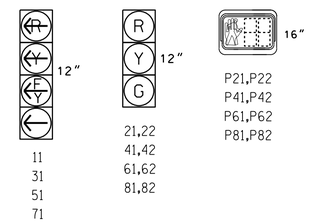
- DETECTED MOVEMENT
- UNDETECTED MOVEMENT (OVERLAP)
- UNSIGNALIZED MOVEMENT
- PEDESTRIAN MOVEMENT

TABLE OF OPERATION

SIGNAL FACE	PHASE 01+5	01+6	02+5	02+6	03+7	03+8	04+7	04+8	FLASH
11	←	←	F Y	F Y	~~R~~	~~R~~	~~R~~	~~R~~	~~Y~~
21,22	R	R	G	G	R	R	R	R	Y
31	~~R~~	~~R~~	~~R~~	~~R~~	←	←	F Y	F Y	~~R~~
41,42	R	R	R	R	R	R	G	G	R
51	←	F Y	←	F Y	~~R~~	~~R~~	~~R~~	~~R~~	~~Y~~
61,62	R	G	R	G	R	R	R	R	Y
71	~~R~~	~~R~~	~~R~~	~~R~~	←	F Y	←	F Y	~~R~~
81,82	R	R	R	R	R	G	R	G	R
P21,P22	DW	DW	W	W	DW	DW	DW	DW	DRK
P41,P42	DW	DW	DW	DW	DW	DW	W	W	DRK
P61,P62	DW	W	DW	W	DW	DW	DW	DW	DRK
P81,P82	DW	DW	DW	DW	DW	W	DW	W	DRK

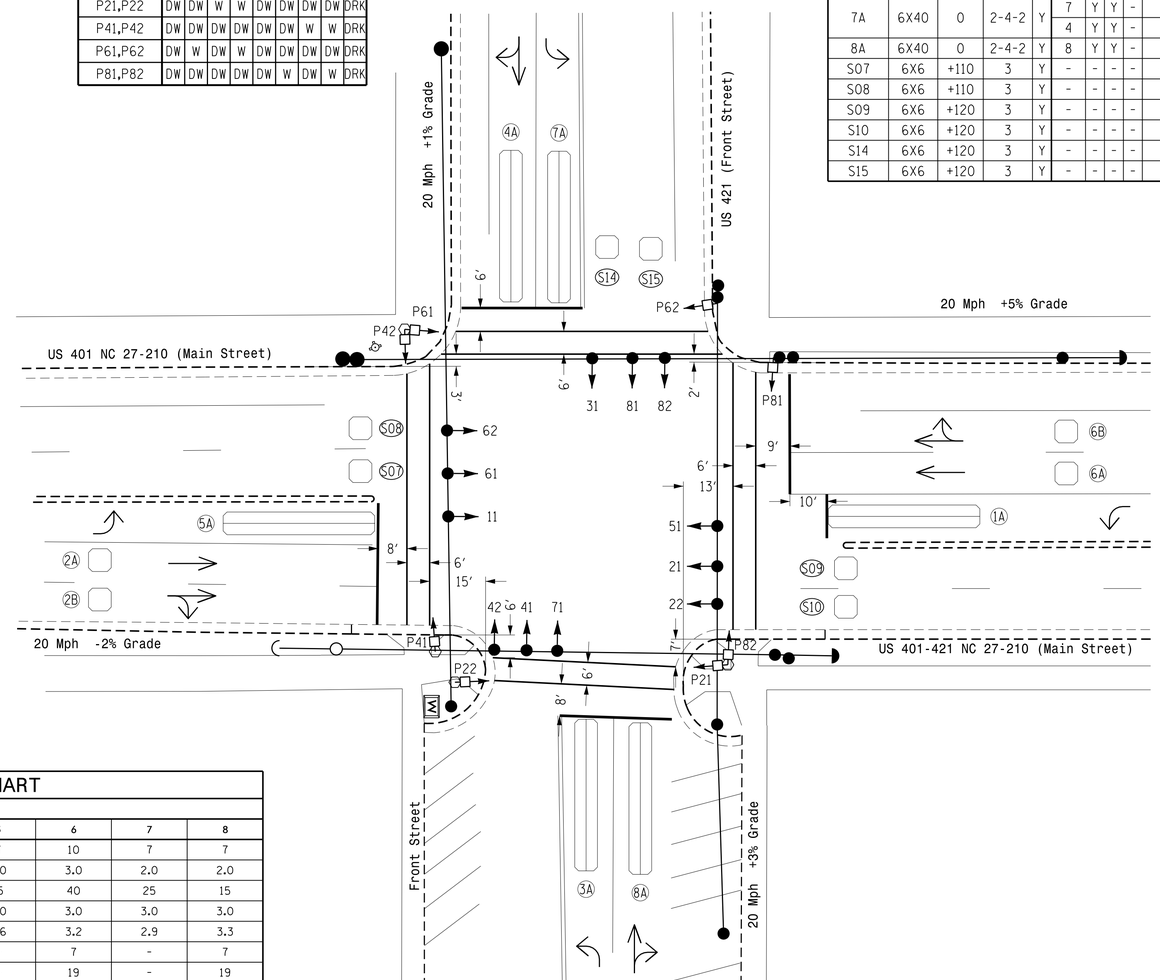
SIGNAL FACE I.D.

All Heads L.E.D.



OASIS 2070 LOOP & DETECTOR INSTALLATION CHART

INDUCTIVE LOOPS					DETECTOR PROGRAMMING							
LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PHASE	CALLING	EXTENSION	FULL TIME DELAY	STRETCH TIME	DELAY TIME	SYSTEM LOOP	NEW CARD
1A	6X40	0	2-4-2	Y	1	Y	Y	-	-	15	-	Y
					6	Y	Y	-	-	-	-	Y
2A,2B	6X6	70	3	Y	2	Y	Y	-	-	-	-	Y
3A	6X40	0	2-4-2	Y	3	Y	Y	-	-	15	-	Y
					8	Y	Y	-	-	3	-	Y
4A	6X40	0	2-4-2	Y	4	Y	Y	-	-	10	-	Y
5A	6X40	0	2-4-2	Y	5	Y	Y	-	-	15	-	Y
					2	Y	Y	-	-	-	-	Y
6A,6B	6X6	70	3	Y	6	Y	Y	-	-	-	-	Y
7A	6X40	0	2-4-2	Y	7	Y	Y	-	-	15	-	Y
					4	Y	Y	-	-	3	-	Y
8A	6X40	0	2-4-2	Y	8	Y	Y	-	-	5	-	Y
S07	6X6	+110	3	Y	-	-	-	-	-	-	Y	Y
S08	6X6	+110	3	Y	-	-	-	-	-	-	Y	Y
S09	6X6	+120	3	Y	-	-	-	-	-	-	Y	Y
S10	6X6	+120	3	Y	-	-	-	-	-	-	Y	Y
S14	6X6	+120	3	Y	-	-	-	-	-	-	Y	Y
S15	6X6	+120	3	Y	-	-	-	-	-	-	Y	Y



OASIS 2070 TIMING CHART

FEATURE	PHASE 1	2	3	4	5	6	7	8
Min Green 1 *	7	10	7	7	7	10	7	7
Extension 1 *	2.0	3.0	2.0	2.0	2.0	3.0	2.0	2.0
Max Green 1 *	15	40	25	15	15	40	25	15
Yellow Clearance	3.0	3.0	3.0	3.0	3.0	3.0	3.0	3.0
Red Clearance	2.9	3.2	2.8	3.3	2.6	3.2	2.9	3.3
Walk 1 *	-	7	-	7	-	7	-	7
Don't Walk 1	-	12	-	19	-	19	-	19
Seconds Per Actuation *	-	-	-	-	-	-	-	-
Max Variable Initial *	-	-	-	-	-	-	-	-
Time Before Reduction *	-	-	-	-	-	-	-	-
Time To Reduce *	-	-	-	-	-	-	-	-
Minimum Gap	-	-	-	-	-	-	-	-
Recall Mode	-	MIN RECALL	-	-	-	MIN RECALL	-	-
Vehicle Call Memory	-	YELLOW	-	-	-	YELLOW	-	-
Dual Entry	-	-	-	ON	-	-	-	ON
Simultaneous Gap	ON	ON	ON	ON	ON	ON	ON	ON

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

8 Phase
Fully Actuated
US 401-421/NC 27-210 CLS

NOTES

1. Refer to "Roadway Standard Drawings NCDOT" dated January 2012 and "Standard Specifications for Roads and Structures" dated January 2012.
2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
3. Phase 1 and/or phase 5 may be lagged.
4. Phase 3 and/or phase 7 may be lagged.
5. Set all detector units to presence mode.
6. Locate new cabinet so as not to obstruct sight distance of vehicles turning right on red.
7. Omit "WALK" and flashing "DON'T WALK" with no pedestrian calls.
8. Program pedestrian heads to countdown the flashing "Don't Walk" time only.
9. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
10. Closed loop system data: Master Asset #10613. Controller Asset #0125.

LEGEND

PROPOSED		EXISTING
	Traffic Signal Head	
	Modified Signal Head	N/A
	Sign	
	Pedestrian Signal Head With Push Button & Sign	
	Signal Pole with Guy	
	Signal Pole with Sidewalk Guy	
	Inductive Loop Detector	
	Controller & Cabinet	
	Master Controller & Cabinet	
	Junction Box	
	2-in Underground Conduit	
N/A	Right of Way	
	Directional Arrow	
	Type II Signal Pedestal	

Signal Upgrade

Prepared in the Offices of:
Transportation Mobility and Safety Division
Signal Design Section
750 N.Greenfield Pkwy,Garner,NC 27529

SCALE 0 20 1"=20'

US 401 NC 27-210 (Main Street) At US 421 (Front Street)

Division 6 Harnett County Lillington

PLAN DATE:	October 2014	REVIEWED BY:	PLA
PREPARED BY:	JPG	REVIEWED BY:	

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 029904 ENGINEER JASON P. GALLOWAY

DocuSigned by: Jason Galloway F700EA704818441D... 10/17/2014 DATE

SIG. INVENTORY NO. 06-0125

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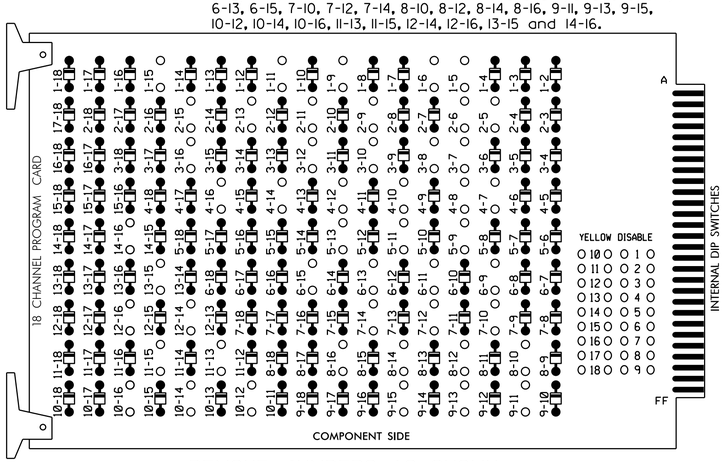
jgalloway

PROJECT REFERENCE NO.	SHEET NO.
B-4138A	Sig. 1.1

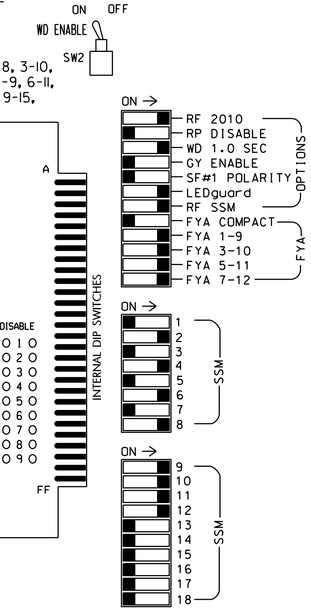
EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS I-5, I-6, I-9, I-11, I-15, 2-5, 2-6, 2-9, 2-11, 2-13, 2-15, 3-7, 3-8, 3-10, 3-12, 3-16, 4-7, 4-8, 4-10, 4-12, 4-14, 4-16, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 6-15, 7-10, 7-12, 7-14, 8-10, 8-12, 8-14, 8-16, 9-11, 9-13, 9-15, 10-12, 10-14, 10-16, 11-13, 11-15, 12-14, 12-16, 13-15 and 14-16.



REMOVE JUMPERS AS SHOWN



■ = DENOTES POSITION OF SWITCH

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all phases.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 2, 4, 6 and 8 for 'STARTUP PED CALL'.
6. Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.
7. The cabinet and controller are part of the US 401-421/ NC 27-210 CLS.

EQUIPMENT INFORMATION

CONTROLLER.....2070L
CABINET.....332 /W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8,S9,S10,S11
S12,AUX S1,AUX S2,AUX S4,AUX S5
PHASES USED.....1,2,2 PED,3,4,4 PED
5,6,6 PED,7,8,8 PED
OVERLAP "A".....1+2
OVERLAP "B".....3+4
OVERLAP "C".....5+6
OVERLAP "D".....7+8

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11★	21,22	P21, P22	31★	41,42	P41, P42	51★	61,62	P61, P62	71★	81,82	P81, P82	11★	31★	NU	51★	71★	NU
RED		128			101			134			107							
YELLOW	✳	129		✳	102		✳	135		✳	108							
GREEN		130			103			136			109							
RED ARROW													A121	A124		A114	A101	
YELLOW ARROW													A122	A125		A115	A102	
FLASHING YELLOW ARROW													A123	A126		A116	A103	
GREEN ARROW	127			118			133			124								
✋			113			104			119			110						
🚶			115			106			121			112						

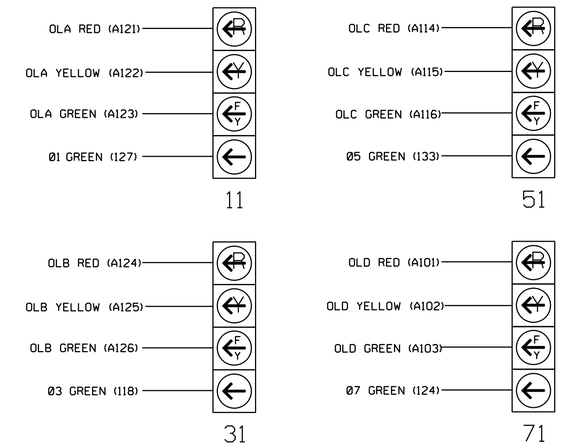
NU = Not Used

✳ Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

1. The sequence display for these signals require special logic programming. See sheet 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	Ø1 1A	Ø2 2A,2B	SLOT EMPTY	WIRED INPUT ⊗	Ø3 3A	Ø4 4A	SYS. DET. S14	WIRED INPUT ⊗	SYS. DET. S07	SLOT EMPTY	SLOT EMPTY	Ø2 PED DC ISOLATOR	Ø6 PED DC ISOLATOR	FS DC ISOLATOR
	L	NOT USED	NOT USED			NOT USED	NOT USED	SYS. DET. S15		SYS. DET. S08			Ø4 PED DC ISOLATOR	Ø8 PED DC ISOLATOR	ST DC ISOLATOR
FILE "J"	U	Ø5 5A	Ø6 6A,6B	SLOT EMPTY	WIRED INPUT ⊗	Ø7 7A	Ø8 8A	SLOT EMPTY	WIRED INPUT ⊗	SYS. DET. S09	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY	SLOT EMPTY
	L	NOT USED	NOT USED			NOT USED	NOT USED			SYS. DET. S10					

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A[1]	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y			
2A,2B	TB2-5,6	I2U	39	1	2	2	Y	Y			
3A[2]	TB4-5,6	I5U	58	20	3	3	Y	Y			15
	-	J8U	50	12	28	8	Y	Y			3
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			10
5A[3]	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y			
6A,6B	TB3-5,6	J2U	40	2	6	6	Y	Y			
7A[4]	TB5-5,6	J5U	57	19	7	7	Y	Y			15
	-	I8U	49	11	24	4	Y	Y			3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			5
✳S07	TB6-9,10	I9U	60	22	11	SYS					
✳S08	TB6-11,12	I9L	62	24	13	SYS					
✳S09	TB7-9,10	J9U	59	21	15	SYS					
✳S10	TB7-11,12	J9L	61	23	17	SYS					
✳S14	TB6-1,2	I7U	65	27	34	SYS					
✳S15	TB6-3,4	I7L	78	40	44	SYS					
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					
P81,P82	TB8-8,9	I13L	70	32	PED 8	8 PED					

NOTE:
INSTALL DC ISOLATORS IN INPUT FILE SLOTS I12 AND I13.

[1] Add jumper from I1-W to J4-W, on rear of input file.
[2] Add jumper from I5-W to J8-W, on rear of input file.
[3] Add jumper from J1-W to I4-W, on rear of input file.
[4] Add jumper from J5-W to I8-W, on rear of input file.

✳ System detector only. Remove the vehicle phase assigned to this detector in the default programming.

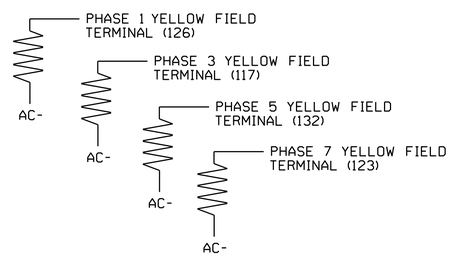
INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0125
DESIGNED: October 2014
SEALED: 10/17/14
REVISED: N/A

Electrical Detail - Sheet 1 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:
Transportation Mobility and Safety Division
Traffic Engineering Branch
Signal Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

US 401 NC 27-210 (Main Street) at US421 (Front Street)

Division 6 Harnett County Lillington

PLAN DATE:	October 2014	REVIEWED BY:	T. Joyce
PREPARED BY:	B. Simmons	REVIEWED BY:	

REVISIONS	INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 022013 ENGINEER GEORGE C. BROWN

DocuSigned by: George C. Brown 10/17/2014
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DATE

SIG. INVENTORY NO. 06-0125

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PROJECT REFERENCE NO.	SHEET NO.
B-4138A	Sig. 1.2

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. From Main Menu press '2' (PHASE CONTROL), then '1' (PHASE CONTROL FUNCTIONS). Scroll to the bottom of the menu and Enable ACT Logic Commands 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11 and 12.

2. From Main Menu press '6' (OUTPUTS), then '3' (LOGICAL I/O PROCESSOR).

```
LOGICAL I/O COMMAND #1  (+/-COMMAND#)
IF  ACTIVE PHASE        #1  IS ON
AND RED CLEAR ON PHASE #1  IS ON
SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #50 ON
SET OUTPUT  ASSIGNMENT #51 OFF
```

NOTE: Logic for Phase 1 RED Clear when transitioning from Phase 1 to Phase 2 (Head 11).

PRESS '+'

```
LOGICAL I/O COMMAND #2  (+/-COMMAND#)
IF  ACTIVE PHASE        #1  IS ON
SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #52 OFF
```

NOTE: Logic for Switching Flashing Yellow Arrow "OFF" during Phase 1 (Head 11).

PRESS '+'

```
LOGICAL I/O COMMAND #3  (+/-COMMAND#)
IF  YELLOW ON PHASE     #1  IS ON
SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #51 ON
```

NOTE: Logic for Yellow Arrow Clearance from Phase 1 (Head 11).

PRESS '+'

```
LOGICAL I/O COMMAND #4  (+/-COMMAND#)
IF  ACTIVE PHASE        #5  IS ON
AND RED CLEAR ON PHASE #5  IS ON
SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #42 ON
SET OUTPUT  ASSIGNMENT #43 OFF
```

NOTE: Logic for Phase 5 RED Clear when transitioning from Phase 5 to Phase 6 (Head 51).

PRESS '+'

```
LOGICAL I/O COMMAND #5  (+/-COMMAND#)
IF  ACTIVE PHASE        #5  IS ON
SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #44 OFF
```

NOTE: Logic for Switching Flashing Yellow Arrow "OFF" during Phase 5 (Head 51).

PRESS '+'

```
LOGICAL I/O COMMAND #6  (+/-COMMAND#)
IF  YELLOW ON PHASE     #5  IS ON
SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #43 ON
```

NOTE: Logic for Yellow Arrow Clearance from Phase 5 (Head 51).

PRESS '+'

```
LOGICAL I/O COMMAND #7  (+/-COMMAND#)
IF  ACTIVE PHASE        #3  IS ON
AND RED CLEAR ON PHASE #3  IS ON
SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #47 ON
SET OUTPUT  ASSIGNMENT #48 OFF
```

NOTE: Logic for Phase 3 RED Clear when transitioning from Phase 3 to Phase 4 (Head 31).

PRESS '+'

```
LOGICAL I/O COMMAND #8  (+/-COMMAND#)
IF  ACTIVE PHASE        #3  IS ON
SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #49 OFF
```

NOTE: Logic for Switching Flashing Yellow Arrow "OFF" during Phase 3 (Head 31).

PRESS '+'

```
LOGICAL I/O COMMAND #9  (+/-COMMAND#)
IF  YELLOW ON PHASE     #3  IS ON
SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #48 ON
```

NOTE: Logic for Yellow Arrow Clearance from Phase 3 (Head 31).

PRESS '+'

```
LOGICAL I/O COMMAND #10 (+/-COMMAND#)
IF  ACTIVE PHASE        #7  IS ON
AND RED CLEAR ON PHASE #7  IS ON
SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #39 ON
SET OUTPUT  ASSIGNMENT #40 OFF
```

NOTE: Logic for Phase 7 RED Clear when transitioning from Phase 7 to Phase 8 (Head 71).

PRESS '+'

```
LOGICAL I/O COMMAND #11 (+/-COMMAND#)
IF  ACTIVE PHASE        #7  IS ON
SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #41 OFF
```

NOTE: Logic for Switching Flashing Yellow Arrow "OFF" during Phase 7 (Head 71).

PRESS '+'

```
LOGICAL I/O COMMAND #12 (+/-COMMAND#)
IF  YELLOW ON PHASE     #7  IS ON
SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #40 ON
```

NOTE: Logic for Yellow Arrow Clearance from Phase 7 (Head 71).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```
PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:          |123456789101112131415 16
VEH OVL PARENTS:|XX
VEH OVL NOT VEH:|
VEH OVL NOT PED:|
VEH OVL GRN EXT:|
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

NOTICE GREEN FLASH

PRESS '+'

```
PAGE 1: VEHICLE OVERLAP 'B' SETTINGS
PHASE:          |123456789101112131415 16
VEH OVL PARENTS:|  XX
VEH OVL NOT VEH:|
VEH OVL NOT PED:|
VEH OVL GRN EXT:|
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

NOTICE GREEN FLASH

PRESS '+'

```
PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:          |123456789101112131415 16
VEH OVL PARENTS:|     XX
VEH OVL NOT VEH:|
VEH OVL NOT PED:|
VEH OVL GRN EXT:|
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

NOTICE GREEN FLASH

PRESS '+'

```
PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
PHASE:          |123456789101112131415 16
VEH OVL PARENTS:|       XX
VEH OVL NOT VEH:|
VEH OVL NOT PED:|
VEH OVL GRN EXT:|
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

In order to insure that signals flash concurrently on the same approach, make the following flasher circuit changes:

1. On rear of PDA - Remove wire from Term. T2-4 and Terminate on T2-2.
2. On rear of PDA - Remove wire from Term. T2-5 and Terminate on T2-3.
3. Remove Flasher Unit 2.

The changes listed above ties all Phases and Overlaps to Flasher Unit 1.

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

OUTPUT REFERENCE SCHEDULE

USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 39 = Overlap D Red
OUTPUT 40 = Overlap D Yellow
OUTPUT 41 = Overlap D Green
OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green
OUTPUT 47 = Overlap B Red
OUTPUT 48 = Overlap B Yellow
OUTPUT 49 = Overlap B Green
OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow
OUTPUT 52 = Overlap A Green

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0125
DESIGNED: October 2014
SEALED: 10/17/14
REVISED: N/A

Electrical Detail - Sheet 2 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR:

US 401 NC 27-210 (Main Street)
at
US421 (Front Street)

Division 6 | Harnett County | Lillington

PLAN DATE:	October 2014	REVIEWED BY:	T. Joyce
PREPARED BY:	B. Simmons	REVIEWED BY:	

REVISIONS	INIT.	DATE

Prepared in the Offices of:
Transportation Mobility and Safety Division
Traffic Engineering Branch
Signals Management Section
750 N.Greenfield Pkwy,Garner,NC 27529

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 022013 ENGINEER GEORGE C. BROWN

DocuSigned by: George C. Brown 10/17/2014
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DATE

SIG. INVENTORY NO. 06-0125

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