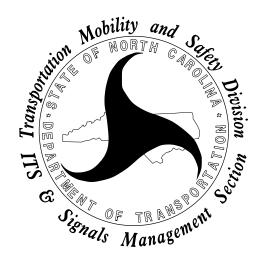
# Design Manual

# Signals Management



Part

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### Signal Plan I D Box

THIS ELECTRICAL DETAIL IS FOR

THE SIGNAL DESIGN: 11-1001

DESIGNED: June 2017 SEALED: 7/15/2017

REVISED: N/A

Every electrical detail must have a Signal Plan I.D. Box. The purpose of this box is to positively identify the signal plan that the electrical detail is designed to implement. The box has four data fields:

Signal Inventory Number - An inventory number is assigned to each signalized intersection. That number is found in the bottom right corner of the signal plan and should be entered in the first data field.

Design Date - This date is found on the signal plan in the area labeled 'Plan Date'. It should be duplicated in the second data field.

Seal Date - The third data field should contain the date that the signal plan was sealed.

Revision Date - If a signal plan has been revised, the date of the revision is shown in the bottom data field and a revision reference number is shown. If the signal plan has no revisions, the data field should be designaed as 'N/A'. If a signal plan has been revised more than once, the I.D. box should be expanded to show all revision dates and the revision text should be edited to read 'REVISED1', 'REVISED2', as required.



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 11-1001 DESIGNED: June 2017 SEALED: 7/15/2017 REVISED: 6/15/2018

# Signal Plan I.D. Box

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### **EQUIPMENT INFORMATION**

### **EQUIPMENT INFORMATION**

### **Equipment Information**

Controller - Gives the controller model.

Cabinet - Gives the cabinet model (332 for a base mount cabinet, or 336 for a pole mount cabinet).

Software - Gives the local software package to be used at a particular location. If the signal design includes railroad preemption, the specific version of the software will be listed.

Cabinet Mount - Specifies whether the traffic signal cabinet is a base mount or pole mount design.

Output File Positions - Lists the number of load switch sockets available in the output file. Also specifies, if applicable, the presence of an auxiliary output file.

Load Switches Used - Indicates which load switches are to be used on the design.

Phases Used - Lists the phases used by the controller, including any phases used for timing only that have no field display.

Overlaps - Lists the parent phases for any overlaps being used.

**Equipment Information** 

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|                       | S   | ΙGΝ | <b>IAL</b>           | HE          | AD | HC    | 0K       | - UF | C                    | HAF         | RΤ  |       |          |   |
|-----------------------|-----|-----|----------------------|-------------|----|-------|----------|------|----------------------|-------------|-----|-------|----------|---|
| LOAD<br>SWITCH NO.    | S   | 51  | S2                   | S3          | S4 | S5    | S6       | S7   | S8                   | S9          | S1Ø | S11   | S12      | A |
| CMU<br>CHANNEL<br>NO. |     | 1   | 2                    | 13          | 3  | 4     | 14       | 5    | 6                    | 15          | 7   | 8     | 16       | • |
| PHASE                 | :   | 1   | 2                    | 2<br>PED    | 3  | 4     | 4<br>PED | 5    | 6                    | 6<br>PED    | 7   | 8     | 8<br>PED | 0 |
| SIGNAL<br>HEAD NO.    | 11  | 82  | 21 <b>,</b> 22<br>23 | P21,<br>P22 | NU | 41,42 | NU       | 51   | 61 <b>,</b> 62<br>63 | P61,<br>P62 | NU  | 81,82 | NU       | 0 |
| RED                   |     |     | 128                  |             |    | 1Ø1   |          |      | 134                  |             |     | 107   |          |   |
| YELLOW                |     |     | 129                  |             |    | 102   |          |      | 135                  |             |     | 108   |          | } |
| GREEN                 |     |     | 130                  |             |    | 103   |          |      | 136                  |             |     | 109   |          |   |
| RED<br>ARROW          | 125 |     |                      |             |    |       |          | 131  |                      |             |     |       |          |   |
| YELLOW<br>ARROW       | 126 | 126 |                      |             |    |       |          | 132  |                      |             |     |       |          | } |
| GREEN<br>ARROW        | 127 | 127 |                      |             |    |       |          | 133  |                      |             |     |       |          |   |
| ₩                     |     |     |                      | 113         |    |       |          |      |                      | 119         |     |       |          |   |
| ×                     |     |     |                      | 115         |    |       |          |      |                      | 121         |     |       |          |   |

(H) Extra column - if more than one type of signal head is attached to the same load switch, a second column is added to the chart as shown above. In this example, both a 3-section all left arrow head and the arrow portion of a 5-section head are to run on phase 1.

### 2070 Signal Head Hook-Up Chart

The chart shown at left appears on all 2070 electrical details. Its purpose is to provide a user-friendly reference on connecting the signal heads to the cabinet field terminals.

### Features:

- Load Switch No. Displays the load switch designation.
- © Phase Lists the function of the load switch. The load switch function can be reassigned in the controller programming. The default settings are shown at left.
- ① Signal Head No. Lists the signal heads that should have connections made to the field terminals for this load switch. Note that a 4- or 5- section head may appear in two different columns because the red, yellow, and green balls are controlled by one load switch while the arrow indications are controlled by another.
- © Red, Yellow, Green Lists the field terminal number to which the red, yellow, and green ball indications for the signal heads listed in the row above should be tied.
- F Red, Yellow, and Green arrows Red, yellow, and green arrow indications for the signal heads should be tied to the field terminals that appear in these rows.
- © Pedestrian Signal Indications The 'Hand' and the 'Man' indications of the pedestrian signal heads should be connected to the field terminals indicated. If no pedestrian signals are used, these two rows may be removed from the drawing.

(continued on next page)

# 2070 Signal Head Hook-Up Chart

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|                       | SIGNAL HEAD HOOK-UP CHART |       |          |    |       |          |     |       |          |     |       |          |           |           |           |           |           |           |
|-----------------------|---------------------------|-------|----------|----|-------|----------|-----|-------|----------|-----|-------|----------|-----------|-----------|-----------|-----------|-----------|-----------|
| LOAD<br>SWITCH NO.    | S1                        | S2    | S3       | S4 | S5    | S6       | S7  | S8    | S9       | S10 | S11   | S12      | AUX<br>S1 | AUX<br>S2 | AUX<br>S3 | AUX<br>S4 | AUX<br>S5 | AUX<br>S6 |
| CMU<br>CHANNEL<br>NO. | 1                         | 2     | 13       | 3  | 4     | 14       | 5   | 6     | 15       | 7   | 8     | 16       | 9         | 10        | 17        | 11        | 12        | 18        |
| PHASE                 | 1                         | 2     | 2<br>PED | 3  | 4     | 4<br>PED | 5   | 6     | 6<br>PED | 7   | 8     | 8<br>PED | OLA       | OLB       | SPARE     | OLC       | OLD       | SPARE     |
| SIGNAL<br>HEAD NO.    | 61                        | 21,22 | NU       | NU | 41,42 | NU       | 21  | 61,62 | NU       | 41  | 81,82 | NU       | 23,24     | 63,64     | NU        | 43,44     | NU        | NU        |
| RED                   | *                         | 128   |          |    | 101   |          | *   | 134   |          |     | 107   |          | A121      | A124      |           | A114      |           |           |
| YELLOW                |                           | 129   |          |    | 102   |          |     | 135   |          | *   | 108   |          | A122      | A125      |           | A115      |           |           |
| GREEN                 |                           | 130   |          |    | 103   |          |     | 136   |          |     | 109   |          | A123      | A126      |           | A116      |           |           |
| RED<br>ARROW          |                           |       |          |    |       |          |     |       |          |     |       |          |           |           |           |           |           |           |
| YELLOW<br>ARROW       | 126                       |       |          |    |       |          | 132 |       |          |     |       |          |           |           |           |           |           |           |
| GREEN<br>ARROW        | 127                       |       |          |    |       |          | 133 |       |          | 124 |       |          |           |           |           |           |           |           |

NU = NOT USED

\* Denotes install load resistor. See load resistor installation detail this page.

### Features (cont.):

- (1) Load Resistor note If there is not a field indication for each of the three outputs on a given load switch, a note referring to the load resistor installation detail should appear below the field hook-up chart. An asterisk is to be placed in the chart to show where a load resistor needs to be installed. If only the green and vellow indications of the load switch are used (common with 5-section heads on protected/permissive left turns), an asterisk referring to the note should be placed in the 'red' row. If only the green arrow indication is used, the asterisk should appear in the 'vellow' row. This scenario can occur when a 4-section head is used to display a left turn that is only used during a preemption. See STD. NO. 4.0 for more information.
- Auxiliary Output file If overlaps are used, an auxiliary output file is installed providing additional load switch capacity for up to six overlaps. The default load switch to function relationships for the auxiliary output file are as follows:

| AUX S | 1 ——— | OVERLAP A        |     |
|-------|-------|------------------|-----|
| AUX S | 2 ——— | OVERLAP B        |     |
| AUX S | 3 ——— | SPARE (OVERLAP E | Ξ,  |
| AUX S | 4 ——— | OVERLAP C        |     |
| AUX S | 5 ——— | OVERLAP D        |     |
| AUX S | 6 ——— | SPARE (OVERLAP F | = } |

# 2070 Signal Head Hook-Up Chart

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### SIGNAL HEAD HOOK-UP CHART FOR 4-SECTION FYA PPLT SIGNAL HEADS USED IN A 332 BASE MOUNTED CABINET

| SIGNAL HEAD HOOK-UP CHART   |               |     |       |          |                |       |          |                |       |          |             |       |          |           |                |           |                |             |           |
|-----------------------------|---------------|-----|-------|----------|----------------|-------|----------|----------------|-------|----------|-------------|-------|----------|-----------|----------------|-----------|----------------|-------------|-----------|
| LOAD<br>SWITCH NO.          | 5             | 51  | S2    | S3       | S4             | S5    | S6       | S7             | S8    | S9       | S1Ø         | S11   | S12      | AUX<br>S1 | AUX<br>S2      | AUX<br>S3 | AUX<br>S4      | AUX<br>S5   | AUX<br>S6 |
| CMU<br>CHANNEL<br>NO.       | :             | 1   | 2     | 13       | 3              | 4     | 14       | 5              | 6     | 15       | 7           | 8     | 16       | 9         | 10             | 17        | 11             | 12          | 18        |
| PHASE                       | :             | 1   | 2     | 2<br>PED | 3              | 4     | 4<br>PED | 5              | 6     | 6<br>PED | 7           | 8     | 8<br>PED | OLA       | OLB            | SPARE     | OLC            | OLD         | SPARE     |
| SIGNAL<br>HEAD NO.          | 11            | 82  | 21,22 | NU       | <b>★</b><br>31 | 41,42 | NU       | <b>★</b><br>51 | 61,62 | NU       | 71 <b>★</b> | 81,82 | NU       | 11        | <b>★</b><br>31 | NU        | <b>★</b><br>51 | 71 <b>★</b> | NU        |
| RED                         |               | *   | 128   |          |                | 101   |          |                | 134   |          |             | 107   |          |           |                |           |                |             |           |
| YELLOW                      |               |     | 129   |          | *              | 102   |          | *              | 135   |          | *           | 108   |          |           |                |           |                |             |           |
| GREEN                       |               |     | 130   |          |                | 103   |          |                | 136   |          |             | 109   |          |           |                |           |                |             |           |
| RED<br>ARROW                |               |     |       |          |                |       |          |                |       |          |             |       |          | A121      | A124           |           | A114           | A1Ø1        |           |
| YELLOW<br>ARROW             |               | 126 |       |          |                |       |          |                |       |          |             |       |          | A122      | A125           |           | A115           | A1Ø2        |           |
| FLASHING<br>YELLOW<br>ARROW |               |     |       |          |                |       |          |                |       |          |             |       |          | A123      | A126           |           | A116           | A1Ø3        |           |
| GREEN<br>ARROW              | 127           | 127 |       |          | 118            |       |          | 133            |       |          | 124         | \     | \        |           |                |           |                |             |           |
| · -                         | NU = NOT USED |     |       |          |                |       |          |                |       |          |             |       |          |           | <u> </u>       |           |                |             |           |

\* Denotes install load resistor. See load resistor installation detail this page.

★ See pictorial of head wiring in detail below.

### 2070 Signal Head Hook-Up Chart

The chart shown at left appears on all 2070 electrical details. Its purpose is to provide a user friendly reference on connecting the signal heads to the cabinet field terminals.

### Features:

(A) Auxiliary Output file - The cabinet must be wired such that for each Flashing Yellow Arrow (FYA) approach, the solid green protected arrow is driven by a load switch monitored on channels 1. 3. 5. and 7. The associated solid red arrow. solid yellow arrow, and flashing yellow arrow (overlap phase) must be driven by a load switch monitored on channels 9, 10, 11, and 12 respectively. The signal monitor makes the following associations when FYA monitoring is enabled for each approach:

> Channel 1 with 9 Channel 3 with 10 Channel 5 with 11 Channel 7 with 12

Overlaps are used to drive the solid red arrow, solid yellow arrow, and flashing vellow arrow. The display sequence is further controlled by logic statements programmed in the controller.

- (B) Any load switch that only drives the solid green arrow on a 4-section FYA head will have a load resistor installed on its associated yellow field terminal on the output file. Additionally, the SSM switch for that channel will remain in the OFF position on the conflict monitor.
- (c) In addition to the hook-up information shown in this chart, every electrical plan utilizing FYA heads will have a FYA signal wiring detail showing a pictorial relationship of the signal head to output file wiring.

(continued on next page)

# 2070 Signal Head Hook-Up Chart For FYA

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### SIGNAL HEAD HOOK-UP CHART FOR 4-SECTION FYA PPLT SIGNAL HEADS USED IN A CABINET OPERATING IN COMPACT MODE

|                             |     | S     | IGN   | IAL         | HE | AD    | HC          | )OK      | -UF   | CI             | HAF      | RΤ  |       |           |          |
|-----------------------------|-----|-------|-------|-------------|----|-------|-------------|----------|-------|----------------|----------|-----|-------|-----------|----------|
| LOAD<br>SWITCH NO.          | S1  | S2    | S     | 3           | S4 | S5    | S6          | S7       | S8    | S              | :9       | SIØ | S11   | S         | 12       |
| CMU<br>CHANNEL<br>NO.       | 1   | 2     | 9     | 13          | 3  | 4     | 14          | 5        | 6     | 11             | 15       | 7   | 8     | 12 ′      | 16       |
| PHASE                       | OLA | 2     | 1 GRN | 2<br>PED    | 3  | 4     | 4<br>PED    | OLC      |       | 5 GRN          | 6<br>PED | OLD | 8     | 7 GRN     | 8<br>PED |
| SIGNAL<br>HEAD NO.          | 11  | 21,22 | 11    | P21.<br>P22 | NU | 41,42 | P41,<br>P42 | <b>★</b> | 61,62 | <b>★</b><br>51 | NU       | 71★ | 81,82 | <b>71</b> | NU       |
| RED                         |     | 128   |       |             |    | 1Ø1   |             |          | 134   |                |          |     | 107   |           |          |
| YELLOW                      |     | 129   |       |             |    | 102   |             |          | 135   |                |          |     | 108   |           |          |
| GREEN                       |     | 130   |       |             |    | 103   |             |          | 136   |                |          |     | 109   |           |          |
| RED<br>ARROW                | 125 |       |       |             |    |       |             | 131      |       |                |          | 122 |       |           |          |
| YELLOW<br>ARROW             | 126 |       |       |             |    |       |             | 132      |       |                |          | 123 |       |           |          |
| FLASHING<br>YELLOW<br>ARROW | 127 |       |       |             |    |       |             | 133      |       |                |          | 124 |       |           |          |
| ₩                           |     |       |       | 113         |    |       | 104         |          |       |                |          |     |       |           |          |
| PED<br>YELLOW               |     |       |       |             |    |       | *           |          |       |                |          |     |       |           |          |
| GREEN<br>ARROW              |     |       | 114   |             |    |       |             |          |       | 120            |          |     |       | 111       |          |
| Ķ                           |     |       |       | 115         |    |       | 106         |          |       |                | *        |     |       |           | *        |

- \* Denotes install load resistor. See load resistor installation detail this sheet.
- $\bigstar$  See pictorial of head wiring in detail below.

NOTE: Load switches S1, S3, S7, S9, S10, and S12 require output remapping. See sheets x through y for details.

Features (cont.):

① Load switch outputs that drive the solid red arrow, solid yellow arrow, and flashing yellow arrow will have to be remapped to function as vehicle overlaps.

Unused ped yellow load switch outputs will have to be remapped to drive the left turn green arrows.

FYA operation when using a cabinet in compact mode. The FYA compact mode switch on the conflict monitor must be set to the ON position. Further details are found in STD. NO. 7.0. The cabinet must be wired such that the (unused) ped yellow load switch outputs are wired to the conflict monitor as follows:

2-PY to Channel 9 Green (CMU pin 13, logical Channel 9) 4-PY to Channel 9 Yellow (CMU pin 16, logical Channel 10) 6-PY to Channel 10 Green (CMU pin R, logical Channel 11) 8-PY to Channel 10 Yellow (CMU pin U, logical Channel 12)

For all cabinets, this is accomplished through a keyed plug connection found on the inside panel of the output file. Plug together the two connectors labeled as shown below:

© Connecting the keyed ped yellow connector in 'E' above connects all ped yellow load switch outputs to the conflict monitor. A ped load switch installed for FYA use that has no ped movement must have a load resistor installed on its Walk output to prevent a conflict. A ped load switch used solely for ped movements must have a load resistor installed on its Ped Yellow output to prevent a conflict.

# 2070 Signal Head Hook-Up Chart For FYA

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TRANSPORTATION MOBILITY AND SAFETY DIVISION
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

STD. NO.

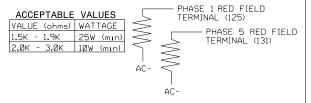
3.1

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### Load Resistor Installation Detail

# LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

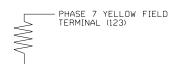


NOTE: The purpose of these resistors is to load the channel red monitor inputs in order for the Signal Sequence Monitor to use the full signal sequence monitoring capability on channels that do not use the red display in the field.

# LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)





In all traffic signal installations, the signal head displays are switched 'ON' and 'OFF' by solid state load switches. These load switches take a logic level input from the controller and switch AC power to the signal heads through a triac device. The triac is protected from transient voltages by a snubber circuit. In the 'OFF' condition there is a small leakage current through the snubber circuit. As long as there is a load across the circuit, such as a bulb or LED module, this leakage current goes unnoticed. If there is no load, however, the conflict monitor will see an 'OFF' condition as an active signal, resulting in either a false conflict or a dual indication fault.

If there is not a field indication for each of the three outputs on a given load switch, a load resistor needs to be installed. The load resistor takes the place of a bulb or LED indication and provides a load for the channel red or yellow monitor input preventing the problems with unwarranted faults.

If only the green and yellow indications of the load switch are used (common with 5-section heads on protected/permissive left turns), a resistor needs to be installed on the red field terminal as shown above left.

If only the green arrow indication is used, the resistor should be installed on the yellow field terminal as shown lower left. This situation can occur when a 4-section head is used to display a left turn that is only used during a preemption, or when a 4-section flashing yellow arrow head is used to display a protected left turn. In either case, no resistor is needed on the red terminal as the signal sequence monitoring capability is not used. See STDS. NO. 3.0 and 7.0 for more information.

### Load Resistor Installation Detail

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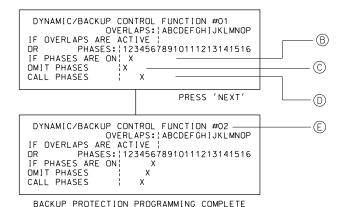
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### (OPTION #1)

### DYNAMIC BACKUP CONTROL PROGRAMMING

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE DYNAMIC/BACKUP CONTROL FUNCTIONS 1 AND 2.
- 2. FROM PHASE CONTROL FUNCTIONS MENU PRESS '2' (DYNAMIC/BACKUP CONTROL FUNCTIONS).



# (OPTION #2) BACKUP PROTECTION NOTE

(program controller as shown below)

From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Program phase 2 for 'Backup Protect'. Make sure the Red Revert times shown on the Signal Design Plans are programmed in the 'Phase Timing' menu.

### Oasis Backup Protection Programming Details

When a signal design requires the use of backup protection to eliminate a yellow trap situation, two options are available.

Option #1 uses the Dynamic Backup function. The upper left image is an exact duplication of the dynamic backup programming display found on a 2070 controller running Oasis control software.

The controller accomplishes dynamic backup protection by omitting the left turn phase while the opposite through movement is "ON". Phase "ON" is a controller function that is active during the phase green, yellow change, and red clearance intervals.

Below is a brief explanation of dynamic backup protection features and functionality:

- (A) Activation note This note directs the installer to the phase control page of the controller programming. At the bottom of this page there is a parameter listed called "Dynamic/Backup". The installer is directed to flag the Dynamic/Backup functions that will be in use, otherwise the backup programming will not function. See function number below in note (E).
- (B) Phases On row Phases selected here determine when an omit is placed during the signal sequence.
- © Omit Phases row Phases selected here determine where an omit is placed during the selected phase "ON".
- ① Call Phases row Phases selected here determine the phase that the omitted phase detectors will call while that phase is omitted. The call placed is a special dynamic call that will be released when the selected phase switches to green. This dynamic call produces a minimum recall type operation (dynamic call will not max out a phase).
- © Function number The controller is capable of up to sixteen dynamic functions. For normal backup protection, one function should be used for each left turn that is being omitted. The example shown to the left shows phases 1 and 5 being omitted by phases 2 and 6 respectively. The phase calls will cycle the controller through the side street through movements before serving phases 1 and/or 5. Please note that each left turn omit is accomplished in a separate function.

Option #2 uses the Backup Protect function. This function puts the through phases in All Red before serving the left turns. This function is typically used in conjunction with increased Red Revert times on the concurrent through phase.

## 2070 OASIS Backup Protection Programming

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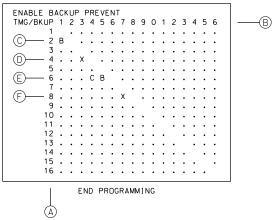
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# ECONOLITE ASC/3-2070 BACKUP PROTECTION ENABLE PROGRAMMING

(program controller as shown)

- 1. From Main Menu select 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 1. CONTROLLER SEO
- 3. From CONTROLLER SEQUENCE Submenu select 3. BACKUP PREVENT PHASES

Follow programming as shown below. On the 'ENABLE BACKUP PREVENT' screen move cursor to the appropriate field and press 'YES/NO' on the controller keypad to toggle field value between 'X' 'B'. 'C' and 'OFF'.



### NOTES

- 1. 'B' without a 'C' programmed for the 'TIMING' (row) phase inhibits the controller from servicing the 'BACKUP' (column) phase when the 'TIMING' (row) phase is active, or next, until the controller goes through Red Revert and Red Clear. Make sure the proper Red Revert and Red Clear times shown on the Signal Design plan are programmed in the controller phase timing.
- 'B' with a 'C' programmed for the 'TIMING' (row) phase places a demand on that 'BACKUP' (column) phase. The controller will then service the called phase and proceed normally.
- 3. X' inhibits the controller from servicing the 'BACKUP' (column) phase when the 'TIMING' (row) phase is active or next.

### Econolite ASC/3-2070 Backup Protection Programming Details

When a signal design requires the use of backup protection to eliminate a yellow trap situation, a dynamic approach as well as a red revert approach are both available from one programming screen as shown to the left.

Use the notes shown beneath the screen shot to interpret the backup protection requirements that may be shown on a signal design plan.

- (A) TMG Row Determines the phase when an omit is placed during the signal sequence.
- BKUP Column Determines the phase where an omit is placed while the TMG row phase is "ON". Also used to determine the phase that will be called while the TMG row phase is "ON" and the programmed omit phase has a call.
- © Example 1: The controller will back up from phase 2 to phase 1 after first going to all Red and timing the Red Revert times programmed in the controller. See Note 1.
- ① Example 2: Omits phase 3 when phase 4 is "ON" (the controller will not back up directly from phase 4 to phase 3). See Note 3.
- Example 3: When phase 6 is "ON" and the controller receives a call on phase 5, the controller will omit the phase 5 call and call phase 4. Phase 5 will be served when the controller recrosses the barrier after phase 4 clears. The controller will not back up directly from phase 6 to phase 5. See Note 2.
- © Example 4: Omits phase 7 when phase 8 is "ON" (the controller will not back up directly from phase 8 to phase 7). See Note 3.

ASC/3-2070 Backup Protection Programming

5.1

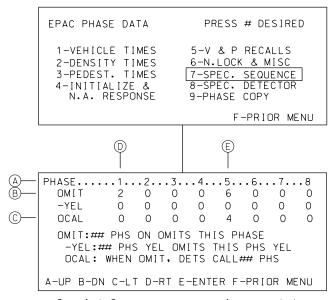
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# SE-PAC2070 BACK-UP PROTECTION PROGRAMMING DETAIL

(program controller as shown below)

From Main Menu, press '3' (Phase Data)



Special Sequence programming complete.

### SE-PAC2070 Backup Protection Programming Details

When a signal design requires the use of backup protection to eliminate a yellow trap situation, dynamic backup protect is available as shown to the left.

- (A) PHASE row Determines the phase where an omit is placed while the programmed OMIT phase is "ON".
- OMIT row Phases programmed here determine when an omit is placed during the signal sequence.
- © OCAL row Phases programmed here determine the phase that will be called while the programmed OMIT phase is "ON" and the phase in the PHASE row has a call.
- ① Example 1: Omits phase 1 when phase 2 is "ON" (the controller will not back up directly from phase 2 to phase 1).
- © Example 2: When phase 6 is "ON" and the controller receives a call on phase 5, the controller will omit the phase 5 call and call phase 4. Phase 5 will be served when the controller recrosses the barrier after phase 4 clears. The controller will not back up directly from phase 6 to phase 5.

SE-PAC2070 Backup Protection Programming

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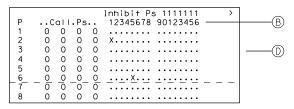
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# NAZTEC APOGEE CALL, INHIBIT, REDIRECT PROGRAMMING DETAIL

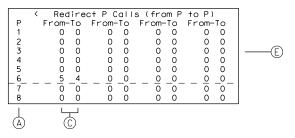
(USED FOR BACK-UP PROTECTION)

(program controller as shown below)

From Main Menu press '1' (Controller), then '1' (Phases), then '5' (Call, Inh, Redirect).



SCROLL TO THE RIGHT TO ACCESS "REDIRECT P CALLS" SCREEN BELOW



### OPERATIONAL NOTE

This programming will omit phase 1 when phase 2 is "ON" and omit phase 5 when phase 6 is "ON". Also, calls will be redirected from phase 5 to phase 4 during phase 6.

### Naztec Apogee Backup Protection Programming Details

When a signal design requires the use of backup protection to eliminate a yellow trap situation, dynamic backup protect is available as shown to the left.

- A Phase column Determines the phase when an inhibit (omit) is placed during the signal sequence.
- (B) Inhibit Phase row Determines the phase where an inhibit (omit) is placed while the phase in the 'Phase' column is "ON".
- © From-To column Phase programmed in the 'From' column will have calls redirected to the phase programmed in the 'To' column while the phase in the 'Phase' column is "ON".
- ① Example 1: Call Inhibits This programming will inhibit (omit) phase 1 when phase 2 is "ON" and will inhibit (omit) phase 5 when phase 6 is "ON". The controller will not back up directly from phase 2 to phase 1 or from phase 6 to phase 5.
- Example 2: Call Redirects A call redirect can work in conjunction with a call inhibit. When phase 6 is on and the controller receives a call on phase 5, the controller will inhibit (omit) the phase 5 call as per Example 1. To serve phase 5, the controller will first redirect phase 5 calls to phase 4 and phase 5 will be served when the controller recrosses the barrier after phase 4 clears. The controller will not back up directly from phase 6 to phase 5.

# Naztec Apogee Backup Protection Programming

STD. NO.

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### NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the signal plans.
- 2. Program phases 4 and 8 for Dual Entry.
- 3. Enable Simultaneous Gap-Out for all phases. (C)

- 6. Program phases 2, 4, 6 and 8 for Startup Ped Call.
- 8. If this signal will be managed by an ATMS software, enable  $-\widehat{\mathbb{H}}$  controller and detector logging for all detectors used at this location.

### Notes

All electrical details have a section of notes. A typical set for an Oasis 2070 design is shown above. Unneeded notes should be removed. Additionally, if there is a need to highlight an unusual setting or feature about the signal design that is not covered elsewhere on the electrical detail, a custom note can be added to this space.

### 2070 Oasis Notes

- (A) Flash setup note The first sentence, concerning flash color setup on unused load switches, may be omitted if all load switches are used. The second sentence is always used.
- (B) Dual Entry note Directs that the indicated phases be programmed for Dual Entry. The '2070 Timing Chart' on the signal plan will specify which phases require this feature.
- © Simultaneous Gap-Out note Directs that all phases be programmed for Simultaneous Gap-Out. This note always appears and never requires modification.
- ① Variable Initial and Gap Reduction note Directs that the indicated phases be programmed for these timing features. If the '2070 Timing Chart' on the signal plan has timing values for 'Seconds Per Actuation' and 'Max Variable Initial', that phase should be programmed for Variable Initial. If values are shown for 'Time Before Reduction', 'Time To Reduce', and 'Minimum Gap', the phase should be programmed for Gap Reduction.
- © Controller Start Up note In general, the controller should be programmed to start up in the phase or phases that flash yellow. If no phases flash yellow, the controller needs to be programmed to start up in a red clearance interval. If this is the case, consult the signal plan designer to see if there is a preference about what phase(s) should be served first.
- (F) Startup Ped Call note Any ped phases that will be in use during normal operation should be listed here.
- © Yellow Flash note This ensures phases 2 and 6 flash yellow during controller flash. Wag overlap programming flashes overlap 1 (OLA) and overlap 2 (OLB) concurrently with phases 1 and 3 (typically for FYA applications).
- (H) Detector Logging note Alerts installer to enable detector logging for detectors in use when the cabinet and controller are controlled by Advanced Traffic Management System (ATMS) software. Include on all plans except for those that are part of a City system.
- ① System note If the signal is part of a closed loop or urban traffic control system, the system type and/or name (if available) is listed here.

### 2070 Oasis Notes

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### ASC/3-2070 Notes

### NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.

- (A) Flash setup note The first sentence, concerning flash color setup on unused load switches, may be omitted if all load switches are used. The second sentence is always used.
- (B) Dual Entry note Directs that the indicated phases be programmed for Dual Entry. The 'ASC/3-2070 Timing Chart' on the signal plan will specify which phases require this feature.
- © Controller Start Up note In general, the controller should be programmed to start up in the phase or phases that flash yellow. If no phases flash yellow, the controller needs to be programmed to start up in a red clearance interval. If this is the case, consult the signal plan designer to see if there is a preference about what phase(s) should be served first. If the the startup phase also has a ped movement, it should be programmed to start in 'Walk' instead of 'Green'.
- ① Detector Logging note Alerts installer to enable detector logging for detectors in use when the cabinet and controller are controlled by Advanced Traffic Management System (ATMS) software. Include on all plans except for those that are part of a City system.
- © System note If the signal is part of a closed loop or urban traffic control system, the system type and/or name (if available) is listed here.

ASC/3-2070 Notes

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### SE-PAC 2070 Notes

### NOTES

- To prevent "flash-conflict" problems, insert red flash
  program blocks for all unused vehicle load switches in
  the output file. The installer shall verify that signal
  heads flash in accordance with the Signal Plans.

- (A) Flash setup note The first sentence, concerning flash color setup on unused load switches, may be omitted if all load switches are used. The second sentence is always used.
- ⑤ Controller Start Up note In general, the controller should be programmed to start up in the phase or phases that flash yellow. If no phases flash yellow, the controller needs to be programmed to start up in a red clearance interval. If this is the case, consult the signal plan designer to see if there is a preference about what phase(s) should be served first.
- © Simultaneous Gap-Out note Directs that all phases be programmed for Simultaneous Gap-Out. This note always appears and never requires modification.
- ① Dual Entry note Directs that the indicated phases be programmed for Dual Entry. The 'SE-PAC 2070 Timing Chart' on the signal plan will specify which phases require this feature.
- © Volume Density Operation note Directs that the indicated phases be programmed for the following timing features if indicated on the 'SE-PAC 2070 Timing Chart' on the signal plan: 'Added Initial', 'Maximum Initial', 'Time Before Reduction', 'Time To Reduce', 'Minimum Gap'.
- System note If the signal is part of a closed loop or urban traffic control system, the system type and/or name (if available) is listed here.

SE-PAC 2070 Notes

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### Apogee Notes

### NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- 2. <u>Initialize database</u> in Naztec 2070 local software (Apogee) as FULL-CALTRANS. This initialization should be done prior to programming controller.

- 7. Ensure "InhFYARedSt" feature is set to "ON".
- 8. Program controller to provide a 1 second delay on the Flash Sense/Local Flash input. Use the following logic statement to provide this functionality:

  FROM MAIN MENU->1->8->7 (I/O LOGIC)

  Result Src.Fcn TimeOp Time 1208 = 01208 DLY 1
- 9. Program phases 2, 4, 6, and 8 for Dual Entry.

- A Flash setup note The first sentence, concerning flash color setup on unused load switches, may be omitted if all load switches are used. The second sentence is always used.
- B Initialize Database note loads controller with defaults required to run standard eight phase with CALTRANS I/O mapping.
- © Initialize I/O Loads the I/O map with the NCDOT I/O mapping that might not be identical to CALTRANS I/O mapping.
- ① Controller Start Up note In general, the controller should be programmed to start up in the phase or phases that flash yellow. If no phases flash yellow, the controller needs to be programmed to start up in a red clearance interval. If this is the case, consult the signal plan designer to see if there is a preference about what phase(s) should be served first. If the the startup phase also has a ped movement, it should be programmed to start in 'Walk' instead of 'Green'.
- (E) Start Up Flash Determines how long a controller will remain in flash following a power interruption.
- E Local Flash Start Set to 'DRK' in a 170 type cabinet so the controller indications initialize in the DRK state when the controller resets and the flash transfer relays are energized.
- ⑤ InhFYARedSt FYA's can be programmed to be inhibited or allowed to run immediately following controller startup by programming this feature.
- (H) Dual Entry note Directs that the indicated phases be programmed for Dual Entry. The Controller Timing Chart on the signal plan will specify which phases require this feature.
- ① System note If the signal is part of a closed loop or urban traffic control system, the system type and/or name (if available) is listed here.

Naztec Apogee Notes

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### EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL WD ENABLE (\) (remove jumpers and set switches as shown) REMOVE DIODE JUMPERS 2-5 and 2-6. 2010 -RP DISABLE WD 1.0 SEC GY FNABLE SF#1 POLARITY LEDguard -FYA COMPACT FYA 1-9 FYA 3-10 0100010 0180 090 COMPONENT SIDE REMOVE JUMPERS AS SHOWN 1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently. DENOTES POSITION

2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.

4. Connect serial cable from conflict monitor to comm. port 1 of 2070

controller. Ensure conflict monitor communicates with 2070.

3. Ensure that Red Enable is active at all times during normal operation.

### 2018 Conflict Monitor Programming

The conflict monitor typically used in all NCDOT 2070 installations is the EDI model 2018ECL-NC. (See note 1 on sheet 2) The representation at the left is found in the top left corner on all the 2070 start drawings.

The 2018ECL-NC has 18 monitor channels. The default channel to load switch to function relationships are as follows:

|       | S1     |  | Phase  | 1        |    |
|-------|--------|--|--------|----------|----|
|       | S2     |  | Phase  | 2        |    |
|       | S4     |  | Phase  | 3        |    |
|       | S5     |  | Phase  | 4        |    |
|       | S7     |  | Phase  | 5        |    |
|       | S8     |  | Phase  | 6        |    |
| · ——  | S10    | $\overline{}$  | Phase  | 7        |    |
|       | S11    | $\overline{}$  | Phase  | 8        |    |
|       | AUX S1 |  | Overla | ар А     |    |
| 0 —   | AUX S2 |  | Overla | ар В     |    |
| 1 ——. | AUX S4 |  | Overla | ap C     |    |
| 2 ——. | AUX S5 |  | Overla | ap D     |    |
| 3 —   | S3     |  | Phase  | 2 PED    |    |
| 4 ——  | S6     | $\overline{}$  | Phase  | 4 PED    |    |
| 5 —   | S9     |  | Phase  | 6 PED    |    |
| 6 —   | S12    |  | Phase  | 8 PED    |    |
| 7 —   | AUX S3 |  | Spare  |          | E) |
| 8     | AUX S6 |  | Spare  | (Overlap | F) |
|       | 0      | \$2<br>\$4<br>\$5<br>\$7<br>\$8<br>\$10<br>\$11<br>\$0<br>\$4<br>\$1<br>\$2<br>\$3<br>\$3<br>\$3<br>\$4<br>\$6<br>\$5<br>\$9<br>\$6<br>\$12<br>7 | \$2    | S2       | S2 |

The channel to load switch relationship is fixed in the cabinet hardware. The load switch function can be changed in the controller software. Load switches AUX S1-AUX S6 are on the auxiliary output file.

### Features:

A Remove diode jumper note - For any two movements to be allowed to run concurrently, the corresponding diode jumper must be removed on the monitor card. This includes not only phases that can run concurrently, but also any ped or overlap that can run concurrently. Any permissible combination that does not have the corresponding jumper removed will result in an unwarranted conflict fault and place the intersection in flash. Conversely, removing a jumper representing a movement that should not be allowed creates a dangerous scenario where a true conflict can go undetected. This note lists the jumpers that should be removed on the monitor card.

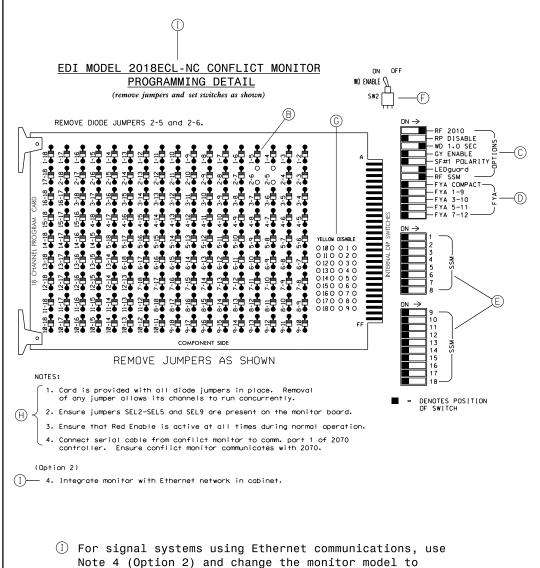
# 2018 Conflict Monitor Programming

OF SWITCH

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Features (cont.):

- Monitor card programming The electrical detail provides a graphic representation of the monitor card after the appropriate diode jumpers have been removed as described above. This drawing should always match the remove diode jumper note directly above.
- © Option switches These dip switches control a variety of optional settings for the 2018ECL-NC monitor. The settings shown at left should be used for all electrical details. For more information on these options, refer to the manufacturer's operations manual.
- FYA switches These switches are used to enable flashing yellow arrow monitoring for 3-section and 4-section FYA's using overlaps. Refer to the manufacturer's operations manual for more information on these switches.
- E SSM switches These switches are used to enable dual indication, red fail, and minimum yellow clearance monitoring on individual monitor channels. In general, any channel that has both a green and a yellow indication in the field should have its SSM switch set to the 'ON' position. Channels used to monitor pedestrian movements, or the green arrow exclusively for a four-section head or four-section FYA head, should be set to the 'OFF' position.
- (F) Watchdog enable Enables the controller watchdog monitoring feature. If the monitor fails to sense the logic level signal being toggled by the controller, a 'WDT Error' fault will be triggered. Should always be shown in the 'ON' position.
- © Yellow disable jumpers This feature allows the minimum yellow change monitoring to be disabled for a channel being used for a pedestrian movement. Since NCDOT also does not monitor dual indication for peds, the SSM switches for those channels should be set to 'OFF', making the use of the yellow disable jumpers unnecessary.
- (H) Notes These notes should appear with the conflict monitor programming detail on all 2070 electrical details. For more information on these options and conflict monitor functionality, refer to the manufacturer's operations manual.

# 2018 Conflict Monitor Programming

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"2018ECLip-NC".

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### INPUT FILE POSITION LAYOUT

(front view)

|       | _ | 1    | 2   | 3   | 4    | 5    | 6   | 7   | 8    | 9            | 10          | 11          | 12             | 13             | 14             |
|-------|---|------|-----|-----|------|------|-----|-----|------|--------------|-------------|-------------|----------------|----------------|----------------|
|       |   | Ø 1  | ø 2 | Ø 2 | Ø 2  | ø 3  | Ø 4 | Ø 4 | Ø 4  | SYS.<br>DET. | SLO         | S L O       | Ø2 PED         | ľ              | ' -            |
| FILE  | ٦ | 1A   | 2A  | 2C  | 2E   | 3A   | 4A  | 4C  | 4E   | S1           | '           | T           | DC<br>ISOLATOR | DC<br>ISOLATOR | DC<br>ISOLATOR |
| " I " |   | NOT  | ø 2 | Ø 2 | NOT  | NOT  | Ø 4 | Ø 4 | NOT  | SYS.<br>DET. | E<br>M<br>P | E<br>M<br>P | Ø4 PED         | Ø8 PED         | ST             |
|       | _ | USED | 2B  | 2D  | USED | USED | 4B  | 4D  | USED | S2           | Ť           | Ť           | DC<br>ISOLATOR | DC<br>ISOLATOR | DC<br>ISOLATOR |
|       |   |      |     |     |      |      |     |     | 1    | 00           |             |             | 1              |                |                |
|       |   | Ø 5  | Ø 6 | Ø6  | Ø6   | Ø 7  | Ø 8 | Ø 8 | Ø 8  | SYS.<br>DET. | S<br>L      | S<br>L      | PRE3           | PRE4           | PRE1           |
| FILE  | ١ | 5A   | 6A  | 6C  | 6E   | 7A   | 8A  | 8C  | 8E   | S3           | ģ           | P           | DC<br>ISOLATOR | DC<br>ISOLATOR | AC<br>ISOLATOR |
| "J"   |   | NOT  | ø6  | Ø6  | NOT  | NOT  | Ø 8 | Ø 8 | NOT  | SYS.<br>DET. | E<br>M<br>P | E M         | PRE5           | PRE6           | PRE2           |
|       | _ | USED | 6B  | 6D  | USED | USED | 8B  | 8D  | USED | S4           | Ť<br>Y      | Ť           | DC<br>ISOLATOR | DC<br>ISOLATOR | AC<br>ISOLATOR |

EX.: 1A. 2A. ETC. = LOOP NO.'S

FS = FLASH SENSE ST = STOP TIME PRE = PREEMPT

### INPUT FILE POSITION LAYOUT

|       |   |      | 1   | A      |        |      |     | (front      | view)       |              |             |             |                |                |                |
|-------|---|------|-----|--------|--------|------|-----|-------------|-------------|--------------|-------------|-------------|----------------|----------------|----------------|
|       | - | 1    | 2   | 3      | 4      | 5    | 6   | 7           | 8           | 9            | 10          | 11          | 12             | 13             | 14             |
|       | ш | Ø 1  | Ø 2 | Ø 2    | SLOT   | Ø 3  | Ø 4 | Ø 4         | S<br>L<br>Q | SYS.<br>DET. | S L Q       | S<br>L<br>Q | Ø2PED          | ľ              | _              |
| FILE  |   | 1A   | 2A  | 2C     | '      | 3A   | 4A  | 4C          |             | S1           | '           | '           | DC<br>ISOLATOR | DC<br>ISOLATOR | DC<br>ISOLATOR |
| " I " |   | NOT  | ø 2 | Ø 2    | EΣP    | NOT  | Ø 4 | NOT         | E<br>M<br>P | SYS.<br>DET. | E<br>M<br>P | EΜP         | NOT            | NOT            | ST             |
|       |   | USED | 2B  | 2D     | T      | USED | 4B  | USED        | Ť           | S2           | Ť           | Ť           | USED           | USED           | DC<br>ISOLATOR |
|       |   | Ø 5  | Ø 6 | ø6/SYS | S      | Ø 7  | Ø 8 | S<br>L      | S<br>L      | S<br>L       | S<br>L      | S           | S              | S              | PRE1           |
| FILE  | U | 5A   | 6A  | 6C/S3  | þ      | 7A   | 8A  | Ď           | Ď           | Ď            | Ď           | Ö           | Ď              | Ď              | AC<br>ISOLATOR |
| "J"   |   | Ø 5  | Ø 6 | ø6/SYS | EΜP    | NOT  | ø 8 | E<br>M<br>P | E<br>M<br>P | E<br>M<br>P  | E<br>M<br>P | E<br>M<br>P | E M            | E<br>M<br>P    | NOT            |
|       | _ | 5B   | 6B  | 6D/S4  | T<br>Y | USED | 8B  | ,<br>T<br>Y | ,<br>T<br>Y | T<br>Y       | ,<br>T<br>Y | T<br>Y      | Ť<br>Y         | Ť<br>Y         | USED           |

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE ST = STOP TIME PRE = PREEMPT

### 2070 Input File Layout (332)

NCDOT uses 2070 controllers in type 170 cabinets. The base mounted 332 cabinet has two input files labeled  $^{\prime}\mathrm{I}^{\prime}$  and  $^{\prime}\mathrm{J}^{\prime}$  that accept inputs for traffic detection, pedestrian push buttons, preempt calls or other functions deemed necessary.

Each input file has 14 slots. Each slot can hold a 2-channel inductive loop detector, AC isolator or DC isolator. Each slot has two input terminals, but not every input terminal is independently connected to the controller. Slots 1, 4, 5 and 8 have the two input pins jumpered together and wired to a single controller harness pin. Neither of the input pins for slot 10 are connected to the controller.

Two examples of the input file layout for the base mounted 332 cabinet are shown left. The upper example shows how the rack is represented on the start drawings. The functions shown for slots 1-8 and 12-14 correspond to the default input assignments in the Econolite Oasis software. The controller detectors for slot 9 are assigned as local detectors by default but NCDOT reserves them for system detectors instead. Slot 10 is not wired to the controller and is therefore unused. The upper and lower channels of slot 11 in the I-File are assigned to Manual Advance and Manual Control Enable, respectively. The upper channel of slot 11 in the J-File is a spare and the lower slot is the Door Ajar input to the controller.

### Features:

A Inductive Loop Detectors - Input file slots 1-9 are set up for inductive loop detector cards. Each card has two channels. Each channel is represented on the electrical detail by a block in the layouts shown on the left. For each channel, the function of the loop is shown in the upper half of the block while the loop name is shown in the lower half. A channel can be assigned to a local detector, a system detector, or both. While the default phase settings should be followed as much as practical, controller detectors can be easily reassigned as needed.

(continued on next page)

# 2070 Input File Layout - 332

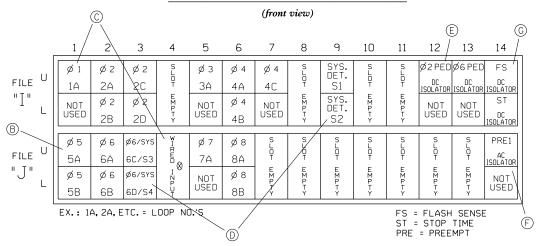
SIGNALS MANAGEMENT
TRANSPORTATION MOBILITY AND SAFETY DIVISION
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

STD. NO.

8.0

SHEET 1 OF 2

### INPUT FILE POSITION LAYOUT



 $\otimes$  Wired Input - Do not populate slot with detector card

(H) Detector Test Switches - There are eight detector test switches in the cabinet labeled 1-8 that can be used to test eight different phase approaches. They are wired, in order, to the controller C1 pin for slots 1,4,5, and 8 of the "I" file and the "J" file. When any of the aforementioned slots are used for detector inputs, it is recommended that the assigned phase match the default phase of the slot so a test switch activation will place a call to the correct phase.

Features (cont.):

- (B) Slots 1, 4, 5 and 8 have only one controller input pin. The lower channel is normally unused. However, the lower channel of these slots may be used if neither the loop on the upper channel nor the loop proposed for use on the lower channel have any associated delay timing and all other settings for both loops are identical. The controller will view the two loops as if they are one.
- © Loops That Call Two Phases Sometimes a left turn loop will call both the left turn phase and the adjacent through movement with different timings or attributes for each. In this case, two detector channels are needed for the single loop. Utilize the default programmed detector settings. Populate the turn phase detector slot with a detector card. Then jumper the turn phase controller input pin to the through movement controller input pin that is associated with slot(s) 4 or 8. The through movement slot is not populated with a detector card as shown in the example at left.
- ① System Detectors Detector cards for system loops are normally placed in slots I9 and J9. If more than four dedicated system loops are needed, an unused channel from slots 1-8 may be used. A detector may also serve as both a local and a system detector, as shown in slot J3 in the example at left.
- © Ped Detectors Pedestrian push buttons interface to the controller through DC isolator cards in slots I12 and I13.
- For Preempt Inputs The default setup can accommodate six preempt inputs. Preempts 1 and 2 interface the controller through an AC isolator card in slot J14. Preempt 1 is normally reserved for railroad preemption, while preempt 2 can be used for a second railroad preempt or (more commonly) for push button style emergency vehicle preemption. Preempts 3-6 are normally reserved for vehicle initiated EV preemptions and interface the controller through DC isolator cards. For more information on preemption see STD. No. 9.0.
- © Slot I14 is reserved for flash sense and stop time. This DC isolator card is equipped from the factory and this slot always appears on electrical details without modification.
- \* Using any of these slots for purposes other than those shown here may require reassignment of inputs in the controller software and/or modification of the surge protection on the cabinet input panel.

2070 Input File Layout - 332

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STD. NO.

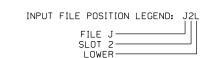
8.0

SHEET 2 OF 2

### INPUT FILE CONNECTION & PROGRAMMING CHART

| LOOP NO.            | LOOP<br>TERMINAL | INPUT<br>FILE POS. | PIN<br>NO. | INPUT<br>ASSIGNMENT<br>NO. | DETECTOR<br>NO. | NEMA<br>PHASE | CALL                 | EXTEND  | FULL<br>TIME<br>DELAY | STRETCH<br>TIME | DELAY<br>TIME |
|---------------------|------------------|--------------------|------------|----------------------------|-----------------|---------------|----------------------|---------|-----------------------|-----------------|---------------|
| 1A 1                | TB2-1,2          | I1U                | 56         | 18                         | 1               | 1             | Υ                    | Y       |                       |                 | 15            |
| l in                | -                | J4U                | 48         | 10                         | 26              | 6             | Y                    | Y       | Υ                     |                 | 3             |
| 2A                  | TB2-5,6          | I2U                | 39         | 1                          | 2               | 2             | Y                    | Y       |                       |                 |               |
| 2B                  | TB2-7,8          | I2L                | 43         | 5                          | 12              | 2             | Υ                    | Υ       |                       |                 |               |
| 2C                  | TB2-9,10         | I3U                | 63         | 25                         | 32              | 2             | Y                    | Y       |                       |                 |               |
| 2D                  | TB2-11,12        | I3L                | 76         | 38                         | 42              | 2             | Υ                    | Y       |                       |                 |               |
| 3A                  | TB4-5,6          | I5U                | 58         | 20                         | 3               | 3             | Υ                    | Υ       |                       |                 | 3             |
| 4A                  | TB4-9,10         | I6U                | 41         | 3                          | 4               | 4             |                      | Υ       |                       | 2.8             |               |
| 4B                  | TB4-11,12        | I6L                | 45         | 7                          | 14              | 4             | Υ                    | Υ       |                       |                 | 15            |
| 4C                  | TB6-1,2          | I7U                | 65         | 27                         | 34              | 4             | Υ                    | Υ       |                       |                 | 15            |
| * S1                | TB6-9,10         | I9U                | 60         | 22                         | 11              | SYS           |                      |         |                       |                 |               |
| * S2                | TB6-11,12        | I9L                | 62         | 24                         | 13              | SYS           |                      |         |                       |                 |               |
| 5A                  | TB3-1,2          | J1U                | 55         | 17                         | 5               | 5             | Υ                    | Υ       |                       |                 |               |
| 5B                  | TB3-3,4          | J1L                | 55         | 17                         | 5               | 5             | Υ                    | Y       |                       |                 |               |
| 6A                  | TB3-5,6          | J2U                | 40         | 2                          | 6               | 6             | Υ                    | Y       |                       |                 |               |
| 6B                  | TB3-7,8          | J2L                | 44         | 6                          | 16              | 6             | Υ                    | Y       |                       |                 |               |
| 6C/S3               | TB3-9,10         | J3U                | 64         | 26                         | 36              | 6/SYS         | Y                    | Y       |                       |                 |               |
| 6D/S4               | TB3-11,12        | J3L                | 77         | 39                         | 46              | 6/SYS         | Υ                    | Y       |                       |                 |               |
| 7A                  | TB5-5,6          | J5U                | 57         | 19                         | 7               | 7             | Υ                    | Y       |                       |                 | 3             |
| 88                  | TB5-9,10         | J6U                | 42         | 4                          | 8               | 8             | Υ                    | Y       |                       |                 |               |
| 8B                  | TB5-11,12        | J6L                | 46         | 8                          | 18              | 8             | Υ                    | Y       |                       |                 |               |
| PED PUSH<br>BUTTONS |                  |                    |            |                            |                 |               | NOT                  | _       |                       |                 |               |
| P21 <b>,</b> P22    | TB8-4,6          | I12U               | 67         | 29                         | PED 2           | 2 PED         | INSTALL DC ISOLATORS |         |                       |                 |               |
| P61,P62             | TB8-7,9          | I13U               | 68         | 30                         | PED 6           | 6 PED         | l I                  | IN INPL | JT FIL                | E SLOTS         |               |
|                     |                  |                    |            |                            |                 |               | Ī                    | 12 ANE  | ) I13.                |                 |               |

\* SYSTEM DETECTOR ONLY. REMOVE THE VEHICLE PHASE ASSIGNED TO THIS DETECTOR IN THE DEFAULT PROGRAMMING.



Full jumper list if all wired inputs are used:

- ADD JUMPER FROM I1-W TO J4-W. ON REAR OF INPUT FILE.
- <sup>2</sup> ADD JUMPER FROM I5-W TO J8-W, ON REAR OF INPUT FILE.
- 3 ADD JUMPER FROM J1-W TO I4-W, ON REAR OF INPUT FILE.
- ADD JUMPER FROM J5-W TO 18-W, ON REAR OF INPUT FILE.

# 2070 Input File Connection & Programming Chart - 332 (Oasis)

The purpose of the Input File Connection & Programming Chart is to provide the installer with a convenient reference for connecting inductive loops and pedestrian push buttons to the cabinet as well as for programming controller detectors. The example shown at left is set up to match the example shown in the 2070 Input File Layout section (STD. No. 8.0 sheet 2, feature "C").

The key value to each row is the input file position (third column from the left). The first six values in the row should be considered attributes of the input file position. The relationship of the input file position with a specific inductive loop (first column) is decided during the preparation of the input file layout. Also, once the input file layout is established, all rows corresponding to unused input file positions can be deleted.

The relationship of the input file position with the loop terminal and pin numbers is fixed in the cabinet hardware. Changing these values entails rewiring the cabinet and should be avoided. The relationship of the input file position with the input assignment and controller detector numbers is set in the controller software. The values shown on the start drawings are the controller defaults. Changing them is only necessary if the detector is to be reassigned to another function.

The remaining (right-most) six columns contain attributes that apply to the specific loop associated with the input file position in question. These values can be found in the '2070 Loop Detector and Installation' chart on the signal plan and should be duplicated in this chart.

### Additional Features:

- A Pedestrian Push buttons If the design utilizes pedestrian push buttons, an extension is added to the Input File Connection & Programming Chart that contains the appropriate values for those detector channels. The values in the last five columns of the main chart do not apply to pedestrian detectors. The CADD cell containing the pedestrian detectors also includes a note reminding the installer to equip the appropriate slots with a DC isolator.
- (See STD. NO. 8.0 sheet 2), a note is placed below the chart detailing which controller input pins should be jumpered together.
- © System Detector Note If a detector channel is to serve as a system detector only, this note is included to remind the installer to remove the vehicle phase assigned to that detector in the default programming.

# 2070 Input File Connection & Programming Chart – 332 (Oasis)

SIGNALS MANAGEMENT
TRANSPORTATION MOBILITY AND SAFETY DIVISION
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

STD. NO.

8.1

SHEET 1 OF 2

### INPUT FILE CONNECTION & PROGRAMMING CHART

| LOOP NO.            | LOOP<br>TERMINAL   | INPUT<br>FILE POS. | PIN<br>NO. | DETECTOR<br>NO. | NEMA<br>PHASE | CALL | EXTEND<br>TIME | DELAY<br>TIME | ADDED<br>INITIAL | DETECTOR<br>TYPE |
|---------------------|--------------------|--------------------|------------|-----------------|---------------|------|----------------|---------------|------------------|------------------|
| 1A 1                | TB2-1,2            | IIU                | 56         | 1               | 1             | YES  |                | 15            |                  | S                |
| '''                 | -                  | J4U                | 48         | 26              | 6             | YES  |                | 3             |                  | G                |
| 2A                  | TB2-5,6            | I2U                | 39         | 2               | 2             | YES  |                |               | Χ                | N                |
| 2B                  | TB2-7,8            | I2L                | 43         | 12              | 2             | YES  |                |               | Χ                | N                |
| 20                  | TB2-9,10           | I3U                | 63         | 32              | 2             | YES  |                |               |                  | S                |
| 2D                  | TB2-11,12          | I3L                | 76         | 42              | 2             | YES  |                |               |                  | S                |
| 3A                  | TB4-5,6            | I5U                | 58         | 3               | 3             | YES  |                | 3             |                  | S                |
| 4A                  | TB4-9,10           | I6U                | 41         | 4               | 4             | YES  | 2.0            |               |                  | S                |
| 4B                  | TB4-11,12          | I6L                | 45         | 14              | 4             | YES  |                | 15            |                  | S                |
| 4C                  | TB6-1,2            | I7U                | 65         | 34              | 4             | YES  |                | 15            |                  | S                |
| * S1                | TB6-9,10           | I9U                | 60         | 11              | SYS           | NO   |                |               |                  | N                |
| * S2                | TB6-11,12          | I9L                | 62         | 13              | SYS           | NO   |                |               |                  | N                |
| 5A                  | TB3-1,2            | J1U                | 55         | 5               | 5             | YES  |                |               |                  | S                |
| 5B                  | TB3-3,4            | J1L                | 55         | 5               | 5             | YES  |                |               |                  | S                |
| 6A                  | TB3-5,6            | J2U                | 40         | 6               | 6             | YES  |                |               | Χ                | N                |
| 6B                  | TB3-7,8            | J2L                | 44         | 16              | 6             | YES  |                |               | Χ                | N                |
| 6C/S3               | TB3-9,10           | J3U                | 64         | 36              | 6/SYS         | YES  |                |               |                  | N                |
| 6D/S4               | TB3-11,12          | J3L                | 77         | 46              | 6/SYS         | YES  |                |               |                  | N                |
| 7A                  | TB5-5 <b>,</b> 6   | J5U                | 57         | 7               | 7             | YES  |                | 3             |                  | S                |
| 8A                  | TB5-9,10           | J6U                | 42         | 8               | 8             | YES  |                |               |                  | S                |
| 8B                  | TB5-11 <b>,</b> 12 | J6L                | 46         | 18              | 8             | YES  |                |               |                  | S                |
| PED PUSH<br>BUTTONS |                    |                    |            |                 |               | NOTE | :              |               |                  |                  |
| P21,P22             | TB8-4,6            | I12U               | 67         | PED 2           | 2 PED         | l IN | NSTALL [       | C ISC         | LATORS           |                  |
| P61,P62             | TB8-7,9            | I13U               | 68         | PED 6           | 6 PED         | l IN | N INPUT        | FILE          | SLOTS            |                  |

I12 AND I13.

st System detector only. Remove any assigned vehicle phase.

INPUT FILE POSITION LEGEND: J2L

FILE J

SLOT 2

LOWER

Full jumper list if all wired inputs are used:

- 1 ADD JUMPER FROM I1-W TO J4-W, ON REAR OF INPUT FILE.
- <sup>2</sup> ADD JUMPER FROM I5-W TO J8-W, ON REAR OF INPUT FILE.
- 3 ADD JUMPER FROM J1-W TO I4-W, ON REAR OF INPUT FILE.
- ADD JUMPER FROM J5-W TO 18-W, ON REAR OF INPUT FILE.

# 2070 Input File Connection & Programming Chart - 332 (ASC/3-2070)

The purpose of the Input File Connection & Programming Chart is to provide the installer with a convenient reference for connecting inductive loops and pedestrian push buttons to the cabinet as well as for programming controller detectors. The example shown at left is set up to match the example shown in the 2070 Input File Layout section (STD. No. 8.0 sheet 2, feature "C").

The key value to each row is the input file position (third column from the left). The first five values in the row should be considered attributes of the input file position. The relationship of the input file position with a specific inductive loop (first column) is decided during the preparation of the input file layout. Also, once the input file layout is established, all rows corresponding to unused input file positions can be deleted.

The relationship of the input file position with the loop terminal and pin numbers is fixed in the cabinet hardware. Changing these values entails rewiring the cabinet and should be avoided. The relationship of the input file positions with the controller detector numbers is set in the controller software. The values shown on the start drawings are the controller defaults. Changing them is only necessary if the detector is to be reassigned to another function.

The remaining (right-most) five columns contain attributes that apply to the specific loop associated with the input file position in question. These values can be found in the '2070 Loop Detector and Installation' chart on the signal plan and should be duplicated in this chart.

Additional Features:

- (A) See STD. No. 8.1 Sheet 1
- B See STD. No. 8.1 Sheet 1
- © See STD. No. 8.1 Sheet 1
- $\odot$  ADDED INITIAL loops that are designed for volume density. Put an 'X' in this column if a corresponding 'X' exists on the detector installation chart on the signal plan.
- © DETECTOR TYPE this column is where the exact detector type is programmed. Examples could be 'G', which allows delay during green, or 'S' for standard detector features. The detector type for each loop is found on the signal plan.

# 2070 Input File Connection & Programming Chart – 332 (ASC/3–2070)

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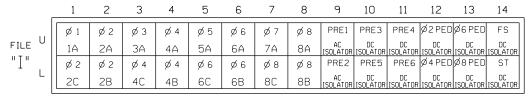
STD. NO.

8.1

SHEET 2 OF 2

### INPUT FILE POSITION LAYOUT

### (front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE ST = STOP TIME PRE = PREEMPT

# 2070 Input File Layout (336)

NCDOT uses 2070 controllers in type 170 cabinets. The pole mounted 336 cabinet has one input file labeled 'I' that accepts inputs for traffic detection, pedestrian push buttons, preempt calls or other functions deemed necessary.

The input file has 14 slots. Each slot can hold a 2-channel inductive loop detector, AC isolator or DC isolator. Each slot has three input terminals,  $^\prime F^\prime$ ,  $^\prime W^\prime$ , and  $^\prime SP^\prime$  that are independently connected to the controller by way of a C1 pin.

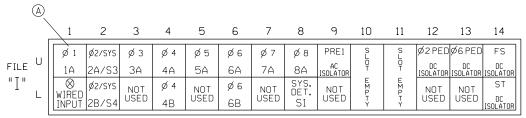
Two examples of the input file layout for the pole mounted 336 cabinet are shown left. The upper example shows how the rack is represented on the start drawings. The functions shown for slots 1-14 correspond to the default input assignments in the Econolite Oasis software. The 'SP' pin in slot I9 is the Door Ajar input to the controller, and the 'SP' pin in slot I14 is the Manual Advance input to the controller.

### Features:

A Inductive Loop Detectors - Input file slots 1-8 are set up for inductive loop detector cards. Each card has two channels. Each channel is represented on the electrical detail by a block in the layouts shown on the left. For each channel, the function of the loop is shown in the upper half of the block while the loop name is shown in the lower half. A channel can be assigned to a local detector, a system detector, or both. While the default phase settings should be followed as much as practical, controller detectors can be easily reassigned as needed.

### INPUT FILE POSITION LAYOUT

### (front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE ST = STOP TIME PRE = PREEMPT

(continued on next page)

# 2070 Input File Layout - 336

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

8.2

SHEET 1 OF 2

### INPUT FILE POSITION LAYOUT

(front view) 9 10 11 12 13 Ø2PEDØ6PED Ø2/SYS øз Ø 4 Ø 5 Ø6 Ø 7 PRE1 FILE U AC SOLATOR 5A 6A 7Α 8A 4Α ISNIÄTNR SOLĂTOR  $\otimes$ SYS. DET. ST Ø2/SYS Ø6 NOT USED NOT NOT NOT NOT USED USED DC USED USED INPUT 2B/S4 4B 6B ISOLĂTOR EX.: 1A. 2A. ETC. = LOOP NO.'S FS = FLASH SENSE ST = STOP TIME PRE = PREEMPT ⊗ Wired Input - turn off Channel 2.

(2006 Spec. cabinet only)

© Detector Test Switches - There are eight detector test switches in the cabinet labeled 1-8 that can be used to test eight different phase approaches. They are wired, in order, to the controller C1 pin for slots 1-8 of the "I" file to the upper channel. When any of the aforementioned slots are used for detector inputs, it is recommended that the assigned phase match the default phase of the slot in the upper channel so a test switch activation will place a call to the correct phase.

### Features (cont):

- (B) System Detectors Detector cards for dedicated system loops may be placed in any unused detector slot. If space in the I-File runs out and additional system detectors are called for, a detector may also serve as both a local and a system detector as shown in slot I2 in the example at left.
- © Preempt Inputs The default setup can accommodate six preempt inputs. Preempts 1 and 2 interface the controller through an AC isolator card in slot J9. Preempt 1 is normally reserved for railroad preemption, while preempt 2 can be used for a second railroad preempt or (more commonly) for push button style emergency vehicle preemption. Preempts 3-6 are normally reserved for vehicle initiated EV preemptions and interface the controller through DC isolator cards. For more information on preemption see STD. No. 9.0.
- Ped Detectors Pedestrian push buttons interface to the controller through DC isolator cards in slots I12 and I13.
- © Slot I14 is reserved for flash sense and stop time. This DC isolator card is equipped from the factory and this slot always appears on electrical details without modification.
- E Loops That Call Two Phases (2006 Spec. cabinets) Loops that call two phases in a 336 pole mounted cabinet require special wiring. A jumper must be added from the controller input pin of the first phase to the controller input pin of the second phase in the same slot that the loop detector is installed. Also, the second channel for the loop detector plugged into the slot must be turned OFF so that the detector can not inadvertantly place a call to the controller on the second channel.

Loops That Call Two Phases (2012 Spec. cabinets) - Loops that call two phases in a 336 pole mounted cabinet require special wiring. Typically a jumper is added from the controller input pin of the first phase to a spare controller input pin of the second phase located in the same slot that the loop detector is installed.

\* Using any of these slots for purposes other than those shown here may require reassignment of inputs in the controller software and/or modification of the surge protection on the cabinet input panel.

# 2070 Input File Layout - 336

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

8.2

SHEET 2 OF 2

### INPUT FILE CONNECTION & PROGRAMMING CHART

| LOOP NO.            | LOOP<br>TERMINAL | INPUT<br>FILE POS. | PIN<br>NO. | INPUT<br>ASSIGNMENT<br>NO. | DETECTOR<br>NO. | NEMA<br>PHASE | CALL                 | EXTEND | FULL<br>TIME<br>DELAY | STRETCH<br>TIME | DELAY<br>TIME |
|---------------------|------------------|--------------------|------------|----------------------------|-----------------|---------------|----------------------|--------|-----------------------|-----------------|---------------|
| 1A 1                | TB21-1,2         | I1U                | 56         | 18                         | 1               | 1             | Υ                    | Y      |                       |                 | 15            |
| IA.                 | -                | -                  | 59         | 21                         | 15              | 6             | Y                    | Y      | Y                     |                 | 3             |
| 2A/S3               | TB21-3,4         | I2U                | 39         | 1                          | 2               | 2/SYS         | Y                    | Y      |                       |                 |               |
| 2B/S4               | TB23-3,4         | I2L                | 43         | 5                          | 12              | 2/SYS         | Y                    | Y      |                       |                 |               |
| 3A                  | TB21-5,6         | I3U                | 58         | 20                         | 3               | 3             | Y                    | Y      |                       |                 |               |
| 4A                  | TB21-7,8         | I4U                | 41         | 3                          | 4               | 4             |                      | Y      |                       | 2.8             |               |
| 4B                  | TB23-7,8         | I4L                | 45         | 7                          | 14              | 4             | Y                    | Y      |                       |                 | 15            |
| 5A                  | TB21-9,10        | I5U                | 55         | 17                         | 5               | 5             | Y                    | Y      |                       |                 |               |
| 6A                  | TB21-11,12       | I6U                | 40         | 2                          | 6               | 6             | Y                    | Y      |                       |                 |               |
| 6B                  | TB23-11,12       | I6L                | 44         | 6                          | 16              | 6             | Y                    | Y      |                       |                 |               |
| 7A                  | TB21-13,14       | I7U                | 57         | 19                         | 7               | 7             | Y                    | Y      |                       |                 | 3             |
| 8A                  | TB22-1,2         | I8U                | 42         | 4                          | 8               | 8             | Y                    | Y      |                       |                 |               |
| * S1                | TB24-1,2         | I8L                | 46         | 8                          | 18              | SYS           |                      |        |                       |                 |               |
| PED PUSH<br>BUTTONS |                  |                    |            |                            |                 |               | NOT                  |        |                       |                 |               |
| P21,P22             | TB22-9,10        | I12U               | 67         | 29                         | PED 2           | 2 PED         | INSTALL DC ISOLATORS |        |                       | S               |               |

PED 6

6 PED

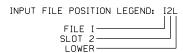
INSTALL DC ISOLATORS
IN INPUT FILE SLOTS
I12 AND I13.

\* SYSTEM DETECTOR ONLY. REMOVE THE VEHICLE PHASE ASIGNED TO THIS DETECTOR IN THE DEFAULT PROGRAMMING.



P61,P62

TB22-11,12



For 2006 Spec. cabinet:

- <sup>1</sup> Add jumper from I1-F to I1-W, on rear of input file.
- <sup>2</sup>Add jumper from I3-F to I3-W, on rear of input file.
- <sup>3</sup>Add jumper from I5-F to I5-W, on rear of input file.
- <sup>4</sup>Add jumper from I7-F to I7-W, on rear of input file.

(Include 'Wired Input' reference for Channel 2 slots where required)



For 2012 Spec. cabinet:

- ¹Add jumper from I1-F to I1-SP, on rear of input file.
- <sup>2</sup>Add jumper from I3-F to I3-SP, on rear of input file.
- <sup>3</sup>Add jumper from I5-F to I5-SP, on rear of input file.
- <sup>4</sup>Add jumper from I7-F to I7-SP, on rear of input file.

### 2070 Input File Connection & Programming Chart - 336 (Oasis)

The purpose of the Input File Connection & Programming Chart is to provide the installer with a convenient reference for connecting inductive loops and pedestrian push buttons to the cabinet as well as for programming controller detectors. The example shown at left is set up to match the example shown in the 2070 Input File Layout section (STD. No. 8.2 sheet 2, feature "F", 2012 Spec. cabinet).

The key value to each row is the input file position (third column from the left). The first six values in the row should be considered attributes of the input file position. The relationship of the input file position with a specific inductive loop (first column) is decided during the preparation of the input file layout. Also, once the input file layout is established, all rows corresponding to unused input file positions can be deleted.

The relationship of the input file position with the loop terminal and pin numbers is fixed in the cabinet hardware. Changing these values entails rewiring the cabinet and should be avoided. The relationship of the input file position with the input assignment and controller detector numbers is set in the controller software. The values shown on the start drawings are the controller defaults. Changing them is only necessary if the detector is to be reassigned to another function.

The remaining (right-most) six columns contain attributes that apply to the specific loop associated with the input file position in question. These values can be found in the '2070 Loop Detector and Installation' chart on the signal plan and should be duplicated in this chart.

### Additional Features:

- A Pedestrian Push buttons If the design utilizes pedestrian push buttons, an extension is added to the Input File Connection & Programming Chart that contains the appropriate values for those detector channels. The values in the last five columns of the main chart do not apply to pedestrian detectors. The CADD cell containing the pedestrian detectors also includes a note reminding the installer to equip the appropriate slots with a DC isolator.
- (See STD. NO. 8.2 sheet 2, a note is placed below the chart detailing which controller input pins should be jumpered together.
- © System Detector Note If a detector channel is to serve as a system detector only, this note is included to remind the installer to remove the vehicle phase assigned to that detector in the default programming.

# 2070 Input File Connection & Programming Chart – 336 (Oasis)

STD. NO.

8.3

SIGNALS MANAGEMENT
TRANSPORTATION MOBILITY AND SAFETY DIVISION
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

### INPUT FILE CONNECTION & PROGRAMMING CHART

| ① - | \     |          | E |
|-----|-------|----------|---|
| 55  | 10050 | 55755755 |   |

| LOOP NO.            | LOOP<br>TERMINAL | INPUT<br>FILE POS. | PIN<br>NO. | DETECTOR<br>NO. | NEMA<br>PHASE | CALL | EXTEND<br>TIME | DELAY<br>TIME | ADDED<br>INITIAL | DETECTOR<br>TYPE |
|---------------------|------------------|--------------------|------------|-----------------|---------------|------|----------------|---------------|------------------|------------------|
| 1A                  | TB21-1,2         | IIU                | 56         | 1               | 1             | YES  |                | 15            |                  | S                |
| 1                   | -                | -                  | 59         | 15              | 1             | YES  |                | 3             |                  | G                |
| 2A/S3               | TB21-3,4         | I2U                | 39         | 2               | 2/SYS         | YES  |                |               | Х                | N                |
| 2B/S4               | TB23-3,4         | I2L                | 43         | 12              | 2/SYS         | YES  |                |               | X                | N                |
| 3A                  | TB21-5,6         | I3U                | 58         | 3               | 3             | YES  |                |               |                  | S                |
| 4A                  | TB21-7,8         | I4U                | 41         | 4               | 4             | YES  | 2.0            |               |                  | S                |
| 4B                  | TB23-7,8         | I4L                | 45         | 14              | 4             | YES  |                | 15            |                  | S                |
| 5A                  | TB21-9,10        | I5U                | 55         | 5               | 5             | YES  |                |               |                  | S                |
| 6A                  | TB21-11,12       | I6U                | 40         | 6               | 6             | YES  |                |               | Χ                | N                |
| 6B                  | TB23-11,12       | I6L                | 44         | 16              | 6             | YES  |                |               | Х                | N                |
| 7A                  | TB21-13,14       | I7U                | 57         | 7               | 7             | YES  |                | 3             |                  | S                |
| 8A                  | TB22-1,2         | I8U                | 42         | 8               | 8             | YES  |                |               |                  | S                |
| * S1                | TB24-1,2         | I8L                | 46         | 18              | SYS           | NO   |                |               |                  | S                |
| PED PUSH<br>BUTTONS |                  |                    |            |                 |               | NOTE | :              |               |                  |                  |

2 PED

6 PED

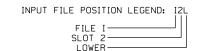
INSTALL DC ISOLATORS IN INPUT FILE SLOTS I12 AND I13.

¹Add jumper from I1-F to I1-SP, on rear of input file. —

PED 6

68

\* SYSTEM DETECTOR ONLY. REMOVE THE VEHICLE PHASE ASIGNED TO THIS DETECTOR IN THE DEFAULT PROGRAMMING.



For 2006 Spec. cabinet:

'Add jumper from I1-F to I1-W, on rear of input file.

<sup>2</sup>Add jumper from I3-F to I3-W, on rear of input file.

<sup>3</sup> Add jumper from I5-F to I5-W, on rear of input file.

<sup>4</sup>Add jumper from I7-F to I7-W, on rear of input file.

(Include 'Wired Input' reference for Channel 2 slots where required)

For 2012 Spec. cabinet:

<sup>1</sup>Add jumper from I1-F to I1-SP, on rear of input file.

<sup>2</sup>Add jumper from I3-F to I3-SP, on rear of input file.

<sup>3</sup>Add jumper from I5-F to I5-SP, on rear of input file.

<sup>4</sup>Add jumper from I7-F to I7-SP, on rear of input file.

### 2070 Input File Connection & Programming Chart - 336 (ASC/3-2070)

The purpose of the Input File Connection & Programming Chart is to provide the installer with a convenient reference for connecting inductive loops and pedestrian push buttons to the cabinet as well as for programming controller detectors. The example shown at left is set up to match the example shown in the 2070 Input File Lavout section (STD. No. 8.2 sheet 2, feature "F", 2012 Spec. cabinet).

The key value to each row is the input file position (third column from the left). The first five values in the row should be considered attributes of the input file position. The relationship of the input file position with a specific inductive loop (first column) is decided during the preparation of the input file layout. Also, once the input file layout is established, all rows corresponding to unused input file positions can be deleted.

The relationship of the input file position with the loop terminal and pin numbers is fixed in the cabinet hardware. Changing these values entails rewiring the cabinet and should be avoided. The relationship of the input file positions with the controller detector numbers is set in the controller software. The values shown on the start drawings are the controller defaults. Changing them is only necessary if the detector is to be reassigned to another function.

The remaining (right-most) five columns contain attributes that apply to the specific loop associated with the input file position in question. These values can be found in the '2070 Loop Detector and Installation' chart on the signal plan and should be duplicated in this chart.

### Additional Features:

(A) See STD. No. 8.3 Sheet 1

(B) See STD. No. 8.3 Sheet 1

© See STD. No. 8.3 Sheet 1

D See STD. No. 8.3 Sheet 1

© See STD. No. 8.3 Sheet 1

2070 Input File Connection & Programming Chart – 336 (ASC/3–2070)

SIGNALS MANAGEMENT

TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

8.3

SHEET 2 OF 2

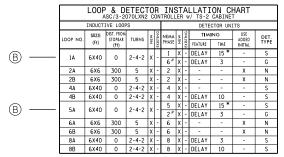
10-18

P21,P22

P61.P62

TB22-9,10

TB22-11.12



\* Reduce Delay to 3 seconds during alternate phasing operation

# Disable phase call during alternate phasing operato

### DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW. PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

|   |    | CH1<br>L3<br>Ø 2 | сн1<br>L1<br>ø 1 | сн1<br>L7<br>ø4  | сн1<br>L5<br>ø 5 | сн1<br>L 1 1<br>Ø 8 | сн1<br>L9<br>ø6  | CH1<br>L15<br>NOT | CH1<br>L13<br>NOT | CHA<br>EVP3<br>Ø1.6 | CHA<br>EVP5<br>NOT | SL     |
|---|----|------------------|------------------|------------------|------------------|---------------------|------------------|-------------------|-------------------|---------------------|--------------------|--------|
| В | IU | **               |                  |                  |                  |                     | **               | USED              | USED              |                     | USED               | 0<br>T |
|   |    | L4<br>Ø2         | L2               | сн2<br>L8<br>ø 4 | L6               | CH2<br>L12<br>Ø8    | ch2<br>L10<br>Ø6 | L16               | L14<br>NOT        | EVP4                | EVP6               | ЕМРТ   |
|   |    | **               | NOT<br>USED      | " "              | NOT<br>USED      | " 0                 | **               | NOT<br>USED       | USED              | 2,3                 | USED               | Ÿ      |

WIRE LOOPS TO TERMINALS ON LOOP PANEL AS SHO

NU L13A+L13B

NU L14A+L14B NU L15A,L15B

NU L16A,L16B NU = Not Used

| SHOWN   | IN THE CHAP             | RT BELOW TO          | ) THE | SCHEDUL    | E SHOWN  | IN THE  | CHART B    | ELOW |
|---------|-------------------------|----------------------|-------|------------|----------|---------|------------|------|
| 00P NO. | LOOP PANEL<br>TERMINALS | NOTE                 |       | ONTROLLER  | FUNCTION |         | MING       | -    |
| 1 A     | L1A,L1B                 | BE SURE TO PROGRAM   | D     | TECTOR NO. |          | FEATURE | TIME (SEC) |      |
|         |                         | DETECTOR TYPES AND   |       | 1          | ø 1      | DELAY   | 15         |      |
| NU      | L2A,L2B                 | TIMERS (EXTEND AND   |       | 2          | ·        |         |            |      |
| 2A      | L3A,L3B                 |                      | _     |            |          |         |            |      |
| 2B      | L4A,L4B                 | DELAY) AS SHOWN ON   | *     | <b>*</b> 3 | ø2       |         | 1          |      |
|         |                         | THE SIGNAL PLANS.    | *     | * 4        | ø 2      |         |            |      |
| 5A      | L5A,L5B                 |                      | - 12  |            |          |         |            |      |
| NU      | L6A,L6B                 |                      |       | 5          | ø 5      | DELAY   | 15         |      |
|         |                         | * Detector Type - G  |       | 6          |          |         |            |      |
| 4A      | L7A.L7B                 |                      | -     | 7          | ø 4      |         |            |      |
| 4B      | L8A,L8B                 | ** Detector Type - N | L     | - 1        |          |         |            |      |
|         |                         |                      |       | R          | of Δ     | DELAY   | 10         |      |

6A L9A+L9B A Logical Detector 6B L10A+L10B 8A L11A+L11B 8B L12A+L12B

| OTE<br>TO PROGRAM | CONTRO<br>DE TECTO |
|-------------------|--------------------|
| TYPES AND         | 1                  |
| EXTEND AND        | 2                  |
| S SHOWN ON        | ** 3               |
| AL PLANS.         | ** 4               |
|                   | - 5                |

| 2             |     |       |    |
|---------------|-----|-------|----|
| ** 3          | ø 2 |       |    |
| ** 4          | ø 2 |       |    |
| 5             | ø 5 | DELAY | 15 |
| 6             |     |       |    |
| 7             | ø 4 |       |    |
| 8             | ø 4 | DELAY | 10 |
| ** 9          | ø6  |       |    |
| <b>*</b> * 10 | ø6  |       |    |
| 11            | ø 8 | DELAY | 3  |
| 12            | ø 8 | DELAY | 10 |
| 13            | ø   |       |    |
| 14            | ø   |       |    |
| 15            | ø   |       |    |
| 16            | ø   |       |    |
|               |     |       |    |
| Δ * 61        | ø6  | DELAY | 3  |
| Δ * 63        | ø 2 | DELAY | 3  |

PROGRAM CONTROLLER DETECTORS ACCORDING

(A)

(D)

A NOTE: SEE LOGICAL DETECTOR PROGRAMMING SHEET 3.

### NEMA TS-2 Detector Rack Detail Including Logical Detectors (ASC/3-2070)

The purpose of the Detector Rack Detail is to provide the installer with a convenient reference for connecting inductive loops and preemption detection equipment to the cabinet as well as for programming detectors in the controller. The example shown to the left is set up to match the Loop and Detector Installation Chart shown to the left which would be found on the signal design plan. A typical NEMA TS-2 cabinet is equipped with two detector racks while only one rack is shown here.

Each detector rack accommodates eight two channel detector cards. The loop numbers and their respective positions in the rack in a TS-2 cabinet are fixed and the detector numbers assigned to each loop input are also fixed as shown in the setup detail to the left. The phases that are assigned to each loop input are flexible and may be assigned at the discretion of the electrical designer. Loop features such as DELAY time are shown in the chart as per the Loop & Detector Installation Chart from the signal plan. Other information such as detector type are indicated in the rack detail and programming chart by footnote annotation.

Some loops are multipurposed to call more than one phase. In such cases the logic processor is used to place a call to a logical vehicle detector when a vehicle is detected on a pavement loop. Such loops are clearly designated in the Loop & Detector Installation Chart on the signal design plan. In the setup to the left, a vehicle detected on L1 will place a call to Detector 1 (phase 1) and Logical Detector 61 (phase 6). A vehicle detected on L5 will place a call to Detector 5 (phase 5) and Logical Detector 63 (phase 2). As noted under the chart, the logic to place the logical detector calls can be found on the designated plan sheet.

As is typical with the multipurposed loops, but not shown here, a vehicle detected on a detector programmed for phase 3 would place a Logical Detector 62 call (phase 8) and a detector programmed for phase 7 would place a Logical Detector 64 (phase 4) call.

### Additional Features:

- (A) Loop & Detector Installation Chart from signal plan.
- (B) Multipurposed loops that call more than one phase.
- Detector Rack showing physical setup of detector cards.
- Charts showing detector programming details including virtual detectors.

NEMA TS-2 Detector Rack Detail Including Logical Detectors (ASC/3-2070)

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

8.4

SHEET 1 OF 2

### ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING FOR LOGICAL DETECTORS

(program controller as shown)

| 1. | From | Main | Menu | select | 1. | CONFIGURATION |
|----|------|------|------|--------|----|---------------|
|    |      |      |      |        |    |               |

- 2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
- 3. From the LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD. PRESS 'ENTER'. AND PROGRAM AS SHOWN.

IP#: 1 COPY FROM: 1 ACTIVE:M (T/F) IF DET 1 IS ON THEN DET SET VEH 49-64 61 ELSE

(DETECTOR 1/LOGICAL DETECTOR 61)

ENTER A "2" IN THE LP# FIELD. PRESS 'ENTER'. AND PROGRAM AS SHOWN.

LP#: 2 COPY FROM: 2 ACTIVE:M (T/F) IF DET 1 IS OFF THEN DET SET VEH 49-64 61 DFF ELSE

LOGIC FOR LOOP 1A

(DETECTOR 1/LOGICAL DETECTOR 61)

ENTER A "3" IN THE LP# FIELD. PRESS 'ENTER'. AND PROGRAM AS SHOWN.

LP#: 3 COPY FROM: 3 ACTIVE:M (T/F) IF DET 5 IS ON THEN DET SET VEH 49-64 63 ON

LOGIC FOR LOOP 5A (DETECTOR 5/LOGICAL DETECTOR 63)

ENTER A "4" IN THE LP# FIELD. PRESS 'ENTER'. AND PROGRAM AS SHOWN.

LP#: 4 COPY FROM: 4 ACTIVE:M (T/F) 5 IS OFF THEN DET SET VEH 49-64 63 OFF

FND PROGRAMMING

LOGIC FOR LOOP 5A (DETECTOR 5/LOGICAL DETECTOR 63) 1. From Main Menu select 1. CONFIGURATION

2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR

3. From the LOGIC PROCESSOR Submenu select 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENTS 1-4 BY POSITIONING THE CURSOR OVER THE FIELD SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE THEM.

LOGIC STATEMENT CONTROL 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6 LP 16-30 . . . . . . . . . . . LP 31-45 . . . . . . . . . 

END PROGRAMMING

### NEMA TS-2 Detector Rack Detail Including Logical Detectors - Logic (ASC/3-2070)

These logic processor statements are designed to enable and disable the logical detectors paired with the physical pavement loops shown in the Loop & Detector Installation Chart.

| Loops that call phase | will call | Logical Detector & Phase |
|-----------------------|-----------|--------------------------|
| 1                     |           | 61 / 6                   |
| 3                     |           | 62 / 8                   |
| 5                     |           | 63 / 2                   |
| 7                     |           | 64 / 4                   |

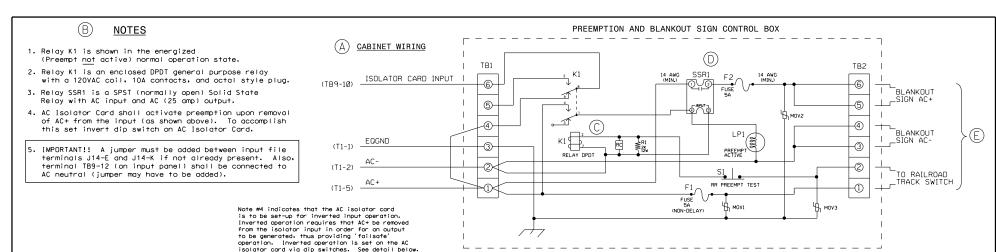
# NEMA TS-2 Detector Rack Detail Including Logical Detectors - Logic (ASC/3-2070)

SIGNALS MANAGEMENT

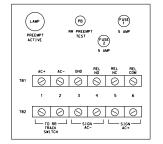
TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

8.4

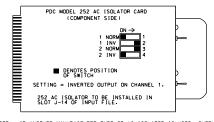
SHEET 2 OF 2



FRONT VIEW



PREEMPT 1 AC ISOLATOR (MODEL 252) OUTPUT PROGRAMMING DETAIL (set DIP switches as shown below)



NOTE: IF ANOTHER MANUFACTURER TYPE OF AC ISOLATOR IS USED, OUTPUT PROGRAMMING IS LIKELY NOT TO EQUATE TO THAT SHOWN ABOVE.

### 2070 RR Preemption and Blankout Sign Control Box

The 2070 Preemption and Blankout Sign Control Assembly/Box provides the following functionality:

- 1. Provides the interface between the railroad crossing signal equipment and the traffic signal equipment, which includes, termination points for the interconnect cable, surge protection, and termination points for blankout signs.
- Provides an output which directs the controller to begin the preemption sequence. A test switch is present to manually test this output.
- 3. Provides the control circuitry for the operation of any blankout signs required by the preemption sequence. This circuitry allows the blankout signs to operate normally, even when cabinet is in the flash mode.

Explanation of major components:

- (A) Cabinet wiring termination points Tells the installer where to make the connections in order to interface the box with the cabinet. These connections supply AC power to the box, as well as tie the preempt relay output to an AC isolator.
- ® Notes section Describes the component types and part numbers used in the box. Any special wiring instructional notes are placed here.
- © Preempt relay The coil of this relay (K1) is tied to the RR cabinet contacts which, when opened, indicate the presence of a train. When the RR contacts open, this relay de-energizes and removes AC+ from the isolator card, thus causing a preempt input to be placed to the controller. The other set of contacts on this relay cause AC+ to be applied to the input of SSR1 which illuminates any blankout signs being used.
- Blankout sign relay This relay is a SPST solid state relay which controls the illumination of the blankout signs. When this relay is activated by the preempt relay (K1), the signs will be switched "ON".
- © Field wiring termination points Tells the installer where the connections are made in order to interface the preempt box with the RR crossing signal equipment. Terminations for blankout sign AC+ and AC- are included here as well.

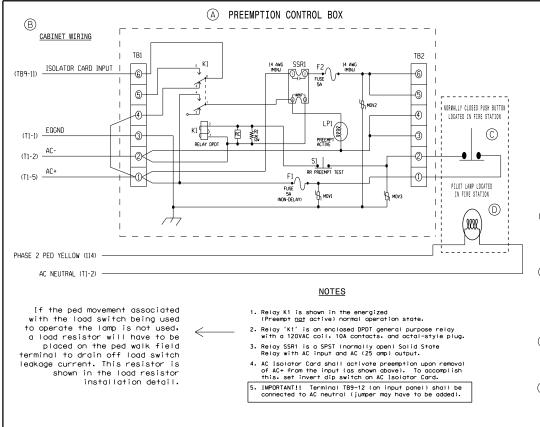
# 2070 RR and EV Preemption and Blankout Sign Control Box

SIGNALS MANAGEMENT
TRANSPORTATION MOBILITY AND SAFETY DIVISION
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

STD. NO.

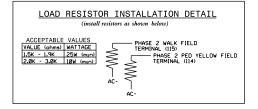
9.0

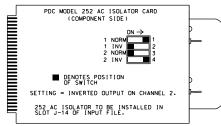
SHEET 1 OF 1



# PREEMPT 2 AC ISOLATOR (MODEL 252) OUTPUT PROGRAMMING DETAIL

(set DIP switches as shown below)





NOTE: IF ANOTHER MANUFACTURER TYPE OF AC ISOLATOR IS USED, OUTPUT PROGRAMMING IS LIKELY NOT TO EQUATE TO THAT SHOWN ABOVE.

# Emergency Vehicle Preemption Push Button And Indicator Lamp Wiring Detail

This wiring detail gives the installer the information needed to interface the controller/cabinet assembly with a firehouse push button. The function of this button is to generate a controller input to initiate the EV preemption sequence.

Usually there is also an indicator (pilot) lamp to be installed in the firehouse. The purpose of this lamp is to give the user positive feedback from the controller that the traffic signal has been preempted. The wiring for the indicator lamp is also shown on this detail.

### Major components:

- A Preemption Control Box This box essentially serves the same purpose in fire preemption applications as it does in railroad preemption applications. See STD. NO. 9.0 sheet 1 for a detailed description of the preempt control box.
- © Firehouse Push Button This is a momentary, normally closed, push button switch. The contacts of this switch are opened when the button is pressed, causing preempt to be activated.
- ① Indicator Lamp The function of this lamp is described above. This lamp is normally controlled by the yellow circuit of a pedestrian load switch. The function of the C1-pin associated with this ped yellow will have to be changed to operate this lamp correctly. A load resistor is normally tied in parallel with the lamp to drain off any induced voltage. Special programming notes are necessary to alert the installer of these changes. If delay before preempt interval is used, special logic processor programming is necessary for proper operation.
- (E) When the push button in the fire station is pressed, the preempt relay in the preempt control box de-energizes and removes AC+ from the AC isolator card. As such, the AC isolator card needs to have its switches set to the inverted position for Channel 2.

# Emergency Vehicle Preemption (Push Button Style) Wiring Detail

SIGNALS MANAGEMENT

TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

STD. NO.

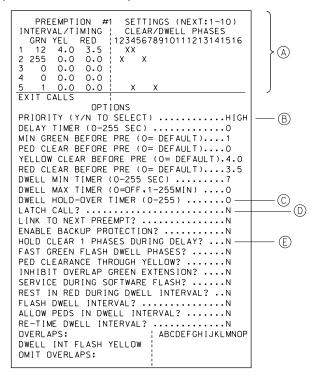
9.1

SHEET 1 OF 1

#### PREEMPTION PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS 'A' (PREEMPTION), THEN '1' (STANDARD PREEMPTION).



### 2070 Oasis Preemption Programming Detail

The image to the left is an exact duplication of the preempt programming display found on a 2070 controller running Oasis control software.

When a signal plan requires Railroad or Emergency Vehicle preemption, this detail is to be used on the electrical detail to instruct the installer on setting the different operational parameters required to operate the preempt sequence per the signal design plans.

Below is a brief description of the most commonly used features:

A Interval programming - This is the section in which interval phase selection and timing are programmed. Each interval consists of green, yellow clear, and red clear times. A section where phases are selected for each interval are positioned to the right of each set of timings. An interval time of 255 sec. is a special flag to the controller instructing it to use that interval as the "dwell" interval. The exit interval is designated when a 1 sec. green is selected following the dwell interval. Always use interval 5 as the exit interval.

Dwell interval - The dwell interval is the interval that the controller will rest in until the following two events occur:

- 1. The dwell minimum timer has expired, and
- 2. The preempt call is removed.
- (B) Priority settings There are four priority settings:
  - 1. OFF indicates the preemptor is not used.
  - 2. LOW use for low priority preempts such as transit vehicle preempts.
  - 3. MED use for emergency vehicle preempts.
  - 4. HIGH use for railroad preempts.

Railroad preempt should always be set to be the highest priority. If multiple preempts are set to the same priority, preempts will be served on a first come, first served basis.

- © Dwell hold-over timer This timer begins to time after the preempt call is removed. If this timer expires, the dwell interval will be released. If this timer does not expire before a second preempt call is received, the dwell interval will be retimed. Normally used with vehicle initiated EV preemption systems.
- ① Latch call Used in conjunction with the delay timer. The application for this feature is normally the fire house push button style of preempt. These types of preempts normally have a delay interval. This feature will allow the preempt call to latch and not release until the preempt is served.
- (E) Hold clear 1 phases during delay This feature is used in conjunction with the delay interval. If clear 1 phases are used in normal operation, and those phases just happen to be served during the delay interval, this feature will apply a hold on the clear 1 phases during the remainder of the delay interval.

(continued on next page)

## 2070 OASIS Preemption Programming Detail

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#### PREEMPTION PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS 'A' (PREEMPTION), THEN '1' (STANDARD PREEMPTION).

| PREEMPTION #1 SETTINGS (NEXT:1-10) INTERVAL/TIMING ; CLEAR/DWELL PHASES |  |
|---|--|
| GRN YEL RED   12345678910111213141516                                   |  |
| 1 12 4.0 3.5   XX   |  |
| 2 255 0.0 0.0   X X   |  |
| 3 0 0.0 0.0   |  |
| 1   |  |
| 5 1 0.0 0.0   X X   EXIT CALLS  |  |
| OPTIONS   |  |
| PRIORITY (Y/N TO SELECT)HIGH  |  |
| DELAY TIMER (0-255 SEC)   |  |
| MIN GREEN BEFORE PRE (O= DEFAULT)1                                      |  |
| PED CLEAR BEFORE PRE (O= DEFAULT)O                                      |  |
| YELLOW CLEAR BEFORE PRE (O= DEFAULT).4.0                                |  |
| RED CLEAR BEFORE PRE (0= DEFAULT)3.5                                    |  |
| DWELL MIN TIMER (0-255 SEC)7  |  |
| DWELL MAX TIMER (0=OFF,1-255MIN)0                                       |  |
| DWELL HOLD-OVER TIMER (0-255)0  |  |
| LATCH CALL?N  |  |
| LINK TO NEXT PREEMPT?N  |  |
| ENABLE BACKUP PROTECTION?N  |  |
| HOLD CLEAR 1 PHASES DURING DELAY?N                                      |  |
| FAST GREEN FLASH DWELL PHASES?N   |  |
| PED CLEARANCE THROUGH YELLOW?N  |  |
| INHIBIT OVERLAP GREEN EXTENSION?N —                                     |  |
| SERVICE DURING SOFTWARE FLASH?N —                                       | (C)  |
| REST IN RED DURING DWELL INTERVAL? N                                    | <del>                                     </del> |
| FLASH DWELL INTERVAL?N ALLOW PEDS IN DWELL INTERVAL?N                   |  |
| RE-TIME DWELL INTERVAL?   | $\perp$  |
| OVERLAPS: : ABCDEFGHIJKLMNOP  |  |
| DWELL INT FLASH YELLOW  |  |
| OMIT OVERLAPS:  | <del> </del>                                     |
|   |  |

## PREEMPT ONLY PHASE OMIT NOTE —

(program controller as shown below)

From Main Menu press '2' (Phase Control). Then '1' (Phase Control Functions). Program Phase 3 for 'Dmit Phase' and Phases 2, 4, 6, and 8 for 'Startup Calls'. This is to prevent Phase 3 from being served when not in Preempt.

### 2070 Oasis Preemption Programming Detail

- (F) Inhibit overlap green extension Affects how green extension overlaps (a.k.a. timed overlaps) transition into preemption. If a green extension overlap will not be used in the preemption, this setting is typically "YES". This will inhibit the overlap green extension from timing and allow transition to preemption to be accomplished in the quickest possible time. This is most important in RR preemption applications. If the overlap is used in the first interval of the preempt, the setting should be programmed as "NO".
- © Service during software flash This allows the controller to come out of software flash in order to serve the EV preempt.
- (H) Rest in red during dwell interval If the signal plan calls for the preempt dwell to be an all red rest state, this feature should be enabled. In addition, do not select any phases for the dwell interval.
- (1) Re-time dwell interval Used in conjunction with dwell hold-over timer. Allows the controller to re-time the dwell interval if a second preempt call is received before the hold-over timer times out. Normally used with EV preemption. Do not use this feature with railroad preemption unless there are special circumstances.
- (J) Omit overlaps This feature allows overlaps to be omitted during preemption when the overlap parents are active during preempt, but the overlap is not desired. Overlaps will return during exit interval 5.

Note: Description of features is not complete. This section is intended to address applicational use. Consult the Signal Design Section of this design manual and/or the Econolite Oasis manual for more details.

(K) In designs with a phase that is only run during preemption, e.g. a four section head with a protected left turn arrow that is only served during the preempt track clearance interval, use the note and the programming shown to the left to omit the protected turn at controller startup.

## 2070 OASIS Preemption Programming Detail

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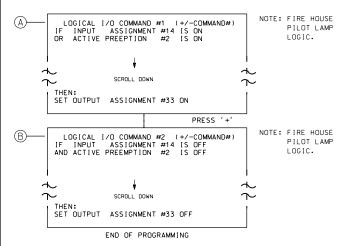
SHEET 2 OF 3

TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

### LOGICAL I/O PROCESSOR PROGRAMMING DETAIL FOR PILOT LAMP CONTROL

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL). THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1 AND 2.
- FROM MAIN MENU PRESS '6' (OUTPUTS). THEN '3' (LOGICAL I/O PROCESSOR).



OUTPUT REFERENCE SCHEDULE
USE TO INTERPRET LOGIC PROCESSOR

INPUT 14 = Preempt 2 (Firestation push button)
OUTPUT 33 = Phase 2 PED Yellow (Pilot lamp)

### 2070 Oasis Firestation Pilot Lamp Logic Programming Detail

For firestation preemption designs that utilize a push button inside the firestation with a pilot lamp, use the logic processor to turn the lamp "ON" when the button is pressed and "OFF" at the end of the preempt sequence. The example shown to the left uses PRE2 for the fire preempt and the PED 2 Yellow output to light the pilot lamp. Make sure to install load resistors as described in STD. NO. 9.1 sheet 1.

- A Firestation preempts are usually always latched calls because the push button is a momentary input and there may be delay time programmed. When the firestation push button is pressed, the preempt call is latched and both statements of command #1 will evaluate TRUE in the order they are shown. This turns on the load switch that drives the pilot lamp in the firestation.
- When the Dwell and/or Cycle intervals end, the actual preempt input (the push button) is already FALSE so both statements will evaluate FALSE, at which point the pilot lamp will turn off.

## 2070 OASIS Firestation Pilot Lamp Logic Programming Detail

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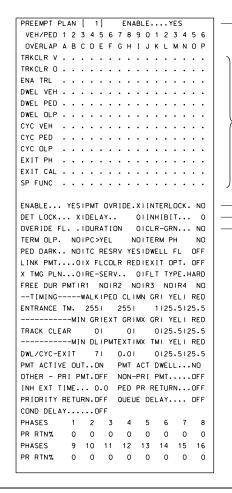
SHEET 3 OF 3

### ECONOLITE ASC/3-2070 RAILROAD PREEMPT PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 4. PREEMPTOR/TSP
- 2. From PREEMPTOR/TSP/SCP Submenu select 1. PREEMPT PLAN 1-10

Place cursor in [ ] next to Preempt Plan and press 1. Then press the right cursor grow and togale the controller to YES. Next cursor down. This will select Railroad Preemot #1.



(B)

### ASC/3-2070 Preemption Programming Detail

The image to the left is an exact duplication of the preempt programming display found on a 2070 controller running Econolite ASC/3-2070 software.

When a signal plan requires Railroad or Emergency Vehicle preemption. this detail is to be used on the electrical detail to instruct the installer on setting the different operational parameters required to operate the preempt sequence per the signal design plans.

Below is a brief description of the most commonly used features:

- (A) Preempt Plan this setting is used to select the preemptor plan as well as establish preempt priority. Preemptor 1 is the highest priority preemptor and should always be used for railroad preemption.
- (B) Interval programming these settings describe which phases run when the controller transitions into preemption, dwells, and exits. All entrance, dwell, and exit timing is shown in another section of the programming display.
  - -TRKCLR V/O are vehicle phases and overlaps that run during the track clearance interval of the preemption sequence.
  - -ENA TRL enables or disables the trailing G/Y/R overlap timing during preemption (often referred to as a timed overlap)
  - -DWEL VEH/PED/OLP are the vehicle, pedestrian, and overlaps that are first served following the TRKCLR interval of the preemption sequence.
  - -CYC VEH/PED/OLP are the vehicle, pedestrian, and overlaps that are served during the preemption sequence after the DWELL phases.
  - -EXIT PH selects the phases that the controller will exit to at the end of the DWELL and/or CYCLE intervals. The preemption sequence terminates when all exit phases are timing.
- © PMT OVRIDE When enabled allows this preemptor to override all higher numbered preemptors.
- ① DELAY The time between receipt of the preemptor call and initialization of preemption. Delay is typically used in firestation preempt applications where a push button in the firestation initiates the preemptor call a set DELAY time after the press of the button.

DET LOCK - A preemptor call is non latched when this setting is not programmed and is latched when it is programmed. When DELAY is used and a preemptor call is dropped during the DELAY period and DET LOCK is programmed, the preempt will be latched and will be serviced. This setting is typically used in tandem with DELAY in firestation preemption applications.

© OVERIDE FL - Allows the preemptor to override automatic flash and time the preemptor sequence, after which the controller returns to automatic flash.

(continued on next page)

## ASC/3-2070 Preemption Programming Detail

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SHEET 1 OF 4

# ECONOLITE ASC/3-2070 RAILROAD PREEMPT PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 4. PREEMPTOR/TSP
- 2. From PREEMPTOR/TSP/SCP Submenu select 1. PREEMPT PLAN 1-10

Place cursor in [ ] next to Preempt Plan and press 1. Then press the right cursor arrow and toggle the controller to YES. Next cursor down. This will select Railroad Preempt #1.

| PREEMPT PI | _AN  | [    | •   | ١]  |       | Е  | N/   | ۱BL  | Ε.  |     | ٠.) | ES.   | 5   |     |    | l |
|------------|------|------|-----|-----|-------|----|------|------|-----|-----|-----|-------|-----|-----|----|---|
| VEH/PED '  | 1 2  | 3    | 4   | 5   | 6     | 7  | 8    | 9    | 0   | 1   | 2   | 3     | 4   | 5   | 6  |   |
| OVERLAP A  | 4 B  | С    | D   | Ε   | F     | G  | Н    | I    | J   | K   | L   | М     | Ν   | 0   | Ρ  |   |
| TRKCLR V . |      |      |     |     |       |    |      |      |     |     |     |       |     |     |    |   |
| TRKCLR O . |      |      |     |     |       |    |      |      |     |     |     |       |     |     |    | l |
| ENA TRL .  |      |      |     |     |       |    |      |      |     |     |     |       |     |     |    | l |
| DWEL VEH . |      |      |     |     |       |    |      |      |     |     |     |       |     |     |    | l |
| DWEL PED . |      |      |     |     |       |    |      |      |     |     |     |       |     |     |    | l |
| DWEL OLP . |      |      |     |     |       |    |      |      |     |     |     |       |     |     |    | l |
| CYC VEH .  |      |      |     |     |       |    |      |      |     |     |     |       |     |     |    | l |
| CYC PED .  |      |      |     |     |       |    |      |      |     |     |     |       |     |     |    | l |
| CYC OLP .  |      |      |     | •   |       |    | •    | •    |     |     | •   |       |     |     |    | l |
| EXIT PH .  |      |      |     | •   |       |    | •    | •    |     |     | •   | •     |     |     |    | l |
| EXIT CAL . |      |      |     | •   |       |    | ٠    | •    | •   |     | •   | ٠     | •   |     |    | l |
| SP FUNC .  |      |      |     | •   |       |    | •    | •    |     |     | •   | •     |     |     | •  | l |
|            |      |      |     |     |       |    |      |      |     |     |     |       |     |     |    | l |
| ENABLE     | YE:  | SIF  | PM1 | ٦ ( | )VF   |    |      |      |     |     |     |       |     |     |    | l |
| DET LOCK.  | >    | KIE  | DEL | A١  | ٠.    |    |      | 0    | ۱١  | NH. | В   | Τ.    |     |     | 0  | l |
| OVERIDE FL |      | . 10 | DUF | RAT | IC    | N  |      | 0 1  | CL  | R-  | -GF | RN.   |     | . 1 | 10 | l |
| TERM OLP.  | N    | DIF  | C:  | >YE | L     |    | ١    | 10 1 | TE  | R   | / F | Н     |     | ١   | 10 | l |
| PED DARK.  | N(   | ווכ  | ГС  | RE  | SF    | ٧۶ | ΥE   | S    | D۷  | ۷EL | .L  | F٤    |     | OF  | F  | l |
| LINK PMT.  |      |      |     |     |       |    |      |      |     |     |     |       |     |     |    | l |
| X TMG PLN. |      |      |     |     |       |    |      |      |     |     |     |       |     |     |    | l |
| FREE DUR F | РМТ  | IR1  | 1   | NC  | ) I F | ₹2 | ١    | 10   | R.  | 3   | NC  | ) I F | ₹4  | ١   | 10 | l |
| TIMING-    |      |      |     |     |       |    |      |      |     |     |     |       |     |     |    | l |
| ENTRANCE ' |      |      |     |     |       |    |      |      |     |     |     |       |     |     |    | l |
|            | M    | ΙN   | GF  | RIE | X1    | (  | GR I | (M   | ( ( | SR  | 1   | ΈL    | . 1 | RE  | D  | l |
| TRACK CLE  |      |      |     |     |       |    |      |      |     |     |     |       |     |     |    | l |
|            |      |      |     |     |       |    |      |      |     |     |     |       |     |     |    | l |
| DWL/CYC-E  |      |      |     |     |       |    |      |      |     |     |     |       |     |     |    | l |
| PMT ACTIVE |      |      |     |     |       | PN | ıΤ   | AC   | T   | D١  | ۷EL | L.    | • • | N(  | )  | l |
| OTHER - PF | RI E | PMT  | Γ.( | )FF |       | NO | ON-  | -PF  | Ι   | ΡM  | ۸T. | ٠.    | • • | OF  | F  | l |
| INH EXT T  | I ME |      | . ( | ).( | )     | PE | D    | PF   | R F | ŖΕ. | TUF | ١N۶.  | • • | OF  | F  | l |
| PRIORITY F |      |      |     |     |       | QL | JΕL  | JΕ   | DE  | L   | ۱Y. | ٠.    | •   | OF  | F  |   |
| COND DELA  | Y    | ٠.,  | (   | )FF |       |    |      |      |     |     |     |       |     |     |    |   |
| l          | 1    |      |     |     |       |    | 4    |      | 5   |     | 6   |       | 7   |     | 8  |   |
| PR RTN%    |      |      |     |     |       |    | 0    |      |     |     | 0   |       | 0   |     | 0  |   |
| PHASES     | 9    | 1    | 0 ا | 1   | 1     | 1  | 2    | 1    | 3   | •   | 4   | 1     | 5   | 1   | 16 |   |
| PR RTN%    | 0    |      | 0   |     | 0     |    | 0    |      | 0   |     | 0   |       | 0   |     | 0  |   |
|            |      |      |     |     |       |    |      |      |     |     |     |       |     |     |    |   |





### ASC/3-2070 Preemption Programming Detail (continued)

F TERM OLP (ASAP) - Forces overlaps to terminate immediately with their included phases and ignore any existing Lagging Overlap programming.

PC>YEL - Allows the Yellow Change indication to time with the completion of Pedestrian Clearance interval.

TERM PH - Terminate all timing phases and force an All Red condition before starting the activated preempt. Phases will NOT be terminated if the current Green phases exactly match the preempt's entry phase(s) and a yellow trap will not be caused for conflicting PPLT overlap programming.

- © TC RESRV Allows the preemptor to reservice the track clearance phases when the preemption call goes away and returns before the preemption sequences terminate. When enabled, the PREEMPTION EXTEND option is disabled.
- $\stackrel{\textstyle (f)}{\textstyle (f)}$  Timing Parameters these settings describe the controller operation as it transitions from normal operation into preemption, dwells, and then exits from preemption back to normal operation.
  - -ENTRANCE TM. describes the minimum green, pedestrian walk and clear, and yellow and red clearance times for the phases currently timing when the preemptor receives a call and transitions from normal operation into preemption. The values 25.5 and 255 are special values used by the controller that allow the phase minimum times of the phases currently running to be timed by the controller. There is no way for the phase indication time to be larger than their programming when entering preemption any time these values are used.
  - -TRACK CLEAR times are used for the track clearance phases and time after the entrance timing. Programming clearance times to 25.5 allows the phase minimum times to be used.
  - -DWL/CYC-EXIT times determine the minimum dwell, preempt extend, max preempt time, and preempt exit clear times. The controller will serve any programmed dwell phases before serving cycle phases. Programming clearance times to 25.5 allows the phase minimum times to be used. Preemption advances to the exit sequence when the preempt input is removed and the preempt sequence is no longer active once all exit phases are timing.
  - -MX TM is the Maximum Time that a preemption call can be active and be recognized by the controller. When it has failed, the input must return to inactive state to be recognized again. Zero entry disables the function for the associated preempt. NOTE: this feature is disabled if the preemptor is NOT called by a low priority input so the Preempt Filtering cell should be added to plans where a maximum preempt time is a desired function.

## ASC/3-2070 Preemption Programming Detail

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SHEET 2 OF 4

# ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING DETAIL FOR PREEMPT ONLY PHASE OMIT

(program controller as shown)

1. From Main Menu select 1. CONFIGURATION

2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR

3. From the LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD. PRESS 'ENTER'. AND PROGRAM AS SHOWN.

LP#: 1 COPY FROM: 1 ACTIVE: M (T/F)
IF PMT PREEMPT ACTIVE 1 IS OFF
THEN CTR OMIT PHASE 3 ON
ELSE

OMITS PHASE 3 DURING NORMAL OPERATION AND ALLOWS PHASE 3 TO RUN WHEN PREEMPT 1 IS ACTIVE

END PROGRAMMING

- 1. From Main Menu select 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
- 3. From the LOGIC PROCESSOR Submenu select 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENT 1 BY POSITIONING THE CURSOR OVER THE FIELD SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE IT.

END PROGRAMMING

## ASC/3-2070 Preempt Only Phase Omit Programming Detail

In designs with a phase that is only run during preemption, ASC/3-2070 uses the logic processor to omit that particular phase when the preempt is not active. The example shown indicates phase 3 is omitted during normal operation and is allowed to run when preempt 1 (typically a railroad preempt) is active. The preempt number and the phase number in the logic statement should be edited to match the signal plan.

ASC/3-2070 Preempt Only Phase Omit Programming Detail

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SHEET 3 OF 4

# ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING DETAIL FOR PILOT LAMP CONTROL

(program controller as shown)

- 1. From Main Menu select 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
- 3. From the LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD. PRESS 'ENTER'. AND PROGRAM AS SHOWN.

NOTE: FIRE HOUSE PILOT LAMP LOGIC

ENTER A "2" IN THE LP# FIELD. PRESS 'ENTER'. AND PROGRAM AS SHOWN.

LP#: 2 COPY FROM: 2 ACTIVE: M (T/F)

IF PMT INPUT 2 IS OFF

AND PMT PREEMPT ACTIVE 2 IS OFF

THEN SIG SET PH PED CLR 2 OFF

ELSE

NOTE: FIRE HOUSE PILOT LAMP LOGIC

END PROGRAMMING

- 1. From Main Menu select 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
- 3. From the LOGIC PROCESSOR Submenu select | 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENTS 1 AND 2 BY POSITIONING THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE THEM.

END PROGRAMMING

## ASC/3-2070 Pilot Lamp Logic Programming Detail

For firestation preemption designs that utilize a push button inside the firestation with a pilot lamp, use the logic processor to turn the lamp "ON" when the button is pressed and "OFF" at the end of the preempt sequence. The example shown to the left uses PRE2 for the fire preempt and the PED 2 Yellow output to light the pilot lamp. Make sure to install load resistors as described in STD. NO. 9.1 sheet 1.

ASC/3-2070 Pilot Lamp Logic Programming Detail

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SHEET 4 OF 4

#### Wireless Emergency Vehicle Preemption (EVP) Push Button Detail

The detail shown on STD. NO. 9.4 sheet 2 gives the installer the information needed to interface the controller/cabinet assembly wirelessly via a multi-contact closure radio with a firehouse push button. The function of the push button is to generate a controller input that initiates an EV preemption sequence.

Typically there is also an indicator (pilot) lamp to be installed in the firehouse used to give the user positive feedback from the controller that the traffic signal has been preempted, but in this application a front panel lamp on the firehouse radio serves as the pilot lamp. The unused ped yellow output of a pedestrian loadswitch is used to energize a relay which sends a confirmation signal back to the firehouse radio for the duration of the preemption interval.

For the wireless emergency vehicle pushbutton preempt shown on the next sheet, preempt 3 is used as the controller preempt input and the cabinet is assumed to be a 332 base mount. The ped yellow output from ped loadswitch 4 (S6) is used to energize the relay for the confirmation signal for the pilot lamp in the firehouse. The radios that are used are capable of transmitting and receiving and have multiple contact closures. The notes shown on the detail contain additional information about the relay and the DC isolator used for the EV preemption application.

# Emergency Vehicle Preemption (Push Button Wireless) Detail

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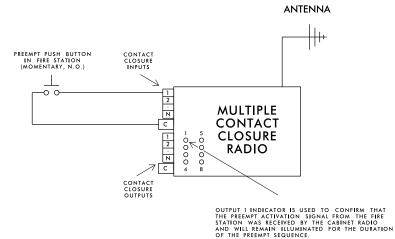
STD. NO.

9.4

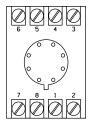
SHEET 1 OF 2

Relay K1 is de-energized under normal operation and is used to activate Input 1 to send a confirmation signal back to the fire station unit during EVP3.

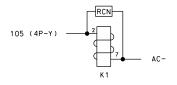
### FIRE STATION

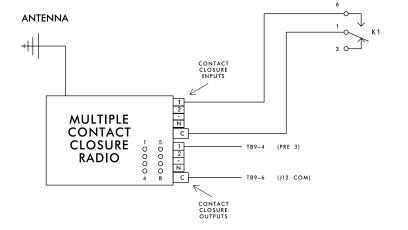


RELAY OCTAL BASE TERMINAL LOCATIONS









#### NOTES:

- Relay K1 is an enclosed SPDT general purpose relay with a 120 VAC coil. 10A contacts, and octal style plug (DOT NO. 625028600).
- The RC Network is valued at 0.1 microfarad, 100 ohm (DOT ND, 106018075).
- 3. Make sure load switch S6 is installed.
- 4. Install DC Isolator in Input File slot J12. See Input File Position Layout on sheet 1.

# Emergency Vehicle Preemption (Push Button Wireless) Detail

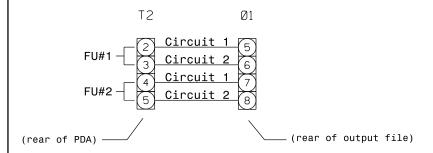
SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

9.4

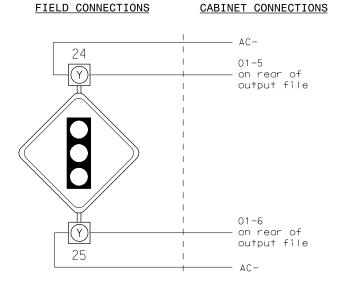
SHEET 2 OF 2

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|-------|--|----------|
|       |  |          |
|       |  | OTD NO   |
| 10-18 | SIGNALS MANAGEMENT<br>TRANSPORTATION MOBILITY AND SAFETY DIVISION<br>NORTH CAROLINA DEPARTMENT OF TRANSPORTATION | STD. NO. |

## CABINET FLASHER OUTPUTS — (A)



## WIG WAG ADVANCE BEACON —®



### Advance Beacons - Continuous Flash

ADVANCE BEACONS that flash continuously can be wired directly to the flasher unit outputs in the cabinet. Flasher outputs are wired from the power distribution assembly to the output file. Each flasher unit has two circuits, each of which flashes 180 degrees out of phase with the other. Single flashing beacons, side by side, or WIG WAG type beacons can all be wired directly to the cabinet flasher outputs. This type of ADVANCE BEACONS will continue to flash even when the cabinet is in flash.

- (A) Cabinet flasher terminal block output reference.
- WIG WAG ADVANCE BEACON This diagram illustrates a beacon that has two heads that flash out of phase with each other. One head flashes with flasher unit #1 circuit #1, and the other head flashes with flasher unit #1 circuit #2. To maintain proper phasing, it is important that a WIG WAG flasher use the outputs of the same flasher unit.

Advance Beacons – Continuous Flash

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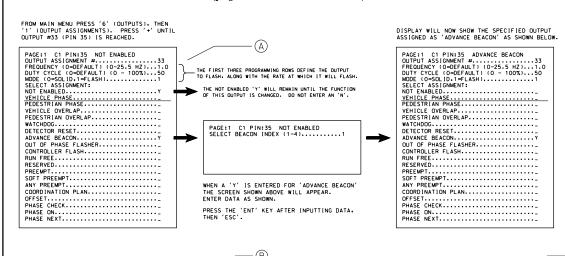
STD. NO.

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SHEET 1 OF 1

# ASSIGNMENT PROGRAMMING DETAIL

(program controller as shown below)



# Oasis Single Programmable/Actuated Advance Beacons

Any output may be programmed as an ADVANCE BEACON and programmed to flash at variable frequencies and duty cycles when turned on. The unused ped yellow load switch outputs are typically chosen for advance beacon outputs and most often turned on and off by the logic processor.

ADVANCE BEACON OUTPUT PROGRAMMING - The four normally unused ped yellow outputs are typically chosen first for ADVANCE BEACON outputs. Their outputs are set up to flash at 1Hz with a 50% duty cycle when turned on. The ADVANCE BEACON is assigned a unique index number from 1-4 as shown.

## TYPICAL ADVANCE BEACON WIRING DETAIL (FOR PHASE 2 APPROACH)

CABINET CONNECTIONS

(wire flasher as shown below)

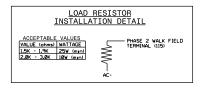


#### IMPORTANT

- 1. REMOVE, TAPE AND LABEL CONFLICT MONITOR WIRE ATTACHED TO THE REAR OF TERMINAL 114 (2PY).
- 2. INSTALL LOAD SWITCH IN OUTPUT FILE SLOT S3.

FIELD CONNECTIONS

- 3. MAKE SURE LOAD RESISTOR IS IN PLACE AS SHOWN IN LOAD RESISTOR INSTALLATION DETAIL ON THIS SHEET.
- 4. TO ACTIVATE ADVANCE BEACON OPERATION AS INDICATED ON THE SIGNAL PLAN, REASSIGN OUTPUT 33 AS SHOWN ON THIS SHEET.



#### ADVANCE BEACON PROGRAMMING DETAIL

(program controller as shown below)

 FROM MAIN MENU PRESS '6' (OUTPUTS). THEN '2' (OUTPUT BEACON SETTINGS).

|                               | OUTPUT BEACON SETTINGS TRIGGER PHASES: :12345678910111213141516 BEACON #1 OFF; BEACON #2 OFF; BEACON #3 OFF; BEACON #4 OFF; BEACON #4 OFF; BEACON #4 OFF; BEACON #4 OFF; BEACON #5 OFF; BEACON #5 OFF; BEACON #6 OFF; BE |
|-------------------------------|--|
| SCROLL<br>DOWN TO<br>VIEW ALL | ON DELAY TIME (0-255); 0 0 0 0 0   STOP-TIME HOLD (0-255); 0 0 0 0   |
| DATA                          | ADVANCE BEACON PROGRAMMING COMPLETE  |

NOTE: AN OUTPUT HAS TO BE ASSIGNED AS AN ADVANCE BEACON IN ORDER FOR PROPER OPERATION TO OCCUR. SEE OUTPUT ASSIGNMENT DETAIL ON SHEET X.

- (B) ADVANCE BEACON WIRING When using a ped yellow load switch output to drive a beacon, a load resistor must be used on the ped green output to prevent a conflict on the monitor. The wire that connects the yellow signal to the conflict monitor must also be disconnected.
- © ADVANCE BEACON PROGRAMMING A typical Advance Beacon is controller by a trigger phase. The beacon will be "OFF" whenever the trigger phase is not in its green interval.

## Oasis Advance Beacons - Single Programmable /Actuated

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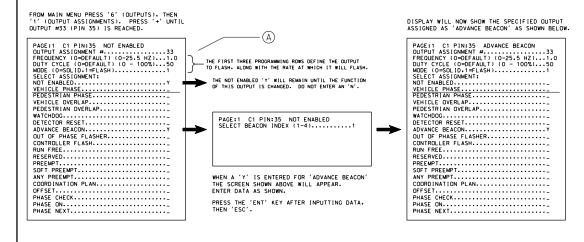
STD. NO.

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SHEET 1 OF 5

# ADVANCE BEACON OUTPUT ASSIGNMENT PROGRAMMING DETAIL

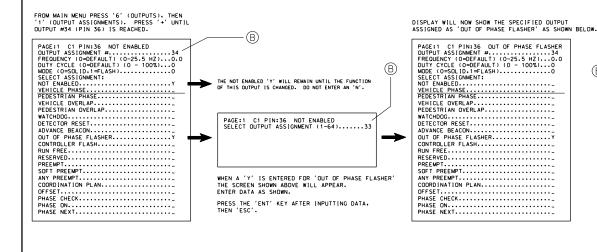
(program controller as shown below)



# Oasis Wig Wag Programmable/Actuated Advance Beacons

Any output may be programmed as an ADVANCE BEACON and programmed to flash at variable frequencies and duty cycles when turned on. Any output may be programmed as an OUT OF PHASE FLASHER and programmed to flash 180 degrees out of phase with an ADVANCE BEACON. Unused ped yellow load switch outputs are typically used for this application and WIG WAG beacons that are actuated by pavements loops and ancillary equipment are the most common application.

ADVANCE BEACON OUTPUT PROGRAMMING - The four normally unused ped yellow outputs are typically chosen first for ADVANCE BEACON outputs. Their outputs are set up to flash at 1Hz with a 50% duty cycle when turned on.



® OUT OF PHASE FLASHER - An output may be programmed to flash 180 degrees out of phase with an existing output that has has already been programmed to flash its output. Typically the OUT OF PHASE FLASHER will be paired with an ADVANCE BEACON. The actual output that the OUT OF PHASE FLASHER is to be paired with must be specified in the programming. Unused ped yellow load switch outputs are typically used for OUT OF PHASE FLASHER outputs.

(continued on next page)

# Oasis Advance Beacons – Wig Wag (Out Of Phase) Programmable /Actuated

10.1

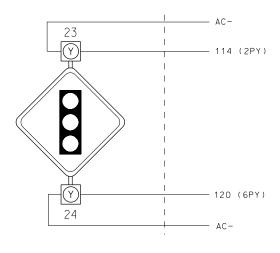
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SHEET 2 OF 5

## ADVANCE BEACON WIRING DETAIL (FOR PHASE 2 APPROACH)

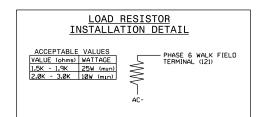
(wire flashers as shown below)

#### FIELD CONNECTIONS CABINET CONNECTIONS



#### IMPORTANT

- 1. REMOVE, TAPE AND LABEL CONFLICT MONITOR WIRE ATTACHED TO THE REAR OF TERMINAL 114 (2PY) AND TERMINAL 120 (6PY).
- 2. INSTALL LOAD SWITCHES IN OUTPUT FILE SLOTS S3 AND S9.
- 3. MAKE SURE LOAD RESISTORS ARE IN PLACE AS SHOWN IN LOAD RESISTOR INSTALLATION DETAIL ON THIS SHEET.
- 4. TO ACTIVATE ADVANCE BEACON OPERATION AS INDICATED ON THE SIGNAL PLAN, REASSIGN OUTPUTS 33 AND 34 AS SHOWN ON THIS SHEET.



# Oasis Wig Wag Programmable/Actuated Advance Beacons

- ADVANCE BEACON WIRING For a typical WIG WAG ADVANCE BEACON application, two unused ped yellow load switch outputs are used to drive the beacons. One beacon is programmed as an ADVANCE BEACON while the other is set up to flash as an OUT OF PHASE FLASHER. To prevent conflicts, load resistors and wiring modifications must be made in the cabinet as shown to the left.
- MOOK-UP CHART WITH ADVANCE BEACON For any location where an ADVANCE BEACON is deployed, the signal head hook-up chart will show which load switches drive the beacon signal heads and other pertinent installation requirements.

(D) —

|                       |    | SI    | GNA         | AL I              | HEA | D H   | 100         | (-U | P C   | HAR | Т                 |     |       |          |
|-----------------------|----|-------|-------------|-------------------|-----|-------|-------------|-----|-------|-----|-------------------|-----|-------|----------|
| LOAD<br>SWITCH NO.    | SI | S2    | s           | 3                 | S4  | S5    | S6          | S7  | S8    | s   | 9                 | S10 | S11   | S12      |
| CMU<br>CHANNEL<br>NO. | 1  | 2     | 1           | 3                 | 3   | 4     | 14          | 5   | 6     | 1   | 5                 | 7   | 8     | 16       |
| PHASE                 | 1  | 2     | PED         | ADVANCE<br>BEACON | 3   | 4     | 4<br>PED    | 5   | 6     | PED | advance<br>Beacon | 7   | 8     | 8<br>PED |
| SIGNAL<br>HEAD NO.    | NU | 21,22 | P21.<br>P22 | 23                | NU  | 41,42 | P41.<br>P42 | NU  | 61,62 | NU  | 24                | NU  | 81,82 | NU       |
| RED                   |    | 128   |             |                   |     | 101   |             |     | 134   |     |                   |     | 107   |          |
| YELLOW                |    | 129   |             |                   |     | 102   |             |     | 135   |     |                   |     | 108   |          |
| GREEN                 |    | 130   |             |                   |     | 103   |             |     | 136   |     |                   |     | 109   |          |
| RED<br>ARROW          |    |       |             |                   |     |       |             |     |       |     |                   |     |       |          |
| YELLOW<br>ARROW       |    |       |             |                   |     |       |             |     |       |     |                   |     |       |          |
| GREEN<br>ARROW        |    |       |             |                   |     |       |             |     |       |     |                   |     |       |          |
| ₩                     |    |       | 113         |                   |     |       | 104         |     |       |     |                   |     |       |          |
| PED<br>YELLOW         |    |       |             | **<br>114         |     |       |             |     |       |     | **<br>120         |     |       |          |
| Χ̈́                   |    |       | 115         |                   |     |       | 106         |     |       | *   |                   |     |       |          |

NU = Not Used

- \* Denotes install load resistor. See load resistor installation detail on sheet x.
- \*\* Special advance beacons will be wired to S3-Y and S9-Y. See wiring and programming details on sheets x and y of this electrical detail.

# Oasis Advance Beacons - Wig Wag (Out Of Phase) Programmable /Actuated

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STD. NO.

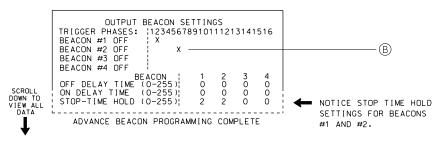
10.1

SHEET 3 OF 5

# ADVANCE BEACON PROGRAMMING DETAIL FOR STOP TIME HOLD

(program controller as shown below)

 FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '2' (OUTPUT BEACON SETTINGS).



NOTE: AN OUTPUT HAS TO BE ASSIGNED AS AN ADVANCE BEACON IN ORDER FOR PROPER OPERATION TO OCCUR. SEE OUTPUT ASSIGNMENT DETAIL ON SHEET X.

# Oasis Wig Wag Advance Beacons With Stop Time Hold

- A STOP TIME HOLD ADVANCE BEACONS are typically programmed to turn "ON" and "OFF" using trigger phases. The beacon will be "OFF" when the trigger phase is green. Some signal designs also specify that the beacon turn "ON" a specified amount of time prior to the end of green on a specified phase. The STOP-TIME HOLD setting specifies the interval that the beacon will turn back "ON" prior to the trigger phase turning yellow. The interval is served as additional green time given to the trigger phase beyond the gap or max termination point. In the illustration to the left, the beacon will turn "ON" two seconds prior to the end of phase 2 green and will flash until the time at which phase 2 turns green again.
- B STOP TIME HOLD FOR DUMMY BEACON In a situation where there are opposing approaches and one is equipped with an ADVANCE BEACON that is programmed with STOP-TIME HOLD and the other does not have a beacon, it is necessary to create a dummy ADVANCE BEACON with the same amount of STOP-TIME HOLD on the approach with no beacon. Without the dummy beacon, the approach would clear while the opposing move would be held green for the duration of STOP-TIME HOLD which could create a yellow trap. The dummy programming ensures the two approaches clear simultaneously.

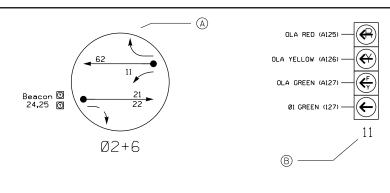
(continued on next page)

# Oasis Advance Beacons - Wig Wag (Out Of Phase) With Stop Time Hold

10.1

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NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

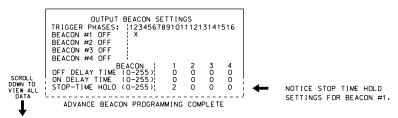
SHEET 4 OF 5



# ADVANCE BEACON PROGRAMMING DETAIL FOR STOP TIME HOLD

(program controller as shown below)

 FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '2' (OUTPUT BEACON SETTINGS).

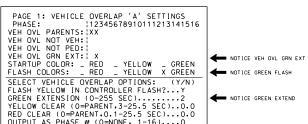


NOTE: AN OUTPUT HAS TO BE ASSIGNED AS AN ADVANCE BEACON IN ORDER FOR PROPER OPERATION TO OCCUR. SEE OUTPUT ASSIGNMENT DETAIL ON SHEET X.

#### OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING COMPLETE

# Oasis Wig Wag Advance Beacons With Stop Time Hold and FYAs

Special consideration must be taken into account when a three or four section flashing yellow arrow opposes a beacon that has stop time hold programmed. Because the flashing yellow arrow has the opposing through movement as a parent, it will clear at the end of the opposing green while the controller turns on the beacon and times the stop time hold. This means the flashing yellow arrow clears while the opposing through movement stays green, and this creates a yellow trap. To eliminate the yellow trap, green extension time equal to the stop time hold time has to be added to the flashing yellow arrow overlap programming to hold the flashing yellow arrow on for the same duration as its parent.

- (A) Signal design example showing an ADVANCE BEACON with two WIG WAG heads on one approach where the opposing approach has a four section flashing yellow arrow.
- ⑤ Four section flashing yellow arrow with typical overlap and protected turn output assignments.
- © ADVANCE BEACON programming detail with a phase 2 trigger phase and two seconds of stop time hold programmed.
- ① Overlap programming showing the green extension time for overlap A equal to the stop time hold specified for beacon #1. This means at the end of green when the controller starts flashing the beacon and extends the green time of the trigger phase, the overlap will begin timing its green extension which keeps the flashing yellow arrow flashing for the same amount of time before clearing. The 'VEH OVL GRN EXT' parameter is the phase that starts the green extension timer at the end of green for the specified phase. Typically only the opposing through move parent phase extends green for a four section FYA signal head because the protected turn is often allowed to lag. If the protected turn lags and the through move is not programmed exclusively to start the green extension timer, each of the parent phases could start the timer and cause it to time twice, which is unnecessary.

# Oasis Advance Beacons - Wig Wag (Out Of Phase) With Stop Time Hold & FYAs

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STD. NO.

10.1

SHEET 5 OF 5



# TYPICAL ADVANCE BEACON WIRING DETAIL (FOR PHASE 2 APPROACH)

(wire flasher as shown below)

#### FIELD CONNECTIONS

CABINET CONNECTIONS



#### IMPORTANT

- 1. REMOVE, TAPE AND LABEL CONFLICT MONITOR WIRE ATTACHED TO THE REAR OF TERMINAL 114 (2PY).
- 2. INSTALL LOAD SWITCH IN OUTPUT FILE SLOT S3.
- 3. MAKE SURE LOAD RESISTOR IS IN PLACE AS SHOWN IN LOAD RESISTOR INSTALLATION DETAIL ON THIS SHEET.

#### LOAD RESISTOR INSTALLATION DETAIL

| ACCEPTABLE VALUES | VALUE (ohms) | WATTAGE | 1.5K - 1.9K | 25W (mjn) | 2.0K - 3.0K | 10W (mjn) |



B

# ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING TO TURN ON ADVANCE BEACON

(program controller as shown)

- 1. From Main Menu select 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
- 3. From the LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD. PRESS 'ENTER'. AND PROGRAM AS SHOWN.

END PROGRAMMING

FLASH ADVANCE BEACON 23 AS LONG AS PHASE 2 GREEN IS OFF AND THE CONTROLLER IS NOT IN FLASH.

COB CODE 544 = Automatic (Remote) Flash COB CODE 546 = Flashing Logic 1Hz

# ASC/3-2070 Single Programmable/Actuated Advance Beacons

Any output may be used to drive an ADVANCE BEACON. The unused ped yellow load switch outputs are typically chosen for ADVANCE BEACON outputs and are most often turned on and off by the logic processor. The example illustrated on this page turns ADVANCE BEACON 23 "ON" whenever phase 2 is not green.

- ADVANCE BEACON WIRING When using a ped yellow load switch output to drive a beacon, a load resistor must be used on the ped green output to prevent a conflict on the monitor. The wire that connects the yellow signal to the monitor must also be disconnected.
- B LOGIC PROCESSOR PROGRAMMING The controller tests to see that phase 2 is not green and the controller is not in flash. When these two statements are true, the beacon will begin to flash on and off at 1Hz with a 50% duty cycle.
  - 1. From Main Menu select 1. CONFIGURATION
  - 2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
  - 3. From the LOGIC PROCESSOR Submenu select 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENT 1 BY POSITIONING THE CURSOR OVER THE FIELD SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE IT.

END PROGRAMMING

# ASC/3-2070 Beacons - Single Programmable /Actuated

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10.2

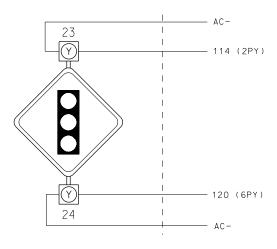
SHEET 1 OF 8

## ADVANCE BEACON WIRING DETAIL (FOR PHASE 2 APPROACH)

(A)

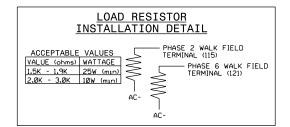
(wire flashers as shown below)

### FIELD CONNECTIONS CABINET CONNECTIONS



#### IMPORTANT

- 1. REMOVE, TAPE AND LABEL CONFLICT MONITOR WIRE ATTACHED TO THE REAR OF TERMINAL 114 (2PY) AND TERMINAL 120 (6PY).
- 2. INSTALL LOAD SWITCHES IN OUTPUT FILE SLOTS S3 AND S9.
- 3. MAKE SURE LOAD RESISTORS ARE IN PLACE AS SHOWN IN LOAD RESISTOR INSTALLATION DETAIL ON THIS SHEET.



# ASC/3-2070 Wig Wag Programmable/Actuated Advance Beacons

Any output may be used to drive an ADVANCE BEACON. The unused ped yellow load switch outputs are typically chosen for ADVANCE BEACON outputs and are most often turned on and off by the logic processor. The example illustrated on this page turns on ADVANCE BEACONS 23 and 24 in a WIG WAG pattern whenever phase 2 is not green by driving them with two different ped yellow load switch outputs.

ADVANCE BEACON WIRING - When using a ped yellow load switch output to drive a beacon, a load resistor must be used on the ped green output to prevent a conflict on the monitor. The wire that connects the yellow signal to the monitor must also be disconnected.

(continued on next page)

## ASC/3-2070 Beacons - Wig Wag Programmable /Actuated

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10.2

SHEET 2 OF 8

# ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING TO TURN ON ADVANCE BEACON

(program controller as shown)

- 1. From Main Menu select 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
- 3. From the LOGIC PROCESSOR Submenu select | 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#: 1 COPY FROM: 1 ACTIVE: M (T/F)

IF VEH GREEN ON PH 2 IS OFF

AND LP COB CODE ON 546

AND LP COB CODE OFF 544

THEN SIG SET PH PED CLR 2 ON

ELSE SIG SET PH PED CLR 2 OFF

FLASHES ADVANCE BEACON 23 AS LONG AS PHASE 2 GREEN IS OFF AND THE CONTROLLER IS NOT IN FLASH.

ENTER A "2" IN THE LP# FIELD. PRESS 'ENTER'. AND PROGRAM AS SHOWN.

FLASHES ADVANCE BEACON 24 AS LONG AS PHASE 2 GREEN IS OFF AND THE CONTROLLER IS NOT IN FLASH.

END PROGRAMMING

- 1. From Main Menu select 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select | 8. LOGIC PROCESSOR
- 3. From the LOGIC PROCESSOR Submenu select 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENTS 1 AND 2 BY POSITIONING THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE THEM.

END PROGRAMMING

# ASC/3-2070 Wig Wag Programmable/Actuated Advance Beacons

B LOGIC PROCESSOR PROGRAMMING - The controller tests to see that phase 2 is not green and the controller is not in flash. When these two statements are true, the beacons will begin to flash on and off at 1Hz with a 50% duty cycle in a WIG WAG pattern.

ASC/3–2070 Beacons – Wig Wag Programmable /Actuated

COB CODE 544 = Automatic (Remote) Flash
COB CODE 546 = Flashing Logic 1Hz

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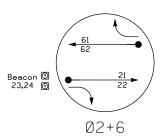
10.2

SHEET 3 OF 8

### ASC/3-2070 Wig Wag Programmable/Actuated Before End Of Green Advance Beacons

Some vehicle approaches with Advance Beacons require the beacons to begin flashing before the end of the vehicle green. This is accomplished using timed overlaps with extended green time (LAG GRN) and logic. For this discussion assume the example in the paragraph below is a continuation of the of the Advance Wig Wag beacon example explained on sheets 2 and 3 of STD. NO. 10.2 with the advance wig wag beacons on the phase 2 approach.

The signal plan will have a note stating something to the effect "flash beacons 3 seconds before the end of green". To accomplish this the load switch driving the signal heads on the phase 2 approach will have to be reassigned as an overlap. That overlap will then require its parent be assigned to the same approach as the beacon (phase 2 in this case) and will have to have LAG GRN, YEL, and RED times added. The LAG X PH (the phase that starts the LAG GRN timer when it clears from green to yellow) will be assigned to phase 2. To make the beacon turn on the requisite time before the end of green, LAG GRN must equal the time specified by the signal plan that the beacon should turn on before the end of green. The YEL and RED times in the overlap programming should equal that shown in the timing chart on the signal plan for phase 2. The logic to flash the beacons is the same as that shown on sheet 3 of STD. NO. 10.2



# ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

Toggle to reach Overlap 'G'

OVERLAP G

Select TMG VEH OVLP [G] and 'OTHER/ECONOLITE'

| TMG VEH 0 | ועכ | .Р. |     | [   | 3]  | T١  | /PE | E :[0 | TI  | HEF | ۱/۲ | EC | ONC | DL  | ΙT | 1 |
|-----------|-----|-----|-----|-----|-----|-----|-----|-------|-----|-----|-----|----|-----|-----|----|---|
| PHASES    | 1   | 2   | 3   | 4   | 5   | 6   | 7   | 8     | 9   | 0   | 1   | 2  | 3   | 4   | 5  | 6 |
| INCLUDED  |     | Х   |     |     |     |     |     |       |     |     |     |    |     |     |    |   |
| PROTECT   |     |     |     |     |     |     |     |       |     |     |     |    |     |     |    |   |
| PED PRTC  |     |     |     |     |     |     |     |       |     |     |     |    |     |     |    |   |
| NOT OVLP  |     |     |     |     |     |     |     |       |     |     |     |    |     |     |    |   |
| FLSH GRN  |     |     |     |     |     |     |     |       |     |     |     |    |     |     |    |   |
| LAG X PH  |     | Χ   |     |     |     |     |     |       |     |     |     |    |     |     |    | ٠ |
| LAG 2 PH  |     |     |     |     |     |     |     |       |     |     |     |    |     |     |    |   |
|           |     |     |     |     |     |     |     |       |     |     |     |    |     |     |    |   |
| LAG GRN 3 | 3.0 | ٬ ٥ | ۲EL | . : | 3.0 | ) F | REC | 2     | 2.0 | )   | ٩D  | V  | GRI | ۷ ( | ٥. | С |

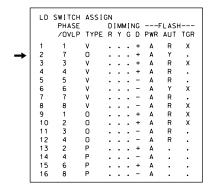
END PROGRAMMING

### ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL

(program controller as shown)

To assign load switch S2 as OLG, program LD SWITCH 2 as OVLP '7' TYPE '0'.

- 1. From Main Menu select | 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select | 3. LOAD SW ASSIGN



## ASC/3-2070 Beacons - Wig Wag Programmable /Actuated Before End Of Green

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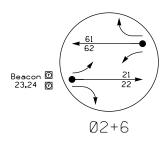
STD. NO.

10.2

SHEET 4 OF 8

# ASC/3-2070 Wig Wag Programmable/Actuated Before End Of Green Advance Beacons With Permitted Left Turn Opposing The Beacon Approach

This example builds on the example shown in STD 10.2 sheet 4 and adds permitted left turns to each of the approaches. When phase 2 clears from green to yellow overlap 'G' begins to time its green extension at which point the advance beacon begins to flash. However, opposing approach phase 6 will clear from green to yellow simultaneously with phase 2 and because phase 6 has a permitted left turn a yellow trap will be created. To avoid a yellow trap a "dummy beacon" has to be created for the phase 6 approach to extend the phase 6 green time the same amount of time as the phase 2 green time. To accomplish this the load switch driving the signal heads on the phase 6 approach will also have to be reassigned as an overlap, overlap 'H' in this example, which will then have to have LAG GRN, YEL, and RED times added that are identical to those of phase 2. This ensures the phase 6 heads will clear simultaneously with the phase 2 heads.



# ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 2. CONTROLLER

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS

Toggle to reach Overlap 'H'

#### OVERLAP H

Select TMG VEH OVLP [H] and 'OTHER/ECONOLITE'

| TMG VEH OV | /LP.  |    | [н  | 1]  | ΤY  | PE  | :[0 | )TH | IEF | ? ∕ E | CC  | NC | )L I | TE  |   |
|------------|-------|----|-----|-----|-----|-----|-----|-----|-----|-------|-----|----|------|-----|---|
| PHASES 1   | 1 2   | 3  | 4   | 5   | 6   | 7   | 8   | 9   | 0   | 1     | 2   | 3  | 4    | 5   | 6 |
| INCLUDED . |       |    |     |     | Х   |     |     |     |     |       |     |    |      |     |   |
| PROTECT .  |       |    |     |     |     |     |     |     |     |       |     |    |      |     |   |
| PED PRTC . |       |    |     |     |     |     |     |     |     |       |     |    |      |     |   |
| NOT OVLP . |       |    |     |     |     |     |     |     |     |       |     |    |      |     |   |
| FLSH GRN . |       |    | •   |     | ٠   | •   |     |     | ٠   |       |     |    |      |     |   |
| LAG X PH . |       |    |     |     | Х   |     |     |     |     |       |     |    |      |     |   |
| LAG 2 PH . |       |    |     |     |     |     |     |     |     |       |     |    |      |     |   |
|            |       |    |     |     |     |     |     |     |     |       |     |    |      |     |   |
| LAG GRN 3  | . O Y | ΈL | . 3 | . ( | ) F | REC | ) 2 | . ( | ) / | ١D١   | / ( | R۱ | 1 (  | ).( | ) |

END PROGRAMMING

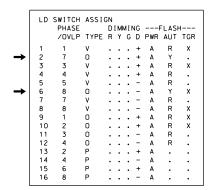
# ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL

(program controller as shown)

To assign load switch S8 as OLH, program LD SWITCH 6 as OVLP '8' TYPE '0'.

1. From Main Menu select | 1. CONFIGURATION

2. From CONFIGURATION Submenu select 3. LOAD SW ASSIGN



# ASC/3-2070 Beacons - Wig Wag Programmable /Actuated Before End Of Green

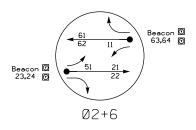
SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

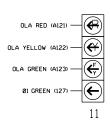
10.2

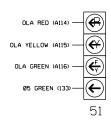
SHEET 5 OF 8

# ASC/3-2070 Wig Wag Programmable/Actuated Before End Of Green Advance Beacons With Flashing Yellow Arrows

When FYAs and Advance Beacons that are actuated before the end of green are used together, care must be taken to ensure that the through heads and the FYAs all clear simultaneously so no yellow traps are created. This is remedied in two ways. First, the load switches that drive the through moves are reassigned as overlaps using LAG GRN, RED, and YEL clear in the same way as described in STD 10.2 sheets 4 and 5. Second, the the DELAY START OF FYA CLEARANCE in the FYA overlap programming is programmed with the identical time as the LAG GRN of the parent. This tells the FYA that after the parent phase clears it should "wait" the programmed amount of time before it clears. When programmed correctly all heads will clear simultaneously and no yellow traps will occur. For the beacons to flash, logic similar to that shown on STD. 10.2 sheet 3 for phase 2 will have to be added.







### ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL

(program controller as shown)

To assign load switch S2 as OLG and S8 as OLH, program LD SWITCH 2 as OVLP  $^{\prime}7^{\prime}$  and LD SWITCH 6 as OVLP  $^{\prime}8^{\prime}$  TYPE  $^{\prime}0^{\prime}$  .

- 1. From Main Menu select 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 3. LOAD SW ASSIGN

LD SWITCH ASSIGN
PHASE DIMMING ---FLASH--/OVLP TYPE R Y G D PWR AUT TGR

1 1 V . . . + A R X
2 7 0 . . . + A R X
4 4 V . . . + A R .
5 5 V . . . - A R .
6 8 0 . . - A Y X
7 7 V . . - A R .
8 8 V . . - A R X
9 1 0 . . + A R X
10 2 0 . . + A R X
11 3 0 . . + A R X
11 3 0 . . - A R .
12 4 0 . . - A R .
13 2 P . . + A . .
14 4 P . . - A . .
15 6 P . . + A . .

# ASC/3-2070 Beacons - Wig Wag Programmable /Actuated Before End Of Green

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SHEET 6 OF 8

# ASC/3-2070 Wig Wag Programmable/Actuated Before End Of Green Advance Beacons With Flashing Yellow Arrows

# ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: .....PPLT FYA]
PROTECTED LEFT TURN.... PHASE 1
OPPOSING THROUGH...... PHASE 2

FLASHING ARROW OUTPUT....CH9 ISOLATE
DELAY START OF: FYA..O.O CLEARANCE..3.0
ACTION PLAN SF BIT DISABLE.........

NOTICE DELAY START OF FYA CLEARANCE TIME EQUAL TO LAG GRN OF OVERLAP PARENT

# ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

Toggle to reach

OVERLAP G

Select TMG VEH OVLP [G] and 'OTHER/ECONOLITE'

END PROGRAMMING

# ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

Toggle to reach

Select TMG VEH OVLP [C] and 'PPLT FYA'

DELAY START OF: FYA..O.O CLEARANCE..3.O ACTION PLAN SF BIT DISABLE.......

← NOTICE DELAY START OF FYA CLEARANCE TIME EQUAL TO LAG GRN OF OVERLAP PARENT

# ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

Toggle to reach Overlap 'H'

OVERLAP H

Select TMG VEH OVLP [H] and 'OTHER/ECONOLITE'

TMG VEH OVLP...[H] TYPE:[OTHER/ECONOLITE]
PHASES 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
INCLUDED X ...
PROTECT ...
PED PRTC ...
NOT OVLP ...
FLSH GRN ...
LAG X PH ... X ...
LAG 2 PH ...
LAG GRN 3.0 YEL 3.0 RED 2.0 ADV GRN 0.0

END PROGRAMMING

# ASC/3-2070 Beacons - Wig Wag Programmable /Actuated Before End Of Green

STD. NO.

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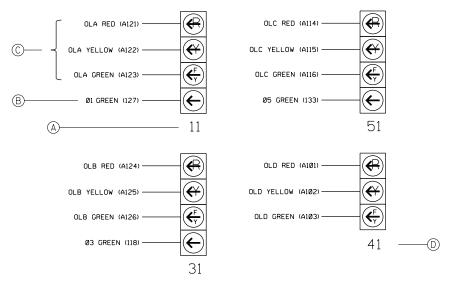
SHEET 7 OF 8

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(This page is intentionally left blank) STD. NO. 10.2 SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION 12-20 SHEET 8 OF 8 NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

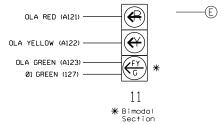
### FYA SIGNAL WIRING DETAIL FOR 332 BASE MOUNTED CABINET

(wire signal heads as shown)



#### NOTE

The sequence display for signal heads 11, 31, and 51 requires special logic programming. See sheet 2 for programming instructions.



NOTE

The sequence display for signal head 11 requires special logic programming. See sheet 2 for programming instructions.

### 2070 FYA 332 Signal Head Wiring

Flashing Yellow Arrow signals may consist of 4-section heads where there is both a protected and permitted vehicle movement, or they may be 3-section heads that are permitted movements only. Each type has unique wiring requirements that vary depending on whether the cabinet type being used is a 332 base type or a 336 pole type.

- A Four section FYA signal Equipped with four signal faces required to implement the protected/permitted flashing yellow arrow vehicle movement.
- Green signal face used for the protected turn and is typically wired to the load switch associated with the protected turn phase.
- © Flashing yellow, solid yellow, and red arrows that comprise the permitted turn portion of the protected/permitted FYA signal sequence. These signal faces are driven by overlaps.
- ① Three section FYA signal Equipped with three signal faces required to implement a permitted only flashing yellow movement. This signal head has no protected turn associated with it but does require a correctly configured overlap with a parent to run correctly.

© Bi-Modal FYA signal - Serves the same purpose as a four section FYA signal head. The bi-modal signal face has wiring for both the protected green turn and the flashing yellow arrow and will display either of those two signals in the correct color when they are active.

## 2070 FYA for 332 Base Mounted Cabinets – Signal Head Wiring

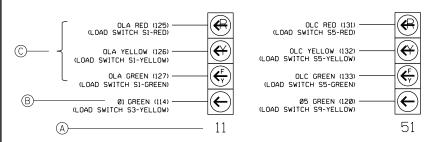
SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

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SHEET 1 OF 2

#### FYA SIGNAL WIRING DETAIL FOR 336 POLE MOUNTED CABINET

(wire signal heads as shown)



#### <u>NOTE</u>

The sequence display for signal heads 11 and 51 requires special logic and output remapping. See sheet 2 for programming instructions.

## 2070 FYA 336 Signal Head Wiring

Flashing Yellow Arrow signals may consist of 4-section heads where there is both a protected and permitted vehicle movement, or they may be 3-section heads that are permitted movements only. Each type has unique wiring requirements that vary depending on whether the cabinet type being used is a 332 base type or a 336 pole type.

- A Four section FYA signal Equipped with four signal faces required to implement the protected/permitted flashing yellow arrow vehicle movement.
- B The green signal face used for the protected turn when using a 336 cabinet must be connected to the PED yellow output of a PED load switch. This is because there are a limited number of load switches due to the fact there is no auxiliary output file in the cabinet. To use this output as a vehicle phase it must first be remapped as a vehicle phase and assigned the appropriate phase.
- © Flashing yellow, solid yellow, and red arrows that comprise the permitted turn portion of the protected/permitted FYA signal sequence. These signal faces are driven by overlaps Before being used as an overlap, the load switch in use must first be remapped as the appropriate vehicle overlap.

2070 FYA for 336 Pole Mounted Cabinets – Signal Head Wiring

SIGNALS MANAGEMENT

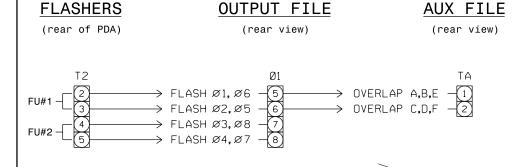
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SHEET 2 OF 2

### 2070 FYA Flasher Circuit Modification



The 170 type signal cabinets, both base and pole mount styles, are equipped with two flasher units that are designed to flash signal heads on the various spans of a design per the signal design plan. All signal heads on a span shall flash in unison, but when dealing with flashing yellow arrow designs the default flasher wiring found in the cabinet does not always readily facilitate this requirement. When faced with heads that do not flash in unison with default cabinet flasher wiring, it becomes necessary to modify the cabinet flasher wiring to ensure all heads on their respective spans will flash in unison. The chart shown to the left illustrates where the flasher circuits originate, where they terminate, and which phases and overlaps they are associated with.

A This diagram shows where the flasher circuits originate and terminate in the cabinets and which phases and overlaps are associated with each flasher circuit. Each flasher unit has two flasher circuits that flash 180 degrees out of phase with each other at a 1Hz 50% duty cycle rate. Each flasher unit operates independently. This diagram can be used to compare which signal heads on a span are being driven by which flasher unit in an effort to determine whether they flash in unison.



#### FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM, T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

(B) FLASHER CIRCUIT MODIFICATION - Often times when FYAs are used on a signal design, the heads on a span will not flash in unison. This happens more often than not on side streets where an overlap flashes out of phase with a through move phase on the same span. When this is the case and flashing all heads on a single flasher unit will remedy the problem, put the note shown to the left on the electrical detail.

## 2070 FYA for 332 and 336 Cabinets – Flasher Circuit Modification

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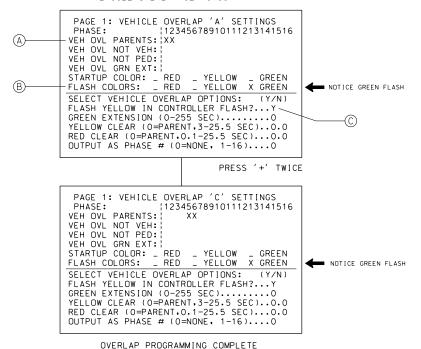
11.1

SHEET 1 OF 1

#### OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS),



## 2070 Oasis FYA Overlap Programming

Flashing Yellow Arrow designs utilizing three and four section heads to run protected/permitted sequences require overlaps to properly run the protected and the permitted movements. The protected turn is assigned a parent phase that is associated with the usual turning phase. The permitted move is assigned a parent phase that is the opposing through movement of the protected turn. In cases where FYA designs are permitted turns only (three section heads), there is only one parent for the overlap and it is normally the opposing through move phase.

- (A) VEH OVL PARENTS: Overlaps will be allowed to run when any of the phases selected in this row are active. Most times the odd phases are the turning phases and the even phases are the opposing through phases.
- (B) FLASH COLORS: When selected, the controller will flash the selected color at 1Hz with a 50% duty cycle when it is timing. For FYAs, the flashing yellow arrow is wired to the overlap load switch green output so flashing the green is what produces the flashing yellow arrow.
- $\odot$  FLASH YELLOW IN CONTROLLER FLASH When programmed with a 'Y', the controller will flash the overlap yellow output if the controller goes into controller flash.

# 2070 Oasis FYA 332 Base and 336 Pole Mounted Cabinets – Overlaps

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11.2

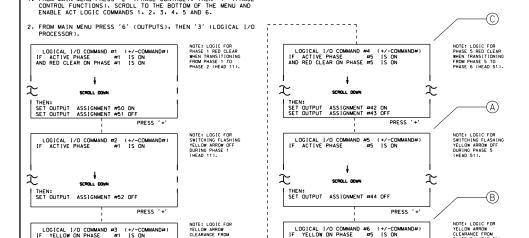
SHEET 1 OF 1

# LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE

PRESS '+'



 $\approx$ 

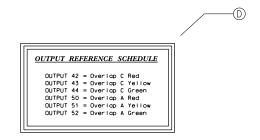
SET OUTPUT ASSIGNMENT #43 ON

LOGIC 1/O PROCESSOR PROGRAMMING COMPLETE

### 2070 Oasis FYA 332 Logic Processor Programming

The Logic Processor contained in the Oasis software is required to produce the proper protected/permitted vehicle sequence when running FYAs that use four section heads.

- (A) When the protected turn phase is being serviced, this logic forces the flashing yellow arrow section of the FYA "OFF".
- B When the protected turn phase is transitioning through yellow clear, this logic forces the four section FYA to display a solid yellow indication.
- © When the protected turn phase is transitioning through red clear, this logic forces the four section FYA to display a solid red indication while ensuring the solid yellow indication is "OFF".
- Reference Schedule that defines the controller output assignment to overlap/signal face relationship.



2070 Oasis FYA 332 Base Mounted Cabinets – Logic Processor

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 $\approx$ 

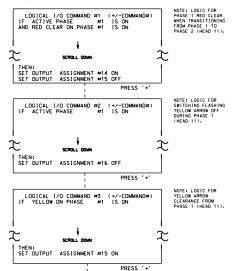
THEN: SET OUTPUT ASSIGNMENT #51 ON

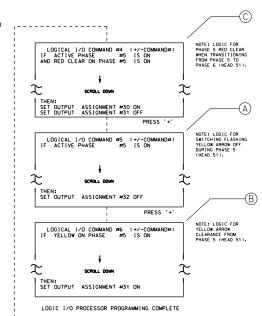
#### LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

#### TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL). THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1. 2. 3. 4. 5. and 6.
- FROM MAIN MENU PRESS '6' (OUTPUTS). THEN '3' (LOGICAL I/O PROCESSOR).

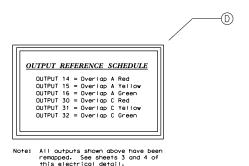




### 2070 Oasis FYA 336 Logic Processor Programming

The Logic Processor contained in the Oasis software is required to produce the proper protected/permitted vehicle sequence when running FYAs that use four section heads.

- (A) When the protected turn phase is being serviced, this logic forces the flashing yellow arrow section of the FYA "OFF".
- (B) When the protected turn phase is transitioning through yellow clear, this logic forces the four section FYA to display a solid yellow indication.
- © When the protected turn phase is transitioning through red clear, this logic forces the four section FYA to display a solid red indication while ensuring the solid yellow indication is "OFF".
- ® Reference Schedule that defines the controller output assignment to overlap/signal face relationship.



## 2070 Oasis FYA for 336 Pole Mounted Cabinets – Logic Processor

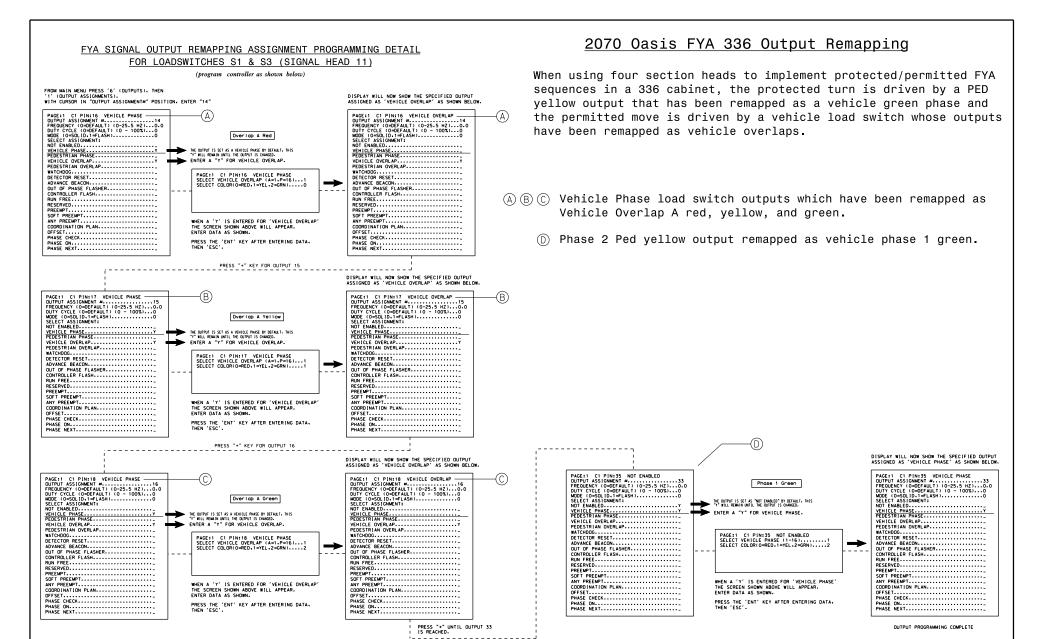
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2070 Oasis FYA for 336 Pole Mounted Cabinets - Output Remapping

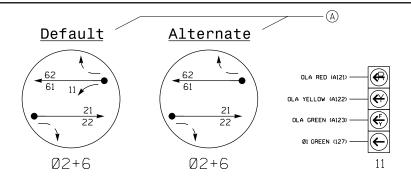
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11.4

SHEET 1 OF 1



## 2070 Oasis 4-Section FYA Alternate Phasing

Often times a signal plan will call for alternate phasing where the protected and permitted turning movements of a four section flashing yellow arrow signal are run as the default phasing but the protected only movement is run during an alternate phasing period. This section illustrates the steps needed to run the protected and permitted turning movements of a four section flashing yellow arrow signal during default phasing and only the protected turn during the alternate phasing. Also shown are loop detector programming changes that are implemented during the alternate phasing period.

#### INPUT FILE CONNECTION & PROGRAMMING CHART

| LOOP NO | LOOP<br>TERMINAL | INPUT<br>FILE POS. | PIN<br>NO. | INPUT<br>ASSIGNMENT<br>NO. | DETECTOR<br>NO. | NEMA<br>PHASE | CALL | EXTEND | FULL<br>TIME<br>DELAY | STRETCH<br>TIME | DELAY<br>TIME |
|---------|------------------|--------------------|------------|----------------------------|-----------------|---------------|------|--------|-----------------------|-----------------|---------------|
|         | TB2-1,2          | IIU                | 56         | 18                         | 1               | 1             | Υ    | Y      |                       |                 | 15            |
| 1A 1    | -                | J4U                | 48         | 10 ★                       | 26              | 6             | Υ    | Y      | Y                     |                 | 3             |
|         | -                | I1U                | 56         | 18 ★                       | 51              | 1             | Υ    | Υ      |                       |                 |               |

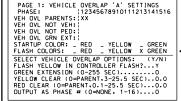
 $\star$  See Input Page Assignment programming details on sheets 3 and 4. ———(B

- (A) Default and Alternate phasing diagrams from the signal plan showing that the permitted turn on flashing yellow arrow signal head 11 does not run during alternate phasing 2+6.
- (B) Input File Chart Information contained here is taken directly from the signal plan. The detector call to phase 6 on loop 1A is turned off during the alternate phasing period and the delay time on loop 1A is reduced. Programming required to implement this is found on subsequent sheets of the electrical detail as shown in the footnote.
- © Overlap Programming To ensure the permitted flashing yellow arrow signal face does not run during the alternate phasing, its parent overlap must be omitted and it should not be programmed to flash green. This is programmed on overlap "PAGE 2" for use during alternate phasing.

# OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING COMPLETE

# OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.

PAGE 2: VEHICLE OVERLAP 'A' SETTINGS
PHASE:
112345678910111213141516
VEH OVL PARENTS:
VEH OVL NOT VEH:
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: RED \_ YELLOW \_ GREEN
FLASH COLORS: \_ RED \_ YELLOW \_ GREEN
SELECT VEHICLE OVERLAP DPTIONS:
(Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).....0
YELLOW (LEAR (0-PARENT.0-1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0-MONE) -1-16)...0

OVERLAP PROGRAMMING COMPLETE

(continued on next page)

## 2070 Oasis 4-Section FYA Alternate Phasing

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#### INPUT PAGE 2 ASSIGNMENT PROGRAMMING DETAIL FOR ALTERNATE PHASING - LOOP 1A -

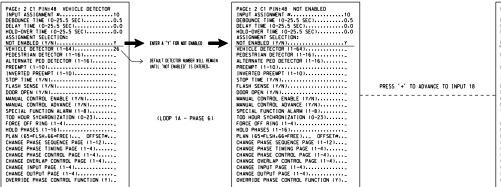
(program controller as shown below)

NOTES: 1. THIS PROGRAMMING APPLIES FOR INPUT PAGE 2 ONLY. INPUT PAGE 1 WILL USE STANDARD DEFAULT SETTINGS. THIS PROGRAMMING IS NECESSARY FOR PROPER DETECTOR OPERATION DURING ALTERNATE PHASING OPERATION.

2. THE FIRST TASK THIS PROGRAMMING ACCOMPLISHES IS THE DISABLING OF INPUT #10 (DETECTOR 26) SO THAT A VEHICLE CALL WILL NOT BE PLACED TO PHASE 6 DURING ALTERNATE PHASING OPERATION. THE SECOND TASK THIS PROGRAMMING ACCOMPLISHES IS THAT IT REASSIGNS DETECTOR 51 TO INPUT #18 SO THAT THE DELAY ON LOOP 1A CAN BE REDUCED FROM 15 SECONDS TO 0 SECONDS.

## 2070 Oasis 4-Section FYA Alternate Phasing

FROM MAIN MENU PRESS '5' (INPUTS). THEN PRESS 'NEXT' TO GET TO INPUT PAGE '2'. PRESS THE '+' KEY UNTIL INPUT 10 IS REACHED.

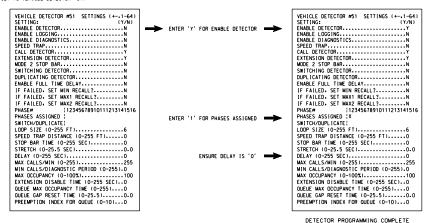


PROGRAMMING COMPLETE

#### SPECIAL DETECTOR PROGRAMMING DETAIL - LOOP 1A (ALT.)

(program controller as shown below)

FROM MAIN MENU PRESS '7' (DETECTORS). THEN PRESS '1' FOR VEHICLE DETECTORS. PRESS THE '-' KEY TO GET TO VEHICLE DETECTOR #51.



① Input Assignment and Detector programming provides the programming steps necessary to disable a phase 6 call on loop 1A, reassign the detector number assigned to loop 1A's input, and reduce the delay. This is all programmed on input "PAGE 2" for use during alternate phasing.

NOTE: DETECTOR IS PROGRAMMED PER THE INPUT FILE CONNECTION AND PROGRAMMING CHART SHOWN ON SHEET 1.

(continued on next page)

2070 Oasis 4-Section FYA Alternate Phasing

SIGNALS MANAGEMENT
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NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

STD. NO.

11.5

SHEET 2 OF 3

## \_\_\_\_E

#### ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING COORDINATION - SELECT ALL PAGE CHANGES (AS SHOWN BELOW) WITHIN COORDINATION PLAN PROGRAMMING.

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM PAGE CHANGES (SHOWN BELOW) IN SEPARATE TIME OF DAY EVENTS. IF PAGE 1 IS USED. NO EVENT PROGRAMMING IS NECESSARY FOR THAT PARTICULAR PAGE.

| PHASING  | INPUTS PAGE | OVERLAPS PAGE |
|--|-------------|---------------|
| ACTIVE PAGES REQUIRED TO RUN DEFAULT PHASING   | 1           | 1             |
| ACTIVE PAGES REQUIRED TO RUN ALTERNATE PHASING | 2           | 2             |
|  |             |               |

NOTE: PAGES NOT SHOWN (i.e. sequence, phase control, etc.) SHOULD REMAIN AS '1', OR AS DEFINED BY TIMING ENGINEER.

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION. DO NOT OPERATE TIME OF DAY PAGE CHANGE EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN PAGE CHANGE EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

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#### ALTERNATE PHASING PAGE CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN THESE OVERLAP/INPUT PAGE CHANGES ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAPS PAGE 2: Modifies overlap parent phases for heads 11 to run protected turns only.

INPUTS PAGE 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

## 2070 Oasis 4-Section FYA Alternate Phasing

© Alternate Phasing Activation Detail is a legend that outlines which inputs, overlaps, and other relevant pages are required to run during normal operation or during alternate phasing operation.

F Alternate Phasing Page Change Summary - This area is used to describe in detail how the programming changes made to the different controller programming pages affect the phasing operation during the alternate phasing period. The loop delay reduction time of "0" seconds shown in this example is taken from the signal design plan, and that value may vary.

2070 Oasis 4-Section FYA Alternate Phasing

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SHEET 3 OF 3

#### PED YELLOW CONFLICT MONITOR WIRING DETAIL

(make cabinet wiring changes as shown below)

In order to use FYA COMPACT mode with the 2018ECL-NC Monitor, the cabinet must be wired such that the (unused) Ped Yellow load switch outputs are wired to the conflict monitor as follows: From 2 PY (field term. 114) to chan. 9 green (monitor pin 13), from 4 PY (field term. 105) to chan. 9 yellow (monitor pin 16), from 6 PY (field term. 120) to chan. 10 green (monitor pin R), and from 8 PY (field term. 111) to chan. 10 yellow (monitor pin U).

Follow the instructions below to make the appropriate connections:

- STEP 1: Fold down rear panel of output file.
- STEP 2: Find unused wiring harness from conflict monitor card edge connector (which should be tied and bundled together).
- STEP 3: Find the conductors that correspond to the following conflict monitor card edge pins and solder wire to the appropriate terminal on the rear of the output file as shown below:

| CMU-13 — | 2PY | (term. | 114 |
|----------|-----|--------|-----|
| CMU-16   | 4PY | (term. | 105 |
| CMU-R -  | 6PY | (term. | 120 |
| CMU-U —  |     | (term. | 111 |

NOTE: Some cabinet manufacturers use keyed connectors to accomplish this wiring configuration. If connectors are used, fold down the rear panel of the output file and find the set of 3 keyed connectors and connect them as shown below:

| 1-2PY 1-CMU-13<br>2-4PY 2-CMU-16<br>3-6PY 3-CMU-R<br>4-8PY 4-CMU-U |
|--|
|--|

# 2070 Oasis FYA 336 Conflict Monitor Wiring Detail

When using four section heads to implement protected/permitted FYA sequences in a 336 cabinet, the protected turn is driven by a PED yellow output that has been remapped as a vehicle green phase and the permitted move is driven by a vehicle load switch whose outputs have been remapped as overlaps. For the monitor to be able to see the protected turn indication on the rempped PED yellow output, special wiring must be made between the output file and the conflict monitor.

 PED Yellow Conflict Monitor Wiring Detail giving the monitor visibility of the protected turn that is output on the remapped PED yellow load switch output.

2070 Oasis FYA for 336 Pole Mounted Cabinets – Conflict Monitor Wiring Detail

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SHEET 1 OF 1

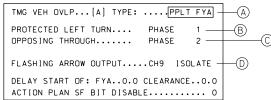
# ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

#### OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'



END PROGRAMMING

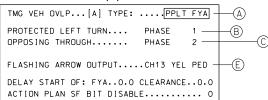
# ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

#### OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'



END PROGRAMMING

## ASC/3-2070 4-Section FYA Overlap Programming

Flashing Yellow Arrow designs utilizing four section heads to run protected/permitted sequences require overlaps to properly run the protected and the permitted movements. ASC/3-2070 has an overlap mode designed specifically for protected/permitted FYA applications that takes care of sequencing the signal face outputs on the four section FYA signal heads.

- A Toggle through the overlap selections in the overlap programming and select PPLT FYA when using four section FYA signal heads. A compliant conflict monitor is required to monitor FYA/s when using this type of overlap.
- B PROTECTED LEFT TURN Represents the protected turning movement of the protected/permitted FYA sequence.
- © OPPOSING THROUGH Represents the opposing through movement during which the left turn movement is permitted for the protected/permitted FYA sequence.

When using a 332 base mounted cabinet...

① FLASHING ARROW OUTPUT - Toggle through the selections to select ISOLATE, which refers to the isolated green indication of the protected turn channel. The appropriate output channel for the assigned protected and permitted phases will be displayed as shown in a read only field.

When using a 336 pole mounted cabinet...

E FLASHING ARROW OUTPUT - Toggle through the selections to select YEL PED in order to assign the permitted turn channel to a PED yellow output channel. The appropriate PED channel for the assigned protected and permitted phases will be displayed as shown in a read only field. Output remapping is required to satisfy the conflict monitor FYA channel monitoring requirements. Refer to STD 11.8, sheet 1 of 2 for remapping details.

## ASC/3-2070 FYA - Overlaps

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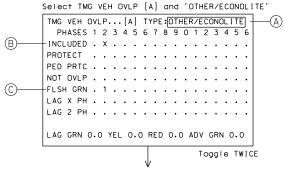
SHEET 1 OF 2

## ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

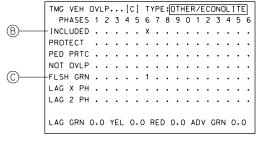
- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

#### OVERLAP A



#### OVERLAP C

Select TMG VEH OVLP [C] and 'OTHER/ECONOLITE'



END PROGRAMMING

### ASC/3-2070 3-Section FYA Overlap Programming

Flashing Yellow Arrow designs utilizing three section heads to run permitted only sequences require overlaps to properly run the permitted movements. ASC/3-2070 has an overlap mode that will flash the flashing yellow arrow signal face during the permitted phase movement.

- (A) Toggle through the overlap selections in the overlap programming and select OTHER/ECONOLITE when using three section FYA signal heads. A compliant conflict monitor is required when using this type of overlap to monitor the FYA's.
- (B) INCLUDED Select the phases in which the permitted move is allowed to run for the permitted FYA sequence.
- © FLASH GRN Defines the rate at which the overlap will flash during the green interval of each included phase. Toggle this setting to '1' to flash the flashing yellow arrow signal face at a 1Hz 50% duty cycle rate.

ASC/3-2070 FYA - Overlaps

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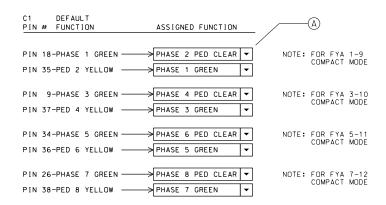
11.7

SHEET 2 OF 2

#### ECONOLITE ASC/3-2070 I/O PIN REMAPPING

The ASC/3 Configurator utility program must be used to remap the I/O pins as shown below. Consult the ASC/3 Configurator User Guide for specific instructions on software use.

- 1. Run the Configurator utility. Load a file as the Current DB.
- Choose the C1-out tab to change the I/O mapping as needed. Use the drop down list within the program to select the assigned function for the pins shown below.
- 3. Save the database file and download it to the controller.



NOTE: The steps below can be used to view changes to I/O pins within the controller. Any I/O pins that have been remapped will display and show their default function in addition to the current assigned function.

- 1. From Main Menu select 7. STATUS DISPLAY
- 2. From STATUS DISPLAY Submenu select 8. INPUTS/OUTPUTS
- 3. From INPUT/OUTPUT Submenu select 9. I/O DIFFERENCES

### ASC/3-2070 4-Section FYA Output Remapping

By default, when "YEL PED" is selected as the flashing arrow output during overlap programming, the ASC/3-2070 software outputs the flashing yellow arrow on a PED yellow channel and the protected turn on the green load switch channel of the protected turn phase. This arrangement places the protected turn and the flashing yellow arrow on the wrong conflict monitor input channels and as such these two outputs must be swapped with each other to satisfy the conflict monitor requirements. The ASC/3-2070 Configurator is used to remap these two outputs.

A By selecting the C1 pin associated with the phase 1 green output and changing its function to "PHASE 2 PED CLEAR" from the drop down menu, and by selecting the C1 pin associated with the Ped 2 yellow output and changing its function to "PHASE 1 GREEN", the flashing yellow arrow will now be output on the overlap A green output and the solid green arrow will be output on the Ped 2 yellow output. These same actions are repeated as needed for any four section FYA in use.

ASC/3-2070 4-SECTION FYA for 336 Pole Mounted Cabinets - Output Remapping

11.8

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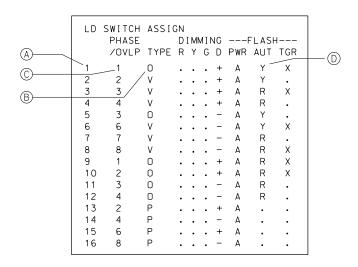
STD. NO.

### ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL

(program controller as shown)

To assign load switches S1 and S5 as OLA and OLC, program LD SWITCH 1 as OVLP '1' TYPE '0' and LD SWITCH 5 as OVLP '3' TYPE '0' as shown below.

- 1. From Main Menu select 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 3. LOAD SW ASSIGN



### ASC/3-2070 FYA Load Switch Reassignment

The function of a load switch can be reassigned using ASC/3-2070 software. To implement permitted turn movements using three section FYA signal heads, vehicle load switches must be reassigned as vehicle overlaps. This is accomplished by reassigning the required load switches using the programming screen shown to the left.

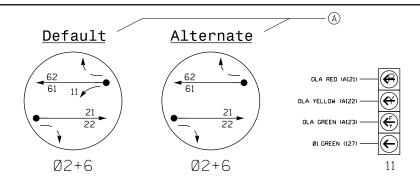
- (A) LD SWITCH ASSIGN This column represents 16 load switches that are typically found in a 170 type cabinet. Numbers 1-8 are vehicle load switches, 9-12 are overlap load switches located in an auxiliary output file if the cabinet were so equipped, and 13-16 are pedestrian load switches. This is a read only field.
- ® TYPE This column defines the output type of the load switch. The four assignment types can be toggled between Vehicle, Overlap, Pedestrian, or the load switch can be turned OFF with no selection being shown in this column.
- © PHASE/OVLP This column defines the Vehicle or Pedestrian phase number assigned to type "V" and type "P" load switches. For load switches reassigned as type "O", the OVLP numbers range from 1-16 which represents overlaps A-P. In the screen shown to the left, load switches 1 and 5 have been reassigned as overlaps "A" and "C" respectively.
- ① AUT This column defines the load switch Automatic Flash color, which can be set to Red, Yellow, or dark. In the screen shown to the left, overlap A and overlap C will flash yellow when the controller goes into automatic flash.

ASC/3-2070 3-SECTION FYA for 336 Cabinets - Load Switch Assignment

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#### INPUT FILE CONNECTION & PROGRAMMING CHART

| LOOP NO. | LOOP<br>TERMINAL | INPUT<br>FILE POS. | PIN<br>NO. | DETECTOR<br>NO. | NEMA<br>PHASE | CALL | EXTEND<br>TIME | DELAY<br>TIME | DETECTOR<br>TYPE |
|----------|------------------|--------------------|------------|-----------------|---------------|------|----------------|---------------|------------------|
| 1A 1     | TB2-1,2          | I1U                | 56         | 1 ★             | 1             | YES  |                | 15            | S                |
|          | -                | J4U                | 48         | 26★             | 6             | YES  |                | 3             | G                |

## ECONOLITE ASC/3-2070 OVERLAP — © PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

#### OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP...[A] TYPE: .....PPLT FYA

PROTECTED LEFT TURN.... PHASE 1

OPPOSING THROUGH......PHASE 2

FLASHING ARROW OUTPUT.....CH9 ISOLATE

DELAY START OF: FYA..O.O CLEARANCE..O.O

ACTION PLAN SF BIT DISABLE..........1

END PROGRAMMING

### ASC/3-2070 4-Section FYA Alternate Phasing

Occasionally a signal plan will call for alternate phasing where the protected and permitted turning movements of a four section flashing yellow arrow signal are run as the default phasing but the protected only movement is run during an alternate phasing period. This section illustrates the steps needed to run the protected and permitted turning movements of a four section flashing yellow arrow signal during default phasing and only the protected turn during the alternate phasing. Also shown are loop detector programming changes that are implemented in the alternate phasing period.

- A Default and Alternate phasing diagrams from the signal plan showing that the permitted turn on flashing yellow arrow signal head 11 does not run during alternate phasing 2+6.
- (B) Input File Chart Information contained here is taken directly from the signal plan. The detector call to phase 6 on loop 1A is turned off during the alternate phasing period and the delay time on loop 1A is reduced. Programming required to implement this is found on subsequent sheets of the electrical detail as shown in the footnote.
- © Overlap Programming ASC/3-2070 has special function bits that can be entered in the overlap programming and be used to disable the permitted left turn of a four section flashing yellow arrow during alternate phasing. Enable the selected special function bit to disable the permitted turn during alternate phasing.

(continued on next page)

## ASC/3-2070 4-Section FYA - Alternate Phasing

SIGNALS MANAGEMENT

TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

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SHEET 1 OF 4

### ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP — PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 1A

(program controller as shown)

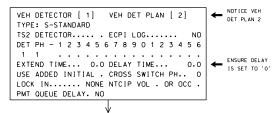
## IMPORTANT!

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

- 1. From Main Menu select 8. UTILITIES
- 2. From UTILITIES Submenu select 1. COPY/CLEAR
- 3. Copy from DETECTOR PLAN "1" to DETECTOR PLAN "2".

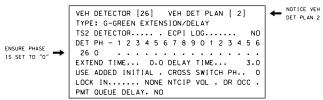
COPY / CLEAR UTILITY FROM PHASE TIMING.... > PHASE TIMING.... . TIMING PLAN..... > TIMING PLAN..... PH DET OPT PLAN. . > PH DET OPT PLAN. . DETECTOR PLAN... 1 > DETECTOR PLAN... 2 TOGGLE TO SELECT A "FROM" AND A "TO" THEN PRESS ENTER

- 4. From Main Menu select 6. DETECTORS
- 5. From DETECTOR Submenu select 2. VEHICLE DETECTOR SETUP
- 6. Place cursor in VEH DET PLAN [ ] position and enter "2".
  - Place cursor in VEH DETECTOR [ ] position and enter "1".
  - Set delay time to "0".



NOTICE VEH

- Place cursor in VEH DETECTOR [ ] position and enter "26".
- Set assigned phase to "0".



END PROGRAMMING

### ASC/3-2070 4-Section FYA Alternate Phasing

Nehicle Detector Setup provides the programming steps necessary to disable a phase 6 call on loop 1A and reduce the delay. This is all programmed on vehicle detector plan 2 for use by an action plan during alternate phasing operation.

(continued on next page)

### ASC/3-2070 4-Section FYA - Alternate Phasing

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

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SHEET 2 OF 4

## ECONOLITE ASC/3-2070 ACTION PLAN ----E

(F)

- 1. From Main Menu select 5. TIME BASE
- 2. From TIME BASE Submenu select 2. ACTION PLAN

PROGRAMMING DETAIL

| ACTION PLA | A 6.1 | ſ   | w | 1  |    |      |    |      |     |     |     |   |   |   |   |   |   |
|------------|-------|-----|---|----|----|------|----|------|-----|-----|-----|---|---|---|---|---|---|
| PATTERN    |       |     |   | •  |    | cvc  |    | /ERR |     |     |     | _ |   |   |   |   | Г |
|            |       |     |   |    |    |      | -  |      |     |     |     | - |   |   |   |   |   |
| TIMING PLA |       |     |   |    |    |      |    | NCE. |     |     |     |   |   |   |   |   |   |
| VEH DETECT | -     | _   |   | _  |    |      |    | OG   |     |     |     |   |   |   |   |   |   |
| FLASH      |       |     |   |    |    |      |    | EST. |     |     |     | - |   |   |   |   |   |
| VEH DET D  | -     | _   |   | -  |    |      |    | ET D |     | _   |     | - |   |   |   |   |   |
| DIMMING EN | NAB   | LE. | • | NO |    | PR I | OR | ITY  | RET | URN | . N | 0 |   |   |   |   |   |
| PED PR RE  | TUR   | Ν   |   | NO |    | QUE  | UE | DEL  | AY. |     | . N | 0 |   |   |   |   |   |
| PMT COND [ | DEL   | ΑY  |   | NO |    |      |    |      |     |     |     |   |   |   |   |   |   |
| PHASE      | 1     | 2   | 3 | 4  | 5  | 6    | 7  | 8    | 9   | 0   | 1   | 2 | 3 | 4 | 5 | 6 |   |
| PED RCL    |       |     |   |    |    |      |    |      |     |     |     |   |   |   |   |   |   |
| WALK 2     |       |     |   |    |    |      |    |      |     |     |     |   |   |   |   |   |   |
| VEX 2      |       |     |   |    |    |      |    |      |     |     |     |   |   |   |   |   |   |
| VEH RCL    |       |     |   |    |    |      |    |      |     |     |     |   |   |   |   |   |   |
| MAX RCL    |       |     |   |    |    |      |    |      |     |     |     |   |   |   |   |   |   |
| MAX 2      |       |     |   |    |    |      |    |      |     |     |     |   |   |   |   |   |   |
| PHASE      | 1     | 2   | 3 | 4  | 5  | 6    | 7  | 8    | 9   | 0   | 1   | 2 | 3 | 4 | 5 | 6 |   |
| MAX 3      |       |     |   |    |    |      |    |      |     |     |     |   |   |   |   |   |   |
| CS INH     |       |     |   |    |    |      |    |      |     |     |     |   |   |   |   |   |   |
| TIMO       |       |     |   |    |    |      |    |      |     |     |     |   |   |   |   |   |   |
| SPC FCT    | Х     |     |   |    |    |      |    |      | ( 1 | -8) |     |   |   |   |   |   |   |
| AUX FCT    |       |     |   | (1 | -3 | )    |    |      |     |     |     |   |   |   |   |   |   |
|            | 1     | 2   | 3 | 4  | 5  | 6    | 7  | 8    | 9   | 0   | 1   | 2 | 3 | 4 | 5 |   |   |
| LP 1-15    |       |     |   |    |    |      |    |      |     |     |     |   |   |   |   |   |   |
| LP 16-30   |       |     |   |    |    |      |    |      |     |     |     |   |   |   |   |   |   |
| LP 31-45   |       |     |   |    |    |      |    |      |     |     |     |   |   |   |   |   |   |
| LP 46-60   |       |     |   |    |    |      |    |      |     |     |     |   |   |   |   |   |   |
| LP 61-75   |       |     |   |    |    |      |    |      |     |     |     |   |   |   |   |   |   |
| LP 76-90   |       |     |   |    |    |      |    |      |     |     |     |   |   |   |   |   |   |
| LP 91-100  |       |     |   |    |    |      |    |      |     |     |     |   |   |   |   |   |   |
|            |       |     |   |    |    |      |    |      |     |     |     |   |   |   |   |   | 1 |

\* The Action Plan number(s) are to be determined by the Division and/or City Traffic Engineer.

### ASC/3-2070 4-Section FYA Alternate Phasing

- © To run protected only turns during the Alternate Phasing period, an Action Plan is programmed to enable Vehicle Detector Plan 2 and to enable Special Funtion Bit 1. The Action Plan for the Alternate Phasing period typically runs during a scheduled day plan or during coordination.
- F The note at the bottom of the programming detail should be included on the electrical detail and makes it clear that the noted engineer(s) will be responsible for determining the Action Plan number(s) required to operate the signal correctly during the Alternate Phasing period(s).

(continued on next page)

### ASC/3-2070 4-Section FYA - Alternate Phasing

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#### ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM CHANCES (SHOWN BELOW) IN A TIME BASED ACTION PLAN SCHEDULE A DAY PLAN THAT INCLUDES THE ACTION PLAN PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BIT 1

TO RUN ALT. PHASING DURING COORDINATION - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BIT 1

| PHASING  | VEH DET PLAN | SF BITS ENABLED |
|--|--------------|-----------------|
| ACTIONS REQUIRED TO RUN DEFAULT PHASING          | 1            | NONE            |
| ACTIONS REQUIRED TO RUN <u>ALTERNATE PHASING</u> | 2            | 1               |
|  |              |                 |

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION. DO NOT OPERATE TIME OF DAY EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).



#### ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN SF BIT 1 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BIT 1:

Modifies overlap parent phases for head 11 to run protected turns only.

VEH DET PLAN 2:

Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

### ASC/3-2070 4-Section FYA Alternate Phasing

F Alternate Phasing Activation Detail is a legend that outlines which vehicle detector plan, special function bits, and other relevant programming is required to run during normal operation or during alternate phasing operation.

© Alternate Phasing Page Change Summary - This area is used to describe in detail how the programming changes made to the different controller programming pages affect the phasing operation during the alternate phasing period. The loop delay reduction time of "0" seconds shown in this example is taken from the signal design plan, and that value may vary.

ASC/3-2070 4-Section FYA - Alternate Phasing

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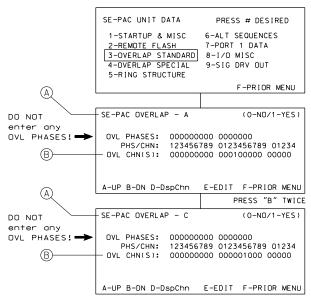
11.10

SHEET 4 OF 4

# SE-PAC2070 FYA PROTECTED/PERMISSIVE SEQUENCE FOR OVERLAPS A & C

(program controller as shown below)

FROM MAIN MENU PRESS 4 (UNIT DATA)



OVERLAP PROGRAMMING COMPLETE
PRESS 'F' TO RETURN TO UNIT DATA

### SE-PAC2070 FYA Overlap Programming

Flashing Yellow Arrow designs utilizing three and four section heads to run protected/permitted sequences typically require overlaps to properly run the protected and the permitted movements. When using SE-PAC2070 software, the protected/permitted overlap phase relationship is programmed in a special overlap portion of the software so care must be taken to ensure no standard overlaps are programmed for overlaps that are to be used for the protected/permitted sequence.

- SE-PAC OVERLAP Overlap being used for the protected/permitted or permitted only flashing yellow arrow movement.
- OVL CHN(S) This represents the signal driver output for the designated overlap. Make sure this channel is correct for the SE-PAC OVERLAP, e.g. OVL CHN 13 = SE-PAC OVERLAP A, and ensure no overlap phases are assigned.

## SE-PAC2070 FYA 332 Base Mounted Cabinets – Overlaps

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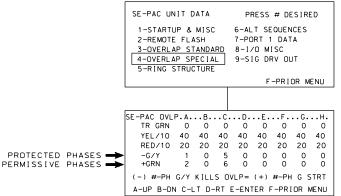
11.11

SHEET 1 OF 1

## PROTECTED AND PERMISSIVE PHASES FOR FLASHING YELLOW ARROW

(program controller as shown below)

FROM MAIN MENU PRESS 4 (UNIT DATA)



PPLT DEFINITION PROGRAMMING COMPLETE
PRESS 'F' TO RETURN TO UNIT DATA

NOTE: THIS PROGRAMMING IS REQUIRED FOR SIGNAL HEADS 11 AND 51 SO THAT THE SOLID GREEN ARROWS TURN ON EXCLUSIVELY DURING PROTECTED GREEN PHASES 1 AND 5. AND THE FLASHING YELLOW ARROWS TURN ON EXCLUSIVELY DURING PERMITTED GREEN PHASES 2 & 6.

#### SE-PAC2070 FYA Protected/Permissive Phases

Flashing Yellow Arrow designs utilizing three and four section heads to run protected/permitted sequences typically require overlaps to properly run the protected and the permitted movements. When using SE-PAC2070 software, the protected/permitted overlap phase relationship is programmed in a special overlap portion of the software shown to the left. The -G/Y entry defines the protected move and the +GRN entry defines the permissive move. Both of the phases must be in the same ring for the software to consider them valid entries.

When three section flashing yellow arrow signal heads are used to run permitted only movements, there is no protected phase. In spite of this, the protected phase that would normally be associated with the permissive must still be entered in the -G/Y field to make the signal head function correctly. This protected phase is turned "OFF" in the INIT & N.A RESPONSE programming.

SE-PAC2070 FYA 332 Base Mounted Cabinets - Protected/Permissive Phases

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### INIT & N.A. RESP PROGRAMMING DETAIL

(program controller as shown below)

From Main Menu, press '3' (Phase Data)

SE-PAC PHASE DATA

1-VEHICLE TIMES
2-DENSITY TIMES
3-PEDEST. TIMES
4-INIT & N.A. RESP
5-V & P RECALLS

PRESS # DESIRED

6-N.LOCK & MISC
7-SPEC. SEQUENCE
8-SPEC. DETECTOR
9-PHASE COPY
0-MISC PED OPTIONS

F-PRIOR MENU

INIT & N.A. RESP programming complete.

A-UP B-DN C-LT D-RT E-ENTER F-PRIOR MENU

### SE-PAC2070 FYA Init & N.A Response Programming

When using SE-PAC2070 software to implement a permitted only flashing yellow arrow movement using a three section signal head, care must be taken to ensure that the protected phase programmed in the PROTECTED AND PERMISSIVE PHASES FOR FYA is not an enabled phase in the Init & N.A. Resp Programming.

A INITIAL - Entering a "0" will turn the load switch outputs "OFF" for the selected phase. This is required for the protected phase that is programmed in the protected/permissive programming when using a three section flashing yellow arrow signal head that has no protected turn move.

SE-PAC2070 FYA 332 Base Mounted Cabinets – Init & N.A. Resp Programming

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| (                       | DASIS 2 | 2070 TI    | MING  | CHART |            |
|-------------------------|---------|------------|-------|-------|------------|
|                         |         |            | PHASE |       |            |
| FEATURE                 | 1       | 2          | 3     | 4     | 6          |
| Min Green 1 *           | 7       | 12         | 7     | 7     | 12         |
| Extension 1 *           | 3.0     | 6.0        | 1.0   | 1.0   | 6.0        |
| Max Green 1 *           | 40      | 90         | 25    | 25    | 90         |
| Yellow Clearance        | 3.0     | 4.6        | 3.7   | 4.3   | 4.6        |
| Red Clearance           | 2.1     | 1.6        | 2.7   | 2.7   | 1.6        |
| Walk 1 *                | -       | -          | -     | -     | 7          |
| Don't Wa <b>l</b> k 1   | -       | -          | -     | -     | 12         |
| Walk Advance Time *     | -       | -          | -     | -     | 5 —        |
| Seconds Per Actuation * | -       | -          | -     | -     | -          |
| Max Variable Initial*   | -       | -          | -     | -     | -          |
| Time Before Reduction * | -       | -          | -     | -     | -          |
| Time To Reduce *        | -       | -          | -     | -     | -          |
| Minimum Gap             | -       | -          | -     | -     | -          |
| Recall Mode             | -       | MIN RECALL | -     | -     | MIN RECALL |
| Vehide Call Memory      | -       | YELLOW     | -     | -     | YELLOW     |
| Dua <b>l</b> Entry      | -       | -          | -     | -     | -          |
| Simultaneous Gap        | ON      | ON         | ON    | ON    | ON         |

<sup>\*</sup> These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

#### ADVANCED WALK NOTE

(program controller as shown below)

From Main Menu press '2' (Phase Control). then '1' (Phase Control Functions). Program phase 6 for 'Advanced Walk'. Make sure the Walk Advance Time shown on the Signal Design plans are programmed in the 'Phase Timing' menu.

|                      | ,        |             | TIMIN<br>3-207 |      |     |      |             |      |      |      |
|----------------------|----------|-------------|----------------|------|-----|------|-------------|------|------|------|
| PHASE                | 02       | 02          |                |      | 05  |      | 06          |      | Ø8   |      |
| MINIMUM GREEN *      | 12       | SEC.        | 7              | SEC. | 7   | SEC. | 12          | SEC. | 7    | SEC. |
| VEHICLE EXT. *       | 6.0      | SEC.        | 2.0            | SEC. | 1.0 | SEC. | 6.0         | SEC. | -    | SEC. |
| GUAR MIN OVL GREEN   | -        | SEC.        | -              | SEC. | -   | SEC. | -           | SEC. | -    | SEC. |
| YELLOW CHANGE INT.   | 4.7      | SEC.        | 3.0            | SEC. | 3.0 | SEC. | 4.7         | SEC. | 3.0  | SEC. |
| RED CLEARANCE        | 1.9      | SEC.        | 3.2            | SEC. | 3.4 | SEC. | 1.9         | SEC. | 3.2  | SEC. |
| MAX. 1 *             | 100      | SEC.        | 60             | SEC. | 30  | SEC. | 100         | SEC. | 35   | SEC. |
| RECALL POSITION      | MIN. REC | MIN. RECALL |                | ΙE   | пои | 4E   | MIN. RECALL |      | NONE |      |
| LOCK DET.            | ОИ       | ON          |                |      | OF  | F    | 40          | 1    | OFF  |      |
| DELAYED GREEN        | -        | SEC.        | _              | SEC. | -   | SEC. | 5 -         | SEC. |      | SEC. |
| WALK *               | -        | SEC.        | _              | SEC. | -   | SEC. | 7           | SEC. | -    | SEC. |
| PED. CLEAR           | -        | SEC.        | -              | SEC. | -   | SEC. | 12          | SEC. | -    | SEC. |
| VOLUME DENSITY       | ОИ       |             | OFF            |      | OFF |      | ON          |      | OFF  |      |
| ACTUATION B4 ADD *   | -        | VEH.        | -              | VEH. | -   | VEH. | -           | VEH. | -    | VEH. |
| SEC. PER ACTUATION * | -        | SEC.        | -              | SEC. | -   | SEC. | -           | SEC. | -    | SEC. |
| MAX. INITIAL *       | -        | SEC.        | -              | SEC. | -   | SEC. | -           | SEC. | -    | SEC. |
| TIME B4 REDUCTION *  | -        | SEC.        | -              | SEC. | -   | SEC. | -           | SEC. | -    | SEC. |
| TIME TO REDUCE *     | -        | SEC.        | -              | SEC. | -   | SEC. | -           | SEC. | -    | SEC. |
| MINIMUM GAP          | -        | SEC.        | -              | SEC. | -   | SEC. | -           | SEC. | -    | SEC. |
| DUAL ENTRY           | OFF      |             | ON             |      | OF  |      | OF          |      | OF   |      |
| SIMULTANEOUS GAP     | ON       |             | ON             |      | ON  | 1    | ON          |      | ON   | 1    |

<sup>\*</sup> These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

### Leading Pedestrian Interval

Some signal designs call for a pedestrian movement that precedes its associated vehicle movement in the phase interval, i.e. the pedestrian move leads the phase interval in question. The controller will serve the pedestrian walk for a designated time while at the same time holding the associated vehicle move red, thus giving the pedestrian a "head start" into the crosswalk.

There are potential vehicle/pedestrian conflicts that may arise, depending on the exact signal design, that remove the "protection" the leading pedestrian interval is designed to offer a pedestrian. These conflicts are remedied in different ways depending on the exact configuration of the signal design and signal heads used, and the software being used in the controller.

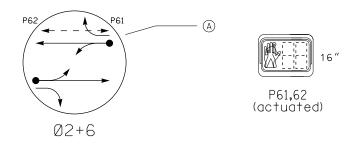
- (A) Oasis Advanced Walk Oasis software refers to the leading pedestrian interval as Advanced Walk. The absolute total pedestrian walk time is shown in the timing chart as the Walk 1 entry. The Walk Advance Time is the amount of walk time that will display on the ped head while its associated vehicle movement is being held in red. After the walk advance time has expired, the controller will display the remaining balance of Walk 1 on the ped head before timing the don't walk time. The Advance Walk Time should never be greater than Walk 1.
- Oasis Advanced Walk Note Include this note on the electrical detail for any design utilizing Oasis software that has leading pedestrian intervals.
- ASC/3-2070 Delayed Green ASC/3-2070 software refers to the leading pedestrian interval as Delayed Green. The absolute total pedestrian walk time is shown in the timing chart as the Walk entry. The Delayed Green time is the amount of walk time that will display on the ped head while its associated vehicle movement is being held in red. After the delayed green time has expired, the controller will display the remaining balance of Walk on the ped head before timing the ped clear time. The Delayed Green time should never be greater than Walk.

### Leading Pedestrian Interval

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5. Program phase 6 for Startup Ped Call.

DO NOT USE THIS NOTE FOR THIS APPLICATION!

| OASIS                   | 3 2070     | TIMIN | G CHAR     | Т   |       |
|-------------------------|------------|-------|------------|-----|-------|
|                         |            |       | PHASE      |     |       |
| FEATURE                 | 2          | 4     | 6          | 8   |       |
| Min Green 1 *           | 12         | 7     | 12         | 7   |       |
| Extension 1 *           | 6.0        | 1.0   | 6.0        | 1.0 |       |
| Max Green 1 *           | 90         | 25    | 90         | 25  |       |
| Yellow Clearance        | 4.6        | 4.3   | 4.6        | 4.3 |       |
| Red Clearance           | 1.6        | 2.7   | 1.6        | 2.7 |       |
| Walk 1 *                | -          | -     | 7          | -   |       |
| Don't Walk 1            | -          | -     | 12         | -   |       |
| Walk Advance Time *     | -          | -     | 5 —        |     | - (C) |
| Seconds Per Actuation * | -          | -     | -          | -   | _     |
| Max Variable Initial*   | -          | -     | -          | -   |       |
| Time Before Reduction * | -          | -     | -          | -   |       |
| Time To Reduce *        | -          | -     | -          | -   |       |
| Minimum Gap             | -          | -     | -          | -   |       |
| Reca <b>ll</b> Mode     | MIN RECALL | -     | MIN RECALL | -   |       |
| Vehicle Call Memory     | YELLOW     | -     | YELLOW     | -   |       |
| Dual Entry              | -          | -     | -          | -   |       |
| Simultaneous Gap        | ON         | ON    | ON         | ON  |       |

<sup>\*</sup> These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

## <u>Leading Pedestrian Interval - No Startup Ped Call</u> With Actuated Peds

Oasis software provides a phase control option that allows pedestrian movements to be called for service one time at controller startup even when no demand may exist. This applies to pedestrian moves that are push button actuated as opposed to those that may be programmed for ped recall. Specific programming instructions are found in the 'NOTES' section of the electrical detail and specify which phases should be served at startup, if any.

Pedestrian phases that have advanced walk time and are specified as the startup in green phases should NOT be programmed for startup ped calls. The reason for this is that when the controller is powered on and is coming out of flash, or if the controller is running in controller flash and is coming out of controller flash, a leading pedestrian interval on the startup phase will cause the startup phase to transition from a flashing yellow indication to a solid red indication as the leading ped interval is being timed. This transition from flashing yellow to solid red is in violation of the MUTCD and is avoided by not programming the pedestrian movement for a startup ped call.

- A Phase diagram from the signal plan illustrating the pedestrian movement on one of the main street phases, phase 6 in this example.
- ® One of the notes in the standard notes section specifies all of the phases that should be programmed for a ped call at controller startup. In order to prevent the MUTCD flash to right of way violation, this is where startup phases with ped movements that have leading pedestrian intervals should have no startup calls specified. If there is more than one pedestrian phase in use, only the phases causing a violation should be deleted from the note.
- © Oasis timing chart from the signal chart showing that the pedestrian phase should be programmed for advance walk.

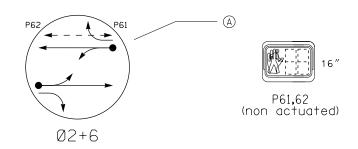
## 2070 Oasis Leading Pedestrian Interval Exceptions – No Startup Ped Call

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| OASIS                   | 3 2070     | TIMIN | G CHAR         | Т   |                         |
|-------------------------|------------|-------|----------------|-----|-------------------------|
|                         |            |       | PHASE          |     |                         |
| FEATURE                 | 2          | 4     | 6              | 8   |                         |
| Min Green 1 *           | 12         | 7     | 12             | 7   |                         |
| Extension 1 *           | 6.0        | 1.0   | 6.0            | 1.0 |                         |
| Max Green 1 *           | 90         | 25    | 90             | 25  |                         |
| Yellow Clearance        | 4.6        | 4.3   | 4.6            | 4.3 |                         |
| Red Clearance           | 1.6        | 2.7   | 1.6            | 2.7 |                         |
| Walk 1 *                | -          | -     | 7              | -   |                         |
| Don't Wa <b>l</b> k 1   | -          | -     | 12             | -   |                         |
| Walk Advance Time *     | -          | -     | 5 —            | -   | <u></u> (B)             |
| Seconds Per Actuation * | -          | -     | -              | -   |                         |
| Max Variable Initial *  | -          | -     | -              | -   |                         |
| Time Before Reduction * | -          | -     | -              | -   |                         |
| Time To Reduce *        | -          | -     | -              | -   |                         |
| Minimum Gap             | -          | -     | -              | -   |                         |
| Recall Mode             | MAX RECALL | -     | MAX/PED RECALL |     | $\vdash$ $\blacksquare$ |
| Vehicle Call Memory     | YELLOW     | -     | YELLOW         | -   |                         |
| Dual Entry              | -          | -     | -              | -   |                         |
| Simultaneous Gap        | ON         | ON    | ON             | ON  |                         |

<sup>\*</sup> These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

## <u>Leading Pedestrian Interval - No Startup Ped Call</u> With Pretimed Peds

Pretimed systems with non actuated ped movements are programmed to serve the pedestrian movement during every interval with a ped recall, and the ped movements are also served at startup. For startup ped phases with leading pedestrian intervals, the only way to omit the startup ped call is through a special phase override function and a series of logic processor statements.

- A Phase diagram from the signal plan illustrating the pedestrian movement on one of the main street phases, phase 6 in this example.
- (B) Oasis timing chart from the signal chart showing that the pedestrian phase should be programmed for advance walk and ped recall.

(continued on next page)

## 2070 Oasis Leading Pedestrian Interval Exceptions – No Startup Ped Call

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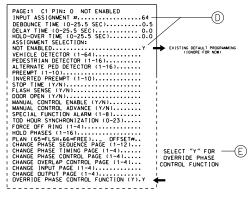
#### PROGRAMMING TO OMIT PHASE 6 —— © PEDESTRIAN OPERATION AT "STARTUP"

#### INPUT ASSIGNMENT PROGRAMMING DETAIL

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '5' (INPUTS).
- 2. WITH CURSOR IN "INPUT ASSIGNMENT #" FIELD. USE "-" KEY TO FIND THE INPUT ASSIGNMENT NUMBER 64. AS SHOWN BELOW.
- 3. PROGRAM CONTROLLER AS SHOWN:

#### STEP 1

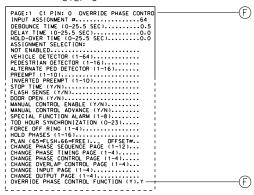


STEP 2 -

AFTER SELECTION IS MADE, THE PHASE CONTROL FUNCTIONS TABLE APPEARS. SCROLL DOWN ON THIS TABLE AND FIND "OMIT PEDESTRIAN". THEN SELECT PHASE 6 FOR "OMIT PEDESTRIAN".

AFTER SELECTION IS MADE PRESS "ESC". SCREEN NOW APPEARS AS SHOWN BELOW.

#### STEP 3



PROGRAMMING COMPLETE

### Leading Pedestrian Interval - No Startup Ped Call With Pretimed Peds (cont.)

- © OMIT PHASE AT "STARTUP" Detail The programming detail illustrates the steps required to override the ped 6 pedestrian movement at controller startup.
- (D) Input Assignment This is any controller input that is not in use that can be assigned as a 'phase override' that will be used by the logic processor to omit the ped 6 movement at controller startup.
- OVERRIDE PHASE CONTROL FUNCTION When this selection is made, the phase control screen will appear. The diagram below represents a portion of that screen where the 'OMIT PEDESTRIAN' entry is made for the desired phase.



(F) After the omit pedestrian programming phase has been entered, the programming may be verified by observing that the input function displays "OVERRIDE PHASE CONTROL" and that the "OVERRIDE PHASE CONTROL FUNCTION" has a 'Y' entered.

(continued on next page)

## 2070 Oasis Leading Pedestrian Interval Exceptions – No Startup Ped Call

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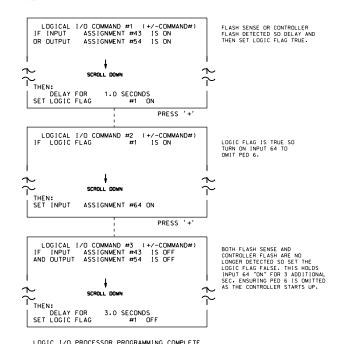
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SHEET 2 OF 3

## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO OMIT PED 6 AT STARTUP

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL). THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



## <u>Leading Pedestrian Interval - No Startup Ped Call</u> With Pretimed Peds (cont.)

⑤ In order to implement the phase control override that was programmed to omit the pedestrian movement at startup, a series of logic processor steps must be programmed. The controller will check to see if flash sense is "ON" (cabinet flash) or controller flash is "ON", and if so the controller will turn input 64 "ON" so that the desired pedestrian movement will be omitted when the controller comes out of flash and starts running. A delay is built in to hold input 64 "ON" for three seconds after the controller starts to prevent the controller from possibly skipping the ped omit due to a controller internal race condition.

### OUTPUT REFERENCE SCHEDULE USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 54 = Controller Flash INPUT 43 = Flash Sense INPUT 64 = Omit Ped 6

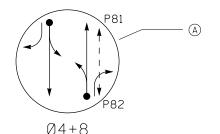
Note: See sheet x for input 64 Input Assignment details.

## 2070 Oasis Leading Pedestrian Interval Exceptions – No Startup Ped Call

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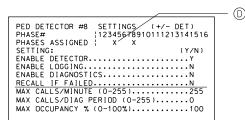


| OASIS                   | 3 2070     | TIMIN | G CHAR     | T   |       |    |
|-------------------------|------------|-------|------------|-----|-------|----|
|                         |            |       | PHASE      |     |       |    |
| FEATURE                 | 2          | 4     | 6          | 8   |       |    |
| Min Green 1 *           | 12         | 7     | 12         | 7   |       |    |
| Extension 1 *           | 6.0        | 1.0   | 6.0        | 1.0 |       |    |
| Max Green 1 *           | 90         | 25    | 90         | 25  |       |    |
| Yellow Clearance        | 4.6        | 4.3   | 4.6        | 4.3 |       |    |
| Red Clearance           | 1.6        | 2.7   | 1.6        | 2.7 |       |    |
| Walk 1 *                | -          | 7     | -          | 7 — | ├ (B) |    |
| Don't Wa <b>l</b> k 1   | -          | 12    | -          | 12  |       | 3) |
| Walk Advance Time *     | -          | 5     | -          | 5   |       |    |
| Seconds Per Actuation * | 1.5        | -     | 1.5        | -   |       |    |
| Max Variable Initial *  | 34         | -     | 34         | -   |       |    |
| Time Before Reduction * | 15         | -     | 15         | -   |       |    |
| Time To Reduce *        | 45         | -     | 45         | -   |       |    |
| Minimum Gap             | 3.0        | -     | 3.0        | -   |       |    |
| Reca <b>ll</b> Mode     | MIN RECALL | -     | MIN RECALL | -   |       |    |
| Vehicle Call Memory     | YELLOW     | -     | YELLOW     | -   |       |    |
| Dual Entry              | -          | -     | -          | -   |       |    |
| Simultaneous Gap        | ON         | ON    | ON         | ON  |       |    |

<sup>\*</sup> These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

## PEDESTRIAN DETECTOR ASSIGNMENT PROGRAMMING DETAIL (program controller as shown below)

FROM MAIN MENU PRESS '7' (DETECTORS). THEN '2' (PEDESTRIAN DETECTOR ASSIGNMENTS). PRESS '+' UNTIL PED DETECTOR # 8 IS REACHED.



### Leading Pedestrian Interval - Opposing Dummy Ped Phase

When an adjacent through vehicle movement is being held red during a leading ped interval, care must be taken to ensure that no other vehicle movement could encroach upon a pedestrian in the crosswalk. In the phase diagram shown to the left, phase 8 vehicles will be held in red during the advanced walk period but a phase 4 vehicle would not be held in red. This means a phase 4 vehicle could potentially make a permitted left turn into the crosswalk during the advanced walk period.

The remedy for this situation is to create a dummy ped movement for phase 4. All phase 4 ped times will be identical to those of phase 8, including the walk advance time. There will be no actual ped signal heads for phase 4. The ped push buttons for ped 8 will have to be programmed to call ped 4 and ped 8 when pressed. What this does is hold vehicle phase 4 red for the same walk advance time as phase 8 giving the pedestrian a leading ped interval without the possibility of vehicle interference. The vehicle phase 4 heads will turn green at the end of the walk advance time just like the phase 8 vehicle heads.

- A Phase diagram from the signal plan illustrating the pedestrian movement on phase 8 on the side street, and the opposing vehicle move phase 4 with a permitted left turn and no ped movement.
- (B) Oasis timing chart from the signal chart showing that the pedestrian phase should be programmed for advance walk.
- $\bigcirc$  Dummy ped times assigned to phase 4, identical to those for phase 8.
- ① Pedestrian Detector Assignment Programming Detail This programming screen assigns the specific ped phases that will be called by the ped detector buttons. For ped detector 8, be sure to include ped phase 4 to run the dummy ped phase.

## 2070 Oasis Leading Pedestrian Interval Exceptions – Opposing Dummy Ped Phase

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SHEET 1 OF 2

| OASI                    | S 2070     | TIMING | G CHAR      | Т   |
|-------------------------|------------|--------|-------------|-----|
|                         |            |        | PHASE       |     |
| FEATURE                 | 2          | 4      | 6           | 8   |
| Min Green 1 *           | 12         | 7      | 12          | 7   |
| Extension 1 *           | 6.0        | 1.0    | 6.0         | 1.0 |
| Max Green 1 *           | 90         | 25     | 90          | 25  |
| Yellow Clearance        | 4.6        | 4.3    | 4.6         | 4.3 |
| Red Clearance           | 1.6        | 2.7    | 1.6         | 2.7 |
| Walk 1 *                | -          | 7      | -           | 7   |
| Don't Wa <b>l</b> k 1   | -          | 12     | <del></del> | 12  |
| Walk Advance Time *     | -          | 5      | -           | 5   |
| Seconds Per Actuation * | 1.5        | -      | 1.5         | -   |
| Max Variable Initial*   | 34         | -      | 34          | -   |
| Time Before Reduction * | 15         | -      | 15          | -   |
| Time To Reduce *        | 45         | -      | 45          | -   |
| Minimum Gap             | 3.0        | -      | 3.0         | -   |
| Reca <b>ll</b> Mode     | MIN RECALL | -      | MIN RECALL  | -   |
| Vehicle Call Memory     | YELLOW     | -      | YELLOW      | -   |
| Dual Entry              | 1          | -      |             | -   |
| Simultaneous Gap        | ON         | ON     | ON          | ON  |

<sup>\*</sup> These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

#### PEDESTRIAN DETECTOR ASSIGNMENT PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '7' (DETECTORS). THEN '2' (PEDESTRIAN DETECTOR ASSIGNMENTS). PRESS '+' UNTIL PED DETECTOR # 4 IS REACHED.

| İ                             |        |
|-------------------------------|--------|
| PHASE#  12345678910111213     | 141516 |
| PHASES ASSIGNED   X X         |        |
| SETTING:                      | (Y/N)  |
| ENABLE DETECTOR               |        |
| ENABLE LOGGING                |        |
| ENABLE DIAGNOSTICS            | N      |
| RECALL IF FAILED              |        |
| MAX CALLS/MINUTE (0-255)      | 255    |
| MAX CALLS/DIAG PERIOD (0-255) | 0      |
| MAX OCCUPANCY % (0-100%)      | 100    |
|                               |        |

#### PEDESTRIAN DETECTOR ASSIGNMENT PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '7' (DETECTORS). THEN '2' (PEDESTRIAN DETECTOR ASSIGNMENTS). PRESS '+' UNTIL PED DETECTOR # 8 IS REACHED.

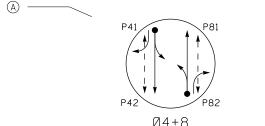
| PHASE#  12345678910111213141516 |
|---------------------------------|
| PHASES ASSIGNED   X X           |
| SETTING: (Y/N)                  |
| ENABLE DETECTOR                 |
| ENABLE LOGGINGN                 |
| ENABLE DIAGNOSTICS              |
| RECALL IF FAILEDN               |
| MAX CALLS/MINUTE (0-255)0       |
| MAX OCCUPANCY % (0-100%)100     |
| MAX UCCUPANCE % (U-100%)100     |

#### Leading Pedestrian Interval - Opposing Ped Phase

When an adjacent through vehicle movement is being held red during a leading ped interval, care must be taken to ensure that no other vehicle movement could encroach upon a pedestrian in the crosswalk. In the phase diagram shown below, if one ped move is being served but the other is not, vehicles with the advance walk will be held in red for the advance walk period. The opposing vehicle move, however, would be free to make a permitted left turn and possibly encroach upon a pedestrian during the leading ped interval.

The remedy for this situation is to program the ped buttons to call their respective phases and to also call the opposing ped phase. The result is that both vehicle moves will be held in red during the advance walk time whenever a ped call is placed even if the ped call is not on the same approach as the vehicle move.

- (A) Oasis timing chart from the signal plan showing that the pedestrian phases should be programmed for advance walk.
- Pedestrian Detector Assignment Programming Detail This programming screen assigns the specific ped phases that will be called by the ped detector buttons. Each ped detector is programmed to call its own ped phase as well as the opposing ped phase.
- © Phase diagram from the signal plan illustrating the pedestrian movements on phases 4 and 8 on the side street, and the opposing vehicle moves that each have a through move and a permitted left turn move.









(actuated)

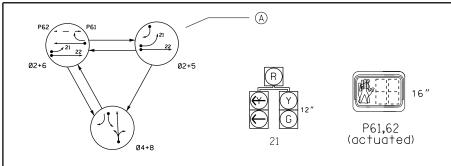
(actuated)

## 2070 Oasis Leading Pedestrian Interval Exceptions – Opposing Ped Phases

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

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SHEET 2 OF 2



| (                       | DASIS 2    | 2070 T | IMING | CHART      |     |   |     |
|-------------------------|------------|--------|-------|------------|-----|---|-----|
|                         |            |        | PHASE |            |     |   |     |
| FEATURE                 | 2          | 4      | 5     | 6          | 8   |   |     |
| Min Green 1 *           | 12         | 7      | 7     | 12         | 7   |   |     |
| Extension 1 *           | 6.0        | 1.0    | 1.0   | 6.0        | 1.0 |   |     |
| Max Green 1 *           | 90         | 25     | 25    | 90         | 25  |   |     |
| Yellow Clearance        | 4.6        | 4.3    | 4.3   | 4.6        | 4.3 |   |     |
| Red Clearance           | 1.6        | 2.7    | 2.7   | 1.6        | 2.7 |   |     |
| Walk 1 *                | 7          | -      | -     | 7 —        |     | B |     |
| Don't Wa <b>l</b> k 1   | 12 —       |        | -     | 12         |     |   | - ( |
| Walk Advance Time *     | \5/        | -      | -     | 5          | -   |   | •   |
| Seconds Per Actuation * | 1.5        | -      | -     | 1.5        | -   |   |     |
| Max Variable Initial*   | 34         | -      | -     | 34         | -   |   |     |
| Time Before Reduction * | 15         | -      | -     | 15         | -   |   |     |
| Time To Reduce *        | 45         | -      | -     | 45         | -   |   |     |
| Minimum Gap             | 3.0        | -      | -     | 3.0        | -   |   |     |
| Recall Mode             | MIN RECALL | -      | -     | MIN RECALL | -   |   |     |
| Vehicle Call Memory     | YELLOW     | -      | -     | YELLOW     | -   |   |     |
| Dual Entry              | -          | -      | -     | -          | -   |   |     |
| Simultaneous Gap        | ON         | ON     | ON    | ON         | ON  | 1 |     |

<sup>\*</sup> These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

### BACKUP PROTECTION NOTE - ①

(program controller as shown below)

From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Program phase 2 for 'Backup Protect'. Make sure the Red Revert times shown on the Signal Design Plans are programmed in the 'Phase Timing' menu.

### Leading Pedestrian Interval - Five Section Heads

When an adjacent through vehicle movement is being held red during a leading ped interval, care must be taken to ensure that no other vehicle movement could encroach upon a pedestrian in the crosswalk. In the phase diagram shown to the left, phase 6 vehicles will be held in red during the advanced walk period but a phase 2 vehicle would not be held in red. This means a phase 2 vehicle could potentially make a permitted left turn into the crosswalk during the advance walk period.

The remedy for this situation is to create a dummy ped movement for phase 2. All phase 2 ped times will be identical to those of phase 6, including the walk advance time. There will be no actual ped signal heads for phase 2. Logic is used to place a call to ped 2 when there is a call on ped 6.

In this phasing arrangement, phase 5 must always lag and all red backup protect for phase 2 must be programmed. This ensures that the leading pedestrian interval will run correctly.

- A Phase diagram from the signal plan illustrating the pedestrian movement on phase 6 on the main street, and the opposing vehicle move phase 2 with five section protected and permitted left turn head and no pedestrian movement.
- ® Oasis timing chart from the signal chart showing that the pedestrian phase should be programmed for advance walk.
- $\ensuremath{\bigcirc}$  Dummy ped times assigned to phase 2, identical to those for phase 6.
- Backup Protection Note Make sure this note is on the electrical detail when five section heads are used in this type of leading pedestrian interval application.

(continued on next page)

## 2070 Oasis Leading Pedestrian Interval Exceptions – Five Section Heads

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SIGNALS MANAGEMENT

12.4

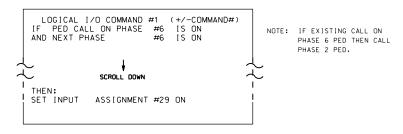
TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

SHEET 1 OF 2

## LOGICAL I/O PROCESSOR PROGRAMMING TO CALL PHASE 2 DUMMY PED WHEN PHASE 6 PED IS CALLED

#### (program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMAND 1.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

#### **OUTPUT REFERENCE SCHEDULE**

INPUT 29 = Phase 2 PED Call

### Leading Pedestrian Interval - Five Section Heads

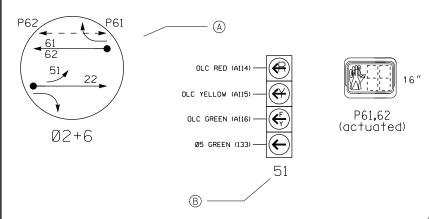
© Logic processor programming to call the phase 2 dummy ped. This logic ensures the dummy ped call on phase 2 is served at the appropriate time. Without this logic, the dummy ped call on phase 2 could be served before the ped call on phase 6.

2070 Oasis Leading Pedestrian Interval Exceptions – Five Section Heads

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

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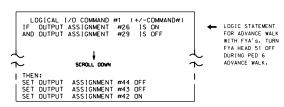
SHEET 2 OF 2



#### LOGICAL I/O PROCESSOR PROGRAMMING FOR FYA SUPPRESSION DURING THE ADVANCE WALK PERIOD

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMAND 1.
- FROM MAIN MENU PRESS '6' (OUTPUTS). THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

#### OUTPUT REFERENCE SCHEDULE

OUTPUT 26 = 6 PED Walk
OUTPUT 29 = Vehicle 6 Green
OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green

### Leading Pedestrian Interval - Flashing Yellow Arrows

When an adjacent through vehicle movement is being held red during a leading ped interval, care must be taken to ensure that no other vehicle movement could encroach upon a pedestrian in the crosswalk. When flashing yellow arrows are being used for the vehicle approach that opposes the ped move, care must be taken to suppress the flashing yellow arrow output, which is the permitted movement, during the leading ped interval. The logic processor is used to accomplish this as shown on this sheet. The same logic is applied to main street and side street three section permitted only flashing yellow arrows. The phase diagram shown to the left is used for the explanation.

- A Phase diagram from a signal plan illustrating the pedestrian movement on phase 6 on the main street, and the opposing vehicle move phase 2 with no ped and a protected/permitted left turn via the flashing yellow arrow. Note that the ped 6 movement can be omitted at startup by omitting it as a startup ped call, thus avoiding the MUTCD startup violation.
- Signal head 51 is a protected and permitted flashing yellow arrow that has overlap parent phases of 5+6 (phase 6 is the opposing through move).
- © To suppress the signal head 51 flashing yellow arrow during the leading pedestrian interval, the logic processor is required. When ped 6 is timing the advanced walk, the phase 6 vehicle move is held red. When the logic processor sees that the ped 6 movement is "ON" and the phase 6 vehicle move is "OFF", it prevents the flashing yellow arrow from turning on by holding the overlap red (head 51) while at the same time allowing the phase 2 through movement (head 22) to be served. After the walk advance time has expired, the logic statement is no longer TRUE and the phase 6 vehicle movement will turn "ON", at which time the flashing yellow arrow signal face will also turn "ON" and begin to flash.

This logic is used whenever a flashing yellow signal head opposes a pedestrian movement that has a leading pedestrian interval whether it happens to be a three section permitted only or a four section protected and permitted head.

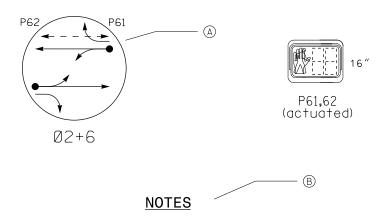
## 2070 Oasis Leading Pedestrian Interval Exceptions – Flashing Yellow Arrows

SIGNALS MANAGEMENT
TRANSPORTATION MOBILITY AND SAFETY DIVISION
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

STD. NO.

12.5

SHEET 1 OF 1



Program controller to start up in phase 2 Green and 6 Green.

NOTE THAT PHASE 6 DOES NOT START IN WALK!

|                      |             | TIMING (<br>/3-2070 C |          |             |          |  |
|----------------------|-------------|-----------------------|----------|-------------|----------|--|
| PHASE                | Ø2          | 04                    | Ø5       | Ø6          | Ø8       |  |
| MINIMUM GREEN *      | 12 SEC.     | 7 SEC.                | 7 SEC.   | 12 SEC.     | 7 SEC.   |  |
| VEHICLE EXT. *       | 6.0 SEC.    | 2.0 SEC.              | 1.0 SEC. | 6.0 SEC.    | - SEC.   |  |
| GUAR MIN OVL GREEN   | - SEC.      | - SEC.                | - SEC.   | - SEC.      | - SEC.   |  |
| YELLOW CHANGE INT.   | 4.7 SEC.    | 3.0 sec.              | 3.0 SEC. | 4.7 SEC.    | 3.0 SEC. |  |
| RED CLEARANCE        | 1.9 SEC.    | 3.2 SEC.              | 3.4 SEC. | 1.9 SEC.    | 3.2 SEC. |  |
| MAX. 1 *             | 100 SEC.    | 60 sec.               | 30 SEC.  | 100 SEC.    | 35 SEC.  |  |
| RECALL POSITION      | MIN. RECALL | NONE                  | NONE     | MIN. RECALL | NONE     |  |
| LOCK DET.            | ON          | OFF                   | OFF      | ON          | OFF      |  |
| DELAYED GREEN        | - SEC.      | - SEC.                | - SEC.   | 5 —sec.     | - SEC.   | <del>                                     </del> |
| WALK *               | - SEC.      | - SEC.                | - SEC.   | 7 SEC.      | - SEC.   | `  |
| PED. CLEAR           | - SEC.      | - SEC.                | - SEC.   | 12 SEC.     | - SEC.   |  |
| VOLUME DENSITY       | ON          | OFF                   | OFF      | ON          | OFF      |  |
| ACTUATION B4 ADD *   | - VEH.      | - VEH.                | - VEH.   | - VEH.      | - VEH.   |  |
| SEC. PER ACTUATION * | - SEC.      | - SEC.                | - SEC.   | - SEC.      | - SEC.   |  |
| MAX. INITIAL *       | - SEC.      | - SEC.                | - SEC.   | - SEC.      | - SEC.   |  |
| TIME B4 REDUCTION *  | - SEC.      | - SEC.                | - SEC.   | - SEC.      | - SEC.   |  |
| TIME TO REDUCE *     | - SEC.      | - SEC.                | - SEC.   | - SEC.      | - SEC.   |  |
| MINIMUM GAP          | - SEC.      | - SEC.                | - SEC.   | - SEC.      | - SEC.   |  |
| DUAL ENTRY           | OFF         | ON                    | OFF      | OFF         | OFF      |  |
| SIMULTANEOUS GAP     | ON          | ON                    | ON       | ON          | ON       |  |

\* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

## <u>Leading Pedestrian Interval - Startup in Green</u> With Actuated Peds

ASC/3-2070 software provides a startup option that allows the controller to start the specified phases in Green, or to start the specified phases in Walk if they have ped movements. Specific programming instructions regarding startup are found in the 'NOTES' section of the electrical detail.

To eliminate a potential MUTCD violation at controller startup, any startup phase with a ped movement that also has a delayed green time (the leading pedestrian interval) should be programmed to start in Green and not in Walk. This ensures the leading pedestrian interval will not be served at startup.

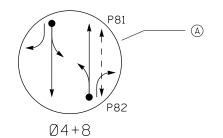
- A Phase diagram from the signal plan illustrating the pedestrian movement on one of the main street phases, phase 6 in this example.
- ® One of the notes in the standard notes section specifies how the startup phases should be programmed. In order to prevent the MUTCD flash to right of way violation, this is where startup phases with ped movements that have leading pedestrian intervals should be programmed to start in Green and not in Walk. If there is more than one pedestrian phase in use, only the phases causing violation should be specified to start in Green. Do this for both actuated and pretimed locations.
- © ASC/3-2070 timing chart from the signal chart showing that the phase 6 should be programmed for delayed green.

ASC/3-2070 Leading Pedestrian Interval Exceptions - Startup in Green

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

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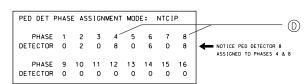
|                      |         |             |        |      | CHART |      |             |      |      |      |  |       |      |
|----------------------|---------|-------------|--------|------|-------|------|-------------|------|------|------|--|-------|------|
| PHASE                | 02      |             | 04     |      | 05    |      | Ø6          |      | 08   |      |  |       |      |
| MINIMUM GREEN *      | 12      | SEC.        | 7      | SEC. | 7     | SEC. | 12          | SEC. | 7    | SEC. |  |       |      |
| VEHICLE EXT. *       | 6.0     | SEC.        | 2.0    | SEC. | 1.0   | SEC. | 6.0         | SEC. | -    | SEC. |  |       |      |
| GUAR MIN OVL GREEN   | -       | SEC.        | -      | SEC. | -     | SEC. | -           | SEC. | -    | SEC. |  |       |      |
| YELLOW CHANGE INT.   | 4.7     | SEC.        | 3.0    | SEC. | 3.0   | SEC. | 4.7         | SEC. | 3.0  | SEC. |  |       |      |
| RED CLEARANCE        | 1.9     | SEC.        | 3.2    | SEC. | 3.4   | SEC. | 1.9         | SEC. | 3.2  | SEC. |  |       |      |
| MAX. 1 *             | 100     | SEC.        | 60     | SEC. | 30    | SEC. | 100         | SEC. | 35   | SEC. |  |       |      |
| RECALL POSITION      | MIN. RE | MIN. RECALL |        | NE   | пои   | 4E   | MIN. RECALL |      | NONE |      |  |       |      |
| LOCK DET.            | ON      | ON          |        | OFF  |       | OFF  |             | ON   |      | F    |  | _     |      |
| DELAYED GREEN        | -       | SEC.        | [ /5 \ | SEC. | -     | SEC. | -           | SEC. | 5 -  | SEC. |  | - (B) | _    |
| WALK *               | -       | SEC.        | 7      | SEC. |       | SEC. |             | SEC. | 7    | SEC. |  |       | -(c) |
| PED. CLEAR           | -       | SEC.        | 12/    | SEC. | -     | SEC. | -           | SEC. | 12   | SEC. |  |       | _    |
| VOLUME DENSITY       | ON      | ı           | ) OF   | F    | OFF   | :    | 01          | ı    | OF   | F    |  |       |      |
| ACTUATION B4 ADD *   | -       | VEH.        | -      | VEH. | -     | VEH. | -           | VEH. | -    | VEH. |  |       |      |
| SEC. PER ACTUATION * | -       | SEC.        | -      | SEC. | -     | SEC. | -           | SEC. | -    | SEC. |  |       |      |
| MAX. INITIAL *       | -       | SEC.        | -      | SEC. | -     | SEC. | -           | SEC. | -    | SEC. |  |       |      |
| TIME B4 REDUCTION *  | -       | SEC.        | -      | SEC. | -     | SEC. | -           | SEC. | -    | SEC. |  |       |      |
| TIME TO REDUCE *     | -       | SEC.        | -      | SEC. | -     | SEC. | -           | SEC. | -    | SEC. |  |       |      |
| MINIMUM GAP          | -       | SEC.        | -      | SEC. | -     | SEC. | -           | SEC. | -    | SEC. |  |       |      |
| DUAL ENTRY           | OFF     | =           | 10     | 7    | OFF   |      | OF          | F    | OF   | F    |  |       |      |
| SIMULTANEOUS GAP     | ON      | ON          |        | ON   |       | ON   |             | ON   |      | ON   |  |       |      |

#### \* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6

## ECONOLITE ASC/3-2070 PED 8 PROGRAMMING ASSIGNMENT DETAIL

(program controller as shown)

- 1. From Main Menu select 6. DETECTORS
- 2. From DETECTOR Submenu select 3. PED DETECTOR INPUT ASSIGNMENT



### <u>Leading Pedestrian Interval - Opposing Dummy Ped Phase</u>

When an adjacent through vehicle movement is being held red during a leading ped interval, care must be taken to ensure that no other vehicle movement could encroach upon a pedestrian in the crosswalk. In the phase diagram shown to the left, phase 8 vehicles will be held in red during the delayed green period but a phase 4 vehicle would not be held in red. This means a phase 4 vehicle could potentially make a permitted left turn into the crosswalk during the delayed green period.

The remedy for this situation is to create a dummy ped movement for phase 4. All phase 4 ped times will be identical to those of phase 8, including the delayed green time. There will be no actual ped signal heads for phase 4. The ped push buttons for ped 8 will have to be programmed to call ped 4 and ped 8 when pressed. What this does is hold vehicle phase 4 red for the same delayed green time as phase 8 giving the pedestrian a leading ped interval without the possibility of vehicle interference. The vehicle phase 4 heads will turn green at the end of the delayed green time just like the phase 8 vehicle heads.

- A Phase diagram from the signal plan illustrating the pedestrian movement on phase 8 on the side street, and the opposing vehicle move phase 4 with a permitted left turn and no ped movement.
- (B) ASC/3-2070 timing chart from the signal chart showing that the pedestrian phase should be programmed for delayed green.
- $\bigcirc$  Dummy ped times assigned to phase 4, identical to those for phase 8.
- ① Pedestrian Detector Assignment Programming Detail This programming screen assigns the specific ped phases that will be called by the ped detector buttons. For ped detector 8, be sure to include ped phase 4 to run the dummy ped phases.

## ASC/3-2070 Leading Pedestrian Interval Exceptions - Opposing Dummy Ped Phase

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TRANSPORTATION MOBILITY AND SAFETY DIVISION
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

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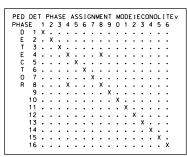
|                      |         |             |     |      | CHAR1 |      |         |      |     |      |  |
|----------------------|---------|-------------|-----|------|-------|------|---------|------|-----|------|--|
| PHASE                | 02      | 2           | 04  |      | 05    |      | Ø6      |      | 08  | ,    |  |
| MINIMUM GREEN *      | 12      | SEC.        | 7   | SEC. | 7     | SEC. | 12      | SEC. | 7   | SEC. |  |
| VEHICLE EXT. *       | 6.0     | SEC.        | 2.0 | SEC. | 1.0   | SEC. | 6.0     | SEC. | -   | SEC. |  |
| GUAR MIN OVL GREEN   | -       | SEC.        | -   | SEC. | -     | SEC. | -       | SEC. | -   | SEC. |  |
| YELLOW CHANGE INT.   | 4.7     | SEC.        | 3.0 | SEC. | 3.0   | SEC. | 4.7     | SEC. | 3.0 | SEC. |  |
| RED CLEARANCE        | 1.9     | SEC.        | 3.2 | SEC. | 3.4   | SEC. | 1.9     | SEC. | 3.2 | SEC. |  |
| MAX. 1 *             | 100     | SEC.        | 60  | SEC. | 30    | SEC. | 100     | SEC. | 35  | SEC. |  |
| RECALL POSITION      | MIN. RE | MIN. RECALL |     | ΝE   | NON   | ٧E   | MIN. RE | CALL | 101 | ΝE   |  |
| LOCK DET.            | 10      | ON          |     | F_   | OF    | OFF  |         | ИО   |     | F/   |  |
| DELAYED GREEN        | -       | SEC.        | 5 ′ | SEC. | -     | SEC. | -       | SEC. | 5 ′ | SEC. |  |
| WALK *               | -       | SEC.        | 7   | SEC. | -     | SEC. | -       | SEC. | 7   | SEC. |  |
| PED. CLEAR           | -       | SEC.        | 12  | SEC. | -     | SEC. | -       | SEC. | 12  | SEC. |  |
| VOLUME DENSITY       | 10      | 1           | OF  | F    | OF    | F    | 10      | 1    | OF  | F    |  |
| ACTUATION B4 ADD *   | -       | VEH.        | -   | VEH. | -     | VEH. | -       | VEH. | -   | VEH. |  |
| SEC. PER ACTUATION * | -       | SEC.        | -   | SEC. | -     | SEC. | -       | SEC. | -   | SEC. |  |
| MAX. INITIAL *       | -       | SEC.        | -   | SEC. | -     | SEC. | -       | SEC. | -   | SEC. |  |
| TIME B4 REDUCTION *  | -       | SEC.        | -   | SEC. | -     | SEC. | -       | SEC. | ı   | SEC. |  |
| TIME TO REDUCE *     | -       | SEC.        | -   | SEC. | -     | SEC. | -       | SEC. | ı   | SEC. |  |
| MINIMUM GAP          | -       | SEC.        | -   | SEC. | -     | SEC. | -       | SEC. | -   | SEC. |  |
| DUAL ENTRY           | OF      | OFF         |     | ON   |       | OFF  |         | OFF  |     | OFF  |  |
| SIMULTANEOUS GAP     | 10      | ON          |     | ON   |       | ON   |         | ON   |     | ON   |  |

<sup>\*</sup> These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

## ECONOLITE ASC/3-2070 PEDESTRIAN DETECTOR PHASE ASSIGNMENT PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 6. DETECTORS
- 2. From DETECTOR Submenu select 3. PED DETECTOR INPUT ASSIGNMENT
- 3. Press the TOGGLE key to select ECONOLITE MODE and press ENTER.



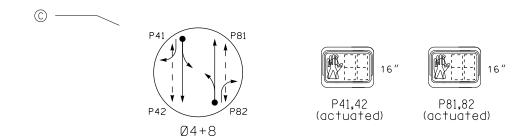
- "." = No assignment, disabled
- X = Assigns Pedestrian Push Button (PPB) to call the phase or phases
- 2 = Call for Ped timing 2
- B = Allows for the PPB to call for Min Green 2 (BIKE GREEN)

### <u>Leading Pedestrian Interval - Opposing Ped Phases</u>

When an adjacent through vehicle movement is being held red during a leading ped interval, care must be taken to ensure that no other vehicle movement could encroach upon a pedestrian in the crosswalk. In the phase diagram shown below, if one ped move is being served but the other is not, vehicles with the delayed green will be held in red for the delayed green period. The opposing vehicle move, however, would be free to make a permitted left turn and possibly encroach upon a pedestrian during the leading ped interval.

The remedy for this situation is to program the ped buttons to call their respective phases and to also call the opposing ped phase. The result is that both vehicle moves will be held in red during the delayed green whenever a ped call is placed even if the ped call is not on the same approach as the vehicle move.

- (A) ASC/3-2070 timing chart from the signal plan showing that the pedestrian phase should be programmed for delayed green.
- B Pedestrian Detector Assignment Programming Detail This programming screen assigns the specific ped phases that will be called by the ped detector buttons. Each ped detector is programmed to call its own ped phase as well as the opposing ped phase.
- © Phase diagram from the signal plan illustrating the pedestrian movements on phases 4 and 8 on the side street, and the opposing vehicle moves that each have a through move and a permitted left turn move.

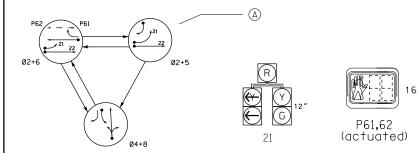


## ASC/3-2070 Leading Pedestrian Interval Exceptions - Opposing Ped Phases

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

12.7

SHEET 2 OF 2



|                      | TIMING CHART<br>ASC/3-2070 CONTROLLER |          |          |             |          |               |  |  |  |  |
|----------------------|---------------------------------------|----------|----------|-------------|----------|---------------|--|--|--|--|
| PHASE                | 02                                    | 04       | 05       | Ø6          | Ø8       |               |  |  |  |  |
| MINIMUM GREEN *      | 12 sec.                               | 7 SEC.   | 7 SEC.   | 12 SEC.     | 7 SEC.   |               |  |  |  |  |
| VEHICLE EXT. *       | 6.0 SEC.                              | 2.0 SEC. | 1.0 SEC. | 6.0 SEC.    | - SEC.   |               |  |  |  |  |
| GUAR MIN OVL GREEN   | - SEC.                                | - SEC.   | - SEC.   | - SEC.      | - SEC.   |               |  |  |  |  |
| YELLOW CHANGE INT.   | 4.7 SEC.                              | 3.0 SEC. | 3.0 SEC. | 4.7 SEC.    | 3.0 SEC. |               |  |  |  |  |
| RED CLEARANCE        | 1.9 SEC.                              | 3.2 SEC. | 3.4 SEC. | 1.9 SEC.    | 3.2 SEC. |               |  |  |  |  |
| MAX. 1 *             | 100 SEC.                              | 60 SEC.  | 30 SEC.  | 100 SEC.    | 35 SEC.  |               |  |  |  |  |
| RECALL POSITION      | MIN. RECALL                           | NONE     | NONE     | MIN. RECALL | NONE     |               |  |  |  |  |
| LOCK DET.            | ON                                    | OFF      | OFF      | ON          | OFF      |               |  |  |  |  |
| DELAYED GREEN        | √5 \ sec.                             | - SEC.   | - SEC.   | 5 — sec.    | - SEC.   | <u></u> ■ B _ |  |  |  |  |
| WALK *               | 7 -sec.                               | - SEC.   | - SEC.   | 7 SEC.      | - SEC.   | - $$          |  |  |  |  |
| PED. CLEAR           | 12 SEC.                               | - SEC.   | - SEC.   | 12 SEC.     | - SEC.   |               |  |  |  |  |
| VOLUME DENSITY       | ON                                    | OFF      | OFF      | ON          | OFF      |               |  |  |  |  |
| ACTUATION B4 ADD *   | - VEH.                                | - VEH.   | – VEH.   | - VEH.      | – VEH.   |               |  |  |  |  |
| SEC. PER ACTUATION * | – SEC.                                | - SEC.   | – SEC.   | – SEC.      | – SEC.   |               |  |  |  |  |
| MAX. INITIAL *       | - SEC.                                | - SEC.   | - SEC.   | - SEC.      | - SEC.   |               |  |  |  |  |
| TIME B4 REDUCTION *  | – SEC.                                | - SEC.   | - SEC.   | – SEC.      | – SEC.   |               |  |  |  |  |
| TIME TO REDUCE *     | – SEC.                                | - SEC.   | - SEC.   | – SEC.      | – SEC.   |               |  |  |  |  |
| MINIMUM GAP          | - SEC.                                | - SEC.   | - SEC.   | - SEC.      | - SEC.   |               |  |  |  |  |
| DUAL ENTRY           | OFF                                   | ON       | OFF      | OFF         | OFF      |               |  |  |  |  |
| SIMULTANEOUS GAP     | ОИ                                    | ON       | ON       | ON          | ON       |               |  |  |  |  |

<sup>\*</sup> These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

### Leading Pedestrian Interval - Five Section Heads

When an adjacent through vehicle movement is being held red during a leading ped interval, care must be taken to ensure that no other vehicle movement could encroach upon a pedestrian in the crosswalk. In the phase diagram shown to the left, phase 6 vehicles will be held in red during the delayed green period but a phase 2 vehicle would not be held in red. This means a phase 2 vehicle could potentially make a permitted left turn into the crosswalk during the delayed green period.

The remedy for this situation is to create a dummy ped movement for phase 2. All phase 2 ped times will be identical to those of phase 6, including the delayed green time. There will be no actual ped signal heads for phase 2. Logic is used to place a call to ped 2 when there is a call on ped 6.

In this phasing arrangement, phase 5 must always lag and all red backup protect for phase 2 must be programmed. This ensures that the leading pedestrian interval will run correctly.

- A Phase diagram from the signal plan illustrating the pedestrian movement on phase 6 on the main street, and the opposing vehicle move phase 2 with five section protected and permitted left turn head and no pedestrian movement.
- (B) ASC/3-2070 timing chart from the signal chart showing that the pedestrian phase should be programmed for advance walk.
- © Dummy ped times assigned to phase 2, identical to those for phase 6.

(continued on next page)

## ASC/3-2070 Leading Pedestrian Interval Exceptions - Five Section Heads

STD. NO.

SIGNALS MANAGEMENT

12.8

TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

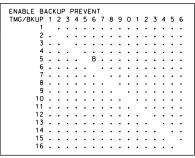
SHEET 1 OF 2

#### ECONOLITE ASC/3-2070 BACKUP PROTECTION ENABLE PROGRAMMING

(program controller as shown)

- 1. From Main Menu select | 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 1. CONTROLLER SEQ
- 3. From CONTROLLER SEQUENCE Submenu select 3. BACKUP PREVENT PHASES

Follow programming as shown below. On the 'ENABLE BACKUP PREVENT' screen move cursor to the appropriate field and press 'YES/ND' on the controller keypad to toggle field value between 'X' ','B', 'C' and 'OFF'.



END PROGRAMMING

### Leading Pedestrian Interval - Five Section Heads

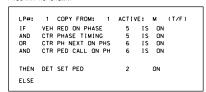
- ① Backup Protection Note This programming will ensure that the controller will not progress from 2+5 to 2+6 without first going to all red.
- © Logic processor programming to call the phase 2 dummy ped. This logic ensures the dummy ped call on phase 2 is served at the appropriate time.

## ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING TO CALL PHASE 2 DUMMY PED WHEN PHASE 6 PED IS CALLED

(program controller as shown)

- 1. From Main Menu select 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
- 3. From LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD. PRESS 'ENTER'. AND PROGRAM AS SHOWN.



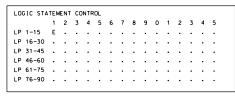
END PROGRAMMING

NOTE: IF CONTROLLER IS IN 2+5 GOING TO 2+6. OR IN 4+8 GOING TO 2+6. AND A PED CALL EXISTS ON PHASE 6. PUT DUMMY PED CALL ON PHASE 2. 1. From Main Menu select 1. CONFIGURATION

2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR

3. From LOGIC PROCESSOR Submenu select 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENT 1 BY POSITIONING THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE IT.



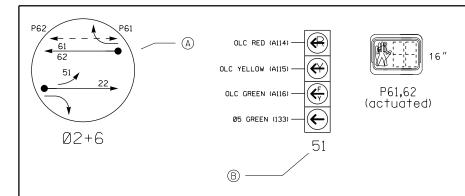
END PROGRAMMING

ASC/3-2070 Leading Pedestrian Interval Exceptions - Five Section Heads

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

12.8

SHEET 2 OF 2

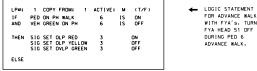


## ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING FOR FYA SUPPRESSION DURING THE DELAYED GREEN PERIOD

(program controller as shown)

- 1. From Main Menu select 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
- 3. From the LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD. PRESS 'ENTER'. AND PROGRAM AS SHOWN.



END PROGRAMMING

- 1. From Main Menu select 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
- 3. From the LOGIC PROCESSOR Submenu select 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENT 1 BY POSITIONING
THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING THE
TOGGLE KEY TO ENABLE IT.

| LOC | LOGIC STATEMENT |   |   | CO | CONTROL |   |   |   |   |   |   |   |   |   |   |   |   |
|-----|-----------------|---|---|----|---------|---|---|---|---|---|---|---|---|---|---|---|---|
|     |                 | 1 | 2 | 3  | 4       | 5 | 6 | 7 | 8 | 9 | 0 | 1 | 2 | 3 | 4 | 5 | 6 |
| LP  | 1-15            | Ε |   |    |         |   |   |   |   |   |   |   |   |   |   |   |   |
| LP  | 16-30           |   |   |    |         |   |   |   |   |   |   |   |   |   |   |   |   |
| LP  | 31-45           |   |   |    |         |   |   |   |   |   |   |   |   |   |   |   |   |
| LP  | 46-60           |   |   |    |         |   |   |   |   |   |   |   |   |   |   |   |   |
| LP  | 61-75           |   |   |    |         |   |   |   |   |   |   |   |   |   |   |   |   |
| LP  | 76-90           |   |   |    |         |   |   |   |   |   |   |   |   |   |   |   |   |
|     |                 |   |   |    |         |   |   |   |   |   |   |   |   |   |   |   |   |

END PROGRAMMING

### Leading Pedestrian Interval - Flashing Yellow Arrows

When an adjacent through vehicle movement is being held red during a leading ped interval, care must be taken to ensure that no other vehicle movement could encroach upon a pedestrian in the crosswalk. When flashing yellow arrows are being used for the vehicle approach that opposes the ped move, care must be taken to suppress the flashing yellow arrow output, which is the permitted movement, during the leading ped interval. The logic processor is used to accomplish this as shown on this sheet. The same logic is applied to main street and side street three section permitted only flashing yellow arrows. The phase diagram shown to the left is used for the explanation.

- (A) Phase diagram from a signal plan illustrating the pedestrian movement on phase 6 on the main street, and the opposing vehicle move phase 2 with no ped and a protected/permitted left turn via the flashing yellow arrow. Ped 6 is not served at startup and no MUTCD startup violation will occur because phase 6 is programmed to start in green. Refer to STD 12.6 sheet 1.
- (B) Signal head 51 is a protected and permitted flashing yellow arrow that has overlap parent phases of 5+6 (phase 6 is the opposing through move).
- © To suppress the signal head 51 flashing yellow arrow during the leading pedestrian interval, the logic processor is required. When ped 6 is timing the delayed green, the phase 6 vehicle move is held red. When the logic processor sees that the ped 6 movement is "ON" and the phase 6 vehicle move is "OFF", it prevents the flashing yellow arrow from turning on by holding the overlap red (head 51) while at the same time allowing the phase 2 through movement (head 22) to be served. After the delayed green time has expired, the logic statements are no longer TRUE and the phase 6 vehicle movement will turn "ON", at which time the flashing yellow arrow signal face will also turn "ON" and begin to flash.

This logic is used whenever a flashing yellow signal head opposes a pedestrian movement that has a leading pedestrian interval whether it happens to be a three section permitted only or a four section protected and permitted head.

## ASC/3-2070 Leading Pedestrian Interval Exceptions - Flashing Yellow Arrows

SIGNALS MANAGEMENT

TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

STD. NO.

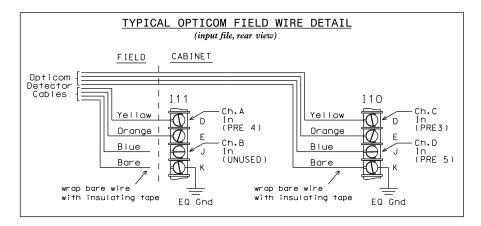
12.9

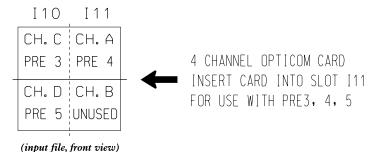
SHEET 1 OF 1

### Optical Emergency Vehicle Detection (Opticom)

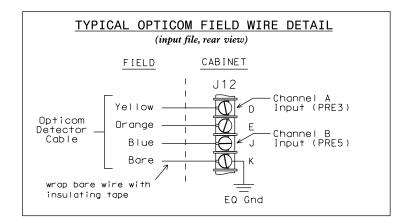
Opticom optical detection systems are typically used for emergency vehicle detection. The detector cards plug into the "I" file and "J" file of the 336 pole mounted and 332 base mounted 170 cabinets respectively. Cards are available in both two channel and four channel configurations. The two channel cards can be used in either of the designated emergency vehicle preemption slots of the 332 or 336 cabinets but are normally used in the leftmost slot. The four channel cards come equipped with a doublewide faceplate and must plugged into the rightmost preempt slot. See STD. NO. 8.0 sheets 1 and 2 to see the preempt slot locations in the input files for the 332 and 336 cabinets.

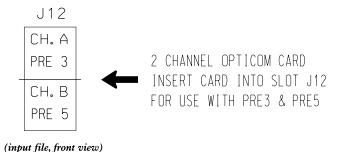
### 336 Pole Mounted Cabinet (uses ("I file")





### 332 Base Mounted Cabinet (uses "J File")





## Detection – Typical Optical Emergency Vehicle (Opticom)

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

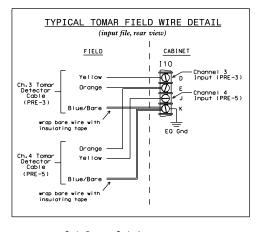
13.0

SHEET 1 OF 3

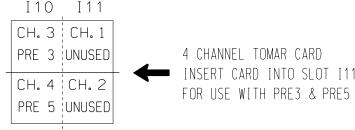
### Optical Emergency Vehicle Detection (Tomar)

Tomar optical detection systems are typically used for emergency vehicle detection. The detector cards plug into the "I" file and "J" file of the 336 pole mounted and 332 base mounted 170 cabinets respectively. Cards are equipped with four preemption inputs and have a doublewide faceplate for use with 170 cabinets. As such, the card should always be plugged into the rightmost preemption slot whether it is used in a 332 or a 336 cabinet. See STD. NO. 8.0 sheets 1 and 2 to see the preempt slot locations in the input file for the 332 base mounted cabinet and 336 pole mounted cabinet.

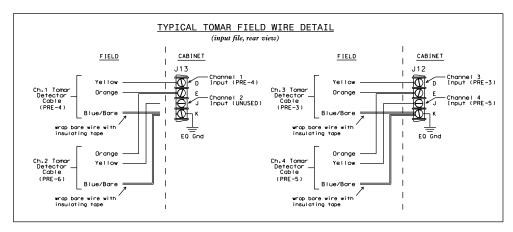
### 336 Pole Mounted Cabinet (uses ("I file")

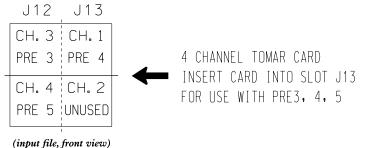


(input file, front view)



### 332 Base Mounted Cabinet (uses "J File")





## Detection – Typical Optical Emergency Vehicle (Tomar)

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

13.0

SHEET 2 OF 3

### GPS Emergency Vehicle Detection

GPS systems can be used for emergency vehicle detection. The detector system is installed in the "J" file in a 332 base mounted cabinet and in the "I" file in a 336 pole mounted cabinet. The diagram below shows the typical representation of the GPS emergency vehicle preemption system deployed in a 332 base mounted cabinet in slots 12 and 13 of the "J" file. The same typical application in a 336 pole mounted cabinet would have the preempt input interface installed in slots 10 and 11 of the "I" file.

### INPUT FILE POSITION LAYOUT

(front view)

|                  | _    | 1    | 2   | 3    | 4    | 5    | 6   | 7    | 8    | 9            | 10          | 11             | 12             | 13             | 14             |
|------------------|------|------|-----|------|------|------|-----|------|------|--------------|-------------|----------------|----------------|----------------|----------------|
| 1.1              |      | Ø 1  | Ø 2 | Ø 2  | Ø 2  | ø 3  | Ø 4 | Ø 4  | Ø 4  | SYS.<br>DET. | S           | S              | Ø2 PED         | Ø6PED          | FS             |
| FILE U           |      | 1A   | 2A  | 2C   | 2E   | 3A   | 4A  | 4C   | 4E   | S1           | ģ           | Ō              | DC<br>ISOLATOR | DC<br>ISOLATOR | DC<br>ISOLATOR |
| "I" <sub>,</sub> |      | NOT  | Ø 2 | ø 2  | NOT  | NOT  | Ø 4 | Ø 4  | NOT  | SYS.<br>DET. | E<br>M<br>P | E<br>M<br>P    | Ø4 PED         | Ø8 PED         | ST             |
| _  [             | USED | 2B   | 2D  | USED | USED | 4B   | 4D  | USED | S2   | T<br>Y       | T<br>Y      | DC<br>ISOLATOR | DC<br>ISOLATOR | DC<br>ISOLATOR |                |
| 1.1              |      | Ø 5  | Ø 6 | Ø 6  | Ø 6  | Ø 7  | Ø 8 | Ø 8  | Ø 8  | SYS.<br>DET. | S           | S<br>L         |                |                | S              |
| FILE             |      | 5A   | 6A  | 6C   | 6E   | 7A   | 8A  | 8C   | 8E   | S3           | Ď           | Ď              | ; →<br>GF      | ' '            | ¥              |
| "J" <sub> </sub> |      | NOT  | ø6  | Ø 6  | NOT  | NOT  | Ø 8 | ø 8  | NOT  | SYS.<br>DET. | E<br>M<br>P | E<br>M<br>P    | E/             | /P :           | E<br>M<br>P    |
| -                |      | USED | 6B  | 6D   | USED | USED | 8B  | 8D   | USED | S4           | Ť           | Ť              |                | <u></u>        | Ť              |

EX.: 1A, 2A, ETC. = LOOP NO.'S

\* See GPS Preemption Installation note below.

FS = FLASH SENSE ST = STOP TIME

### GPS PREEMPTION INSTALLATION NOTE

Install a GPS preemption system. Perform installation according to manufacturer's directions and NCDOT engineer approved mounting location to accomplish the preemption schemes shown on the Signal Design Plans.

## Detection - Typical GPS Emergency Vehicle Detection

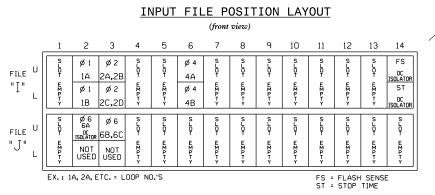
SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

13.0

SHEET 3 OF 3

#### Microwave Pulse Detection

Microwave motion detectors can be used for vehicle pulse detection. When a vehicle enters the detection zone defined by the microwave head, the equipment will trigger a single momentary input to a DC Isolator located in the input file and register a true input to the controller. Based on the requirements of the signal design, the controller might use the input to lock a vehicle call, extend a phase, register a system count, or similar. The following sheets illustrate a typical pulse detection application.



(A) INPUT FILE LAYOUT - In this example the DC isolator used with the pulse detector is located in slot J2-U and is used for loop 6A.

Note: Install a model 242 DC isolator in slot J2 for use with microwave detector. See the Microwave Detector Wiring Details on sheet 2.

IMPORTANT: For proper operation of the microwave detector, remove surge protection from TB3-5 and TB3-6, and from TB3-7 and TB3-8.

#### INPUT FILE CONNECTION & PROGRAMMING CHART -

| LOOP NO.    | LOOP<br>TERMINAL | INPUT<br>FILE POS. | PIN<br>NO. | INPUT<br>ASSIGNMENT<br>NO. | DETECTOR<br>NO. | NEMA<br>PHASE | CALL | EXTEND | FULL<br>TIME<br>DELAY | STRETCH<br>TIME | DELAY<br>TIME |
|-------------|------------------|--------------------|------------|----------------------------|-----------------|---------------|------|--------|-----------------------|-----------------|---------------|
| 1A          | TB2-5,6          | 12U                | 39         | 1                          | 2               | 1             | Y    | Υ      |                       |                 |               |
| 18          | TB2-7,8          | I2L                | 43         | 5                          | 12              | 1             | Y    | Y      |                       |                 |               |
| 2A,2B       | TB2-9,10         | I3U                | 63         | 25                         | 32              | 2             | Y    | Y      |                       | 1.8             |               |
| 2C,2D       | TB2-11,12        | I3L                | 76         | 38                         | 42              | 2             | Y    | Υ      |                       |                 |               |
| 4A          | TB4-9,10         | 16U                | 41         | 3                          | 4               | 4             | Y    | Y      |                       |                 |               |
| 4B          | TB4-11,12        | I6L                | 45         | 7                          | 14              | 4             | Y    | Y      |                       |                 | 15            |
| <b>★</b> 6A | TB3-5,6          | J2U                | 40         | 2                          | •               | 6             | Y    | Υ      |                       | 1.2             |               |
| 6B,6C       | TB3-9,10         | J3U                | 64         | 26                         | 36              | 6             | Y    | Y      |                       |                 |               |

B INPUT FILE CONNECTION - Details for loop 6A are found in this chart. When a vehicle enters the microwave zone, a phase 6 call is placed, phase 6 is extended if it is timing, and stretch detection is implemented.

 $\bigstar$  Microwave pulse detector. See wiring and programming detail on sheet 2.

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWER

(continued on next page)

## Detection – Microwave Pulse (Oasis)

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

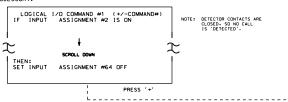
13.1

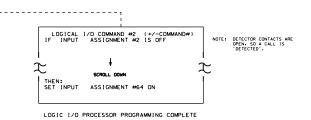
SHEET 1 OF 4

### ECONOLITE OASIS LOGICAL I/O PROCESSOR PROGRAMMING DETAIL FOR MICROWAVE DETECTOR INPUT PROCESSING

#### (program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL). THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1 AND 2.
- FROM MAIN MENU PRESS '6' (OUTPUTS). THEN '3' (LOGICAL I/O PROCESSOR).





#### Microwave Pulse Detection

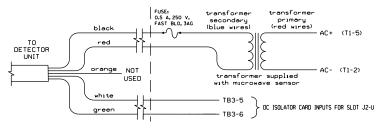
#### OUTPUT REFERENCE SCHEDULE

INPUT 2 = Detector Physical Input (Not Enabled)
INPUT 64 = Dummy Detector Input (Detector 6)

#### TYPICAL MICROWAVE PULSE DETECTOR WIRING DETAIL

(wire as shown)

#### CONTROLLER CABINET



#### MICROWAVE DETECTOR WIRE LIST

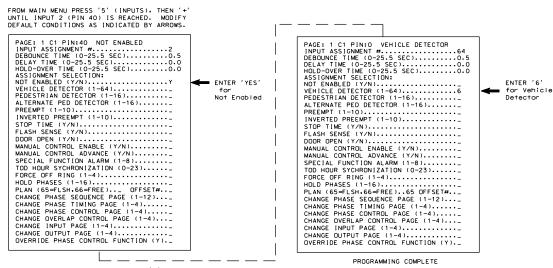
| COLOR  | FUNCTION                       |
|--------|--------------------------------|
| black  | 12V to 24V AC/DC (no polarity) |
| red    | 12V to 24V AC/DC (no polarity) |
| orange | Output Relay Normally Open     |
| white  | Output Relay Normally Closed   |
| green  | Output Relau Common            |

#### NOTES:

- Sensor is a microwave motion detector mounted on poles as indicated on the Signal Design Plans.
- 2. Microwave wiring shown above will cause a permanent call unless the Input Assignment Programming and Logical I/O Processor Programming details are entered as shown on this sheet. These programming details will cause a call to be placed upon opening the Normally Closed contact on the microwave detector.
- DC Isolator's LED will be ON when no call is present and will be OFF when a call is present.
- Important: For proper operation of the microwave detector, remove surge protection from TB3-5, TB3-6, TB3-7, and TB3-8 and insert 242 DC Isolator in slat 42.

#### INPUT ASSIGNMENT PROGRAMMING DETAIL FOR MICROWAVE DETECTOR INPUT

(program controller as shown below)



PRESS '-' until input Assignment #64 is reached

NOTE:

This remapping removes the default detector from the microwave's physical input and reassigns it to unused INPUT 64. The Logical I/O Processor Programming Detail on this sheet will invert the disabled input and control INPUT 64 and the reassigned detector.

### Detection - Microwave Pulse (Oasis)

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

13.1

SHEET 2 OF 4

### Microwave Pulse Detection (ASC/3-2070)

Microwave motion detectors can be used for vehicle pulse detection. When a vehicle enters the detection zone defined by the microwave head, the equipment will trigger a single momentary input to a DC Isolator located in the input file. Using the logic processor, and based on the requirements of the signal design, the controller might then use the input to lock a vehicle call, extend a phase, register a system count, or similar. The following sheets illustrate a typical pulse detection application.

#### INPUT FILE POSITION LAYOUT

(front view) 8 13 14 ø2 FS Ø 1 DC ISOLATOR FILE 1A 2A,2B 4Α ST Ø 1 ø2 Ø 4 1B 4B FILE 6B,6C DC ISOLATOR "J" Ė NOT USED NOT USED EX.: 1A, 2A, ETC. = LOOP NO.'S

(A) INPUT FILE LAYOUT - In this example the DC isolator used with the pulse detector is located in slot J9-U, which by default is detector 15. It will have no phase assigned.

ST = STOP TIME

Note: Install a model 242 DC isolator in slot J9 for use with microwave detector.

See the Microwave Detector Wiring Details on sheet 2.

IMPORTANT: For proper operation of the microwave detector, remove surge protection from TB7-9 and TB7-10, and from TB7-11 and TB7-12.

#### INPUT FILE CONNECTION & PROGRAMMING CHART

| LOOP NO.    | LOOP<br>TERMINAL | INPUT<br>FILE POS. | PIN<br>NO. | DETECTOR<br>NO. | NEMA<br>PHASE | CALL | EXTEND<br>TIME | DELAY<br>TIME | ADDED<br>INITIAL | DETECTOR<br>TYPE |
|-------------|------------------|--------------------|------------|-----------------|---------------|------|----------------|---------------|------------------|------------------|
| 1A          | TB2-7,8          | I2L                | 43         | 12              | 1             | YES  |                |               |                  | S                |
| 1B          | TB2-9,10         | I3U                | 63         | 32              | 1             | YES  |                |               |                  | S                |
| 2A,2B       | TB2-11,12        | I3L                | 76         | 42              | 2             | YES  | 1.8            |               |                  | S                |
| 2C,2D       | TB4-1,2          | I4U                | 47         | 22              | 2             | YES  |                |               |                  | S                |
| 4A          | TB4-9,10         | I6U                | 41         | 4               | 4             | YES  |                |               |                  | S                |
| 4B          | TB4-11,12        | I6L                | 45         | 14              | 4             | YES  |                | 15            |                  | S                |
| <b>*</b> 6A | -                | J2U                | 40         | 6               | 6             | YES  | 1.2            |               | Χ                | N                |
| 511         | TB7-9,10         | J9U                | 59         | <b>★</b> 15     | -             | N0   |                |               |                  |                  |
| 6B,6C       | TB3-9,10         | J3U                | 64         | 36              | 6             | YES  |                |               |                  | S                |

B INPUT FILE CONNECTION - Details for loop 6A are found in this chart. When a vehicle enters the microwave detection zone, the logic processor sees an input on the logical detector 15 input but will not place a call to the controller via that input. The logic will instead place a call to the controller via detector 6, which is a phase 6 detector. In this example, if phase 6 is green, 1.2 seconds of extend will be timed if a vehicle passes through the detection zone.

- \* Microwave pulse detector. See wiring and programming detail on sheet 2.
- ★ 6A logical detector. See Logic Processor Programming Detail for Microwave Detector Input Processing on sheet 2.

INPUT FILE POSITION LEGEND: J2I
FILE J
SLOT 2
LOWER

(continued on next page)

## Detection - Microwave Pulse (ASC/3-2070)

SIGNALS MANAGEMENT

TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

STD. NO.

13.1

SHEET 3 OF 4

## ECONOLITE ASC/3-2070 LOGIC PROCESSOR PROGRAMMING DETAIL FOR MICROWAVE DETECTOR INPUT PROCESSING

(program controller as shown)

- 1. From Main Menu select 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
- 3. From the LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD. PRESS 'ENTER'. AND PROGRAM AS SHOWN.

LP#: 1 COPY FROM: 1 ACTIVE: M (T/F)
IF DET 15 IS ON

THEN DET SET VEH 1-16 6 OFF

ELSE

NOTE: DETECTOR CONTACTS ARE CLOSED. SO NO CALL IS 'DETECTED'.

ENTER A "2" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#: 2 COPY FROM: 2 ACTIVE: M (T/F)

IF DET 15 IS OFF

THEN DET SET VEH 1-16 6 ON

ELSE

NOTE: DETECTOR CONTACTS ARE OPEN. SO A CALL IS 'DETECTED'.

END PROGRAMMING

- 1. From Main Menu select | 1. CONFIGURATION
- 2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR
- 3. From the LOGIC PROCESSOR Submenu select 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENTS 1 AND 2 BY POSITIONING THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE THEM.

END PROGRAMMING

#### ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR LOGICAL DETECTOR 15

- 1. From Main Menu select 6. DETECTORS
- 2. From DETECTOR Submenu select 2. VEHICLE DETECTOR SETUP
  - Place cursor in VEH DETECTOR [ ] position and enter "15".
  - Set PH to "0" and CALL OPTION to "NO"

VEH DETECTOR [15] VEH DET PLAN [1]

TYPE: N-NTCIP

TS2 DETECTOR.... ECPI LOG..... NO

DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6

CALL = NO 

EXT OPTION... NO

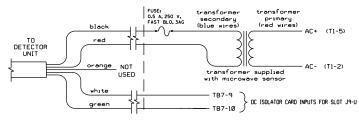
EXT OPTION. PASSAGE EXTENTION TIME. 0.0

USE ADDED INITIAL . CROSS SWICTH PH... 0

#### TYPICAL MICROWAVE PULSE DETECTOR WIRING DETAIL

(wire as shown)

CONTROLLER CABINET



#### MICROWAVE DETECTOR WIRE LIST

| COLOR  | FUNCTION                       |
|--------|--------------------------------|
| black  | 12V to 24V AC/DC (no polarity) |
| red    | 12V to 24V AC/DC (no polarity) |
| orange | Output Relay Normally Open     |
| white  | Output Relay Normally Closed   |
| green  | Output Relay Common            |

#### NOTES:

- Sensor is a microwave motion detector mounted on poles as indicated on the Signal Design Plans.
- 2. Microwave wiring shown above will cause a permanent call unless the Input Assignment Programming and Logical I/O Processor Programming details are entered as shown on this sheet. These programming details will cause a call to be placed upon opening the Normally Closed contact on the microwave detector.
- DC Isolator's LED will be ON when no call is present and will be OFF when a call is present.
- 4. Important: For proper operation of the microwave detector, remove surge protection from TB7-9, TB7-10, TB7-11, and TB7-12 and insert 242 DC Isolator in slot J9.

## Detection - Microwave Pulse (ASC/3-2070)

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NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

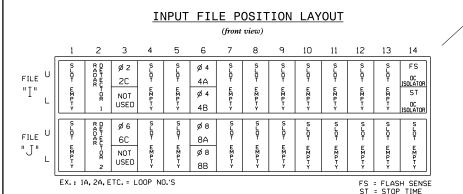
STD. NO.

13.1

SHEET 4 OF 4

#### Microwave Presence Detection

Microwave (radar) motion detectors can be used for vehicle presence detection. When a vehicle enters the detection zone defined by the microwave head, the equipment will send a presence signal to the detection equipment located in the input file and register a true input to the controller for as long as the vehicle remains in the detection zone. Based on the requirements of the signal design, the controller might use the input to lock a vehicle call, extend a phase, register a system count, or similar. The following illustrates a typical presence detection application.



(A) INPUT FILE LAYOUT - In this example the radar detection cards are located in input file slots I2 and J2.

#### INPUT FILE CONNECTION & PROGRAMMING CHART

| L00P NO. | LOOP<br>TERMINAL | INPUT<br>FILE POS. | PIN<br>NO. | INPUT<br>ASSIGNMENT<br>NO. | DETECTOR<br>NO. | NEMA<br>PHASE | CALL | EXTEND | FULL<br>TIME<br>DELAY | STRETCH<br>TIME | DELAY<br>TIME |
|----------|------------------|--------------------|------------|----------------------------|-----------------|---------------|------|--------|-----------------------|-----------------|---------------|
| 2C       | TB2-9,10         | I3U                | 63         | 25                         | 32              | 2             | Υ    | Y      | Y                     |                 | 3             |
| 4A       | TB4-9,10         | I6U                | 41         | 3                          | 4               | 4             | Υ    | Y      |                       |                 | 5             |
| 4B       | TB4-11,12        | I6L                | 45         | 7                          | 14              | 4             | Y    | Y      |                       |                 | 15            |
| 6C       | TB3-9,10         | J3U                | 64         | 26                         | 36              | 6             | Υ    | Y      | Y                     |                 | 3             |
| 8A       | TB5-9,10         | J6U                | 42         | 4                          | 8               | 8             | Υ    | Y      |                       |                 | 5             |
| OD.      | TDE-11 12        | TCI                | A.C.       | 0                          | 10              |               | ~    | v      |                       |                 | 15            |

B INPUT FILE CONNECTION - Details for the radar detection system setup are found on the signal plan and not on the electrical detail. Installation and setup is left to field personnel, the manufacturer, and the manufacturer's representative.

INPUT FILE POSITION LEGEND: J2L
FILE J
SLOT 2
LOWFR

© SPECIAL DETECTOR NOTE - Refers the installer to the manufacturer for specifics regarding installation and setup of the equipment, and to the NCDOT engineering staff for other operational guidance. The zones specified in the note are taken directly from the signal plan.

#### SPECIAL DETECTOR NOTE

Install a radar detection system for vehicle detection for zones 2A, 2B, 6A, and 6B. Perform installation according to manufacturer's directions and NCDDT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

### Detection - Microwave Presence

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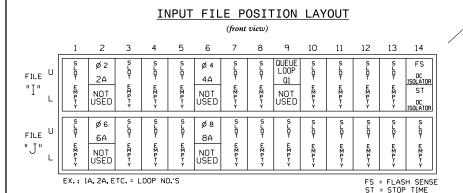
STD. NO.

13.2

SHEET 1 OF 3

### Multizone Microwave Detection

Multizone Microwave Detectors can be used for vehicle pulse or presence detection. When multizone microwave detectors are used for presence detection and a vehicle enters the detection zone defined by the microwave head, a signal is sent to the detection equipment located in the cabinet and registers a true input to the controller for as long as the vehicle remains in the detection zone. Depending on the requirements of the signal design and the microwave equipment configuration, the controller might use the input to lock a vehicle call, extend a phase, register a system count, or similar.



(A) INPUT FILE LAYOUT - In this example the loop and detector installation chart on the signal plan showed microwave presence detection zone on a bridge deck labeled as "Q1" and all other detectors were inductive loops in the pavement. The electrical detail shows no specific placement or installation instructions for the microwave equipment, but instead instructs the installer to follow manufacturer and engineer approved guidelines via a Special Detector Note.

INPUT INPLIT STRETCH DELAY DETECTOR NFMΔ OOP NO. TERMINAL FILE POS. NO. ASSIGNMENT CALL EXTEND TIME PHASE TIME NN. TIME NO. TB2-5.6 1211 2A 39 4A TB4-9.10 I6U 41 4 J2U 40 6 Υ TB3-5.6 TB5-9,10 J6U 42

INPUT FILE CONNECTION & PROGRAMMING CHART

B INPUT FILE CONNECTION - Details for the multizone microwave detection system setup are found on the signal plan and not on the electrical detail. Installation and setup is left to field personnel, the manufacturer, the manufacturer's representative, and NCDOT engineering.

INPUT FILE POSITION LEGEND: J2I
FILE J
SLOT 2
LOWFR

#### SPECIAL DETECTOR NOTE

Install a multizone microwave detection system for vehicle detection for zone Q1. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

© SPECIAL DETECTOR NOTE - Refers the installer to the manufacturer for specifics regarding installation and setup of the equipment, and to the NCDOT engineering staff for other operational guidance.

Detection - Multizone Microwave Presence

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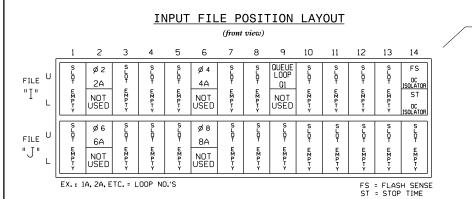
STD. NO.

13.2

SHEET 2 OF 3

#### Video Detection

Video Detectors can be used for vehicle pulse or presence detection. When video detectors are used for presence detection and a vehicle enters the detection zone defined by the video equipment, a signal is sent to the detection equipment located in the cabinet and registers a true input to the controller for as long as the vehicle remains in the detection zone. Depending on the requirements of the signal design and the video equipment configuration, the controller might use the input to lock a vehicle call, extend a phase, register a system count, or similar.

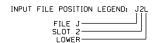


| $\bigcirc$ | INPUT FILE LAYOUT - In this example the loop         |
|------------|--|
|            | and detector installation chart on the signal        |
|            | plan showed video presence detection zone on         |
|            | a bridge deck labeled as "Q1" and all other          |
|            | detectors were inductive loops in the pavement.      |
|            | The electrical detail shows no specific placement    |
|            | or installation instructions for the video equipment |
|            | but instead instructs the installer to follow        |
|            | manufacturer and engineer approved guidelines via a  |
|            | Special Detector Note                                |
|            | !  |

#### INPUT FILE CONNECTION & PROGRAMMING CHART

|   | L00P NO. | LOOP<br>TERMINAL | INPUT<br>FILE POS. | PIN<br>NO. | INPUT<br>ASSIGNMENT<br>NO. | DETECTOR<br>NO. | NEMA<br>PHASE | CALL | EXTEND | FULL<br>TIME<br>DELAY | STRETCH<br>TIME | DELAY<br>TIME |
|---|----------|------------------|--------------------|------------|----------------------------|-----------------|---------------|------|--------|-----------------------|-----------------|---------------|
| ſ | 2A       | TB2-5.6          | I2U                | 39         | 1                          | 2               | 2             | Υ    | Y      |                       |                 |               |
| ſ | 4A       | TB4-9,10         | I6U                | 41         | 3                          | 4               | 4             | Y    | Y      |                       |                 | 3             |
| ľ | 6A       | TB3-5,6          | J2U                | 40         | 2                          | 6               | 6             | Y    | Y      |                       |                 |               |
| Ī | 8A       | TB5-9,10         | J6U                | 42         | 4                          | 8               | 8             | Υ    | Y      |                       |                 | 3             |

(B) INPUT FILE CONNECTION - Details for the video detection system such as zone location and type, are found on the signal plan and not on the electrical detail. Installation and setup is left to field personnel, the manufacturer, the manufacturer's representative, and NCDOT engineering.



#### SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection for zone 01. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans. © SPECIAL DETECTOR NOTE - Refers the installer to the manufacturer for specifics regarding installation and setup of the equipment, and to the NCDOT engineering staff for other operational guidance.

### Detection - Video Presence

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STD. NO.

13.2

SHEET 3 OF 3

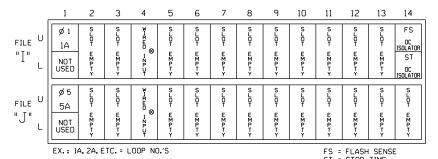
# Presence Detection and Wired Inputs

Loops and/or zones that are referred to as "wired inputs" typically place a call for a protected turning movement and also place a call to the adjacent through movement. While installation instructions and zone locations in the Input File are not usually provided on an electrical detail for Multizone Microwave or Video detection systems, the location of detectors for wired input programming to function properly is critical for wired inputs. As such, a set of special Detector Notes is provided on electrical details for signal plans that have wired inputs to ensure proper detector operation and timing. Certain slots are "reserved" for proper functionality and are shown in the layout, programming chart, and notes. More information about wired inputs and the jumpers is found in STD. NO. 8.1.

For the example shown below, all detection was accomplished using a video detection system where the signal plan had default phasing only. Detection zones 1A and 5A each call two phases and are installed as a wired input. Their specific locations in the Input File need to be as shown so the detector programming is correct and the phasing runs properly.

### INPUT FILE POSITION LAYOUT

(front view)



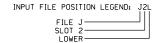
 $<sup>\</sup>otimes$  Wired Input - Do not populate slot with detector card

# INPUT FILE CONNECTION & PROGRAMMING CHART

| L | .00P NO. | LOOP<br>TERMINAL | INPUT<br>FILE POS. | PIN<br>NO. | INPUT<br>ASSIGNMENT<br>NO. | DETECTOR<br>NO. | NEMA<br>PHASE | CALL | EXTEND | FULL<br>TIME<br>DELAY | STRETCH<br>TIME | DELAY<br>TIME |
|---|----------|------------------|--------------------|------------|----------------------------|-----------------|---------------|------|--------|-----------------------|-----------------|---------------|
| Γ | 1A 1     | TB2-1,2 I1U      |                    | 56         | 18                         | 1               | 1             | Y    | Υ      |                       |                 | 15            |
|   |          | -                | J4U                | 48         | 10                         | 26              | 6             | Y    | Υ      | Υ                     |                 | 3             |
| Γ | 5A²      | TB3-1,2          | JIU                | 55         | 17                         | 5               | 5             | Y    | Y      |                       |                 | 15            |
|   | JH       | -                | I4U                | 47         | 9                          | 22              | 2             | Y    | Y      | Υ                     |                 | 3             |

<sup>&#</sup>x27;Add jumper from JI-W to J4-W, on rear of input file.

 $<sup>^{2}\,\</sup>mbox{Add}$  jumper from J1-W to I4-W, on rear of input file.



# SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

For Detection Zones 1A and 5A, the equipment placement and slots reserved for wired inputs are typical for a NCDOT installation.

# Detection – Presence Detection and Wired Inputs (Default Phasing)

STD. NO.

13.3

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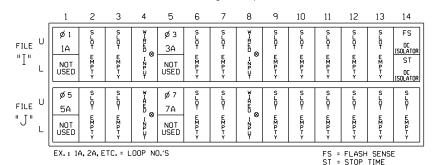
# Presence Detection and Wired Inputs

For the example shown below, all detection was accomplished using a video detection system where the signal plan had both default phasing and alternate phasing. Detection zones 1A, 3A, 5A, and 7A each call two phases and are installed as wired inputs. Their specific locations in the Input File need to be as shown so the detector programming is correct and the phasing runs properly during the default and alternate phasing periods. The Special Detector Notes and the programming chart are different than those shown for the electrical detail that only runs default phasing due to the fact that detector operation is reprogrammed during the alternate period and the notes make a reference to instructions found on other sheets of the electrical detail.

The Input File Connection & Programming Chart shown below is for an Oasis design. The detector programming details for alternate phasing are found in STD. NO. 11.5. Designs that have alternate phasing and use ASC/3-2070 software implement detection in the same way and the Input File Connection & Programming Chart and detector programming details are shown in STD. NO. 11.10.

# INPUT FILE POSITION LAYOUT

(front view)



<sup>&</sup>lt;sup>⊗</sup> Wired Input - Do not populate slot with detector card

### SPECIAL DETECTOR NOTES

- Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.
- 2) For detection zones 1A, 3A, 5A, and 7A, detector card placement and slots reserved for wired inputs are typical for a NCDOT installation. Inputs associated with these slots are compatible with time of day instructions located on sheets 3 and 4 of this electrical detail.

# INPUT FILE CONNECTION & PROGRAMMING CHART (Oasis)

| LOOP NO.        | LOOP<br>TERMINAL | INPUT<br>FILE POS. | PIN<br>NO. | INPUT<br>ASSIGNMENT<br>NO. | DETECTOR<br>NO. | NEMA<br>PHASE | CALL | EXTEND | FULL<br>TIME<br>DELAY | STRETCH<br>TIME | DELAY<br>TIME |
|-----------------|------------------|--------------------|------------|----------------------------|-----------------|---------------|------|--------|-----------------------|-----------------|---------------|
|                 | TB2-1,2          | IIU                | 56         | 18                         | 1               | 1             | Y    | Y      |                       |                 | 15            |
| 1A 1            | -                | J4U                | 48         | 10 ★                       | 26              | 6             | Y    | Y      | Y                     |                 | 3             |
|                 | -                | IIU                | 56         | 18 ★                       | 51              | 1             | Y    | Y      |                       |                 |               |
|                 | TB4-5,6          | I5U                | 58         | 20                         | 3               | 3             | Υ    | Y      |                       |                 | 15            |
| 3A1             | -                | J8U                | 50         | 12 ★                       | 28              | 8             | Υ    | Y      |                       |                 | ω             |
|                 | -                | I5U                | 58         | 20★                        | 53              | 3             | Υ    | Y      |                       |                 |               |
|                 | TB3-1.2          | J1U                | 55         | 17                         | 5               | 5             | Υ    | Y      |                       |                 | 15            |
| 5A <sup>2</sup> | -                | I4U                | 47         | 9★                         | 22              | 2             | Υ    | Y      | Y                     |                 | ω             |
|                 | -                | J1U                | 55         | 17 ★                       | 55              | 5             | Υ    | Y      |                       |                 |               |
|                 | TB5-5,6          | J5U                | 57         | 19                         | 7               | 7             | Υ    | Y      |                       |                 | 15            |
| 7A <sup>2</sup> | -                | I8U                | 49         | 11 ★                       | 24              | 4             | Y    | Y      |                       |                 | 3             |
|                 | -                | J5U                | 57         | 19★                        | 57              | 7             | Υ    | Y      |                       |                 |               |

<sup>&#</sup>x27;Add jumper from II-W to J4-W, on rear of input file.

# Detection – Presence Detection and Wired Inputs (Default & Alternate Phasing)

10

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13.3

STD. NO.

SHEET 2 OF 2

<sup>&</sup>lt;sup>2</sup>Add jumper from I5-W to J8-W. on rear of input file.

<sup>&</sup>lt;sup>3</sup>Add jumper from J1-W to I4-W, on rear of input file.

<sup>\*</sup>Add jumper from J5-W to 18-W. on rear of input file.

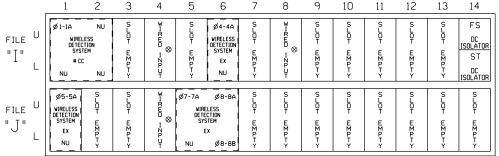
 $<sup>\</sup>star$  See Input Page Assignment programming details on sheets 3 and 4.

# In Pavement Wireless Detection

In pavement wireless detection systems make use of wireless sensors, repeaters, an access point, and special contact closure cards to implement a vehicle detection system. Wireless sensors are installed beneath the pavement surface and transmit detector information to repeaters that communicate the information to the cabinet via an access point mounted on a pole in the intersection. Each system must have one master CC contact closure card in the input file. Expansion contact closure cards (EX) are available in two and four channel configurations. The details shown below illustrate how a typical in pavement wireless detection system would be represented on an electrical detail.

# INPUT FILE POSITION LAYOUT

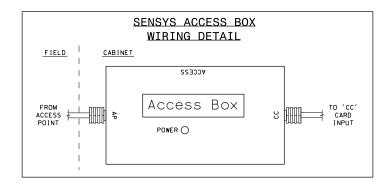
(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S
NU = CHANNEL NOT USED

FS = FLASH SENSE ST = STOP TIME

- $^{igotimes}$  Wired Input Do not populate slot with detector card
- # See Sensys Access Box Wiring Detail below.

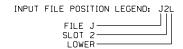


# INPUT FILE CONNECTION & PROGRAMMING CHART

| LO | OP NO. | LOOP<br>TERMINAL | INPUT<br>FILE POS. | PIN<br>NO. | INPUT<br>ASSIGNMENT<br>NO. | DETECTOR<br>NO. | NEMA<br>PHASE | CALL | EXTEND | FULL<br>TIME<br>DELAY | STRETCH<br>TIME | DELAY<br>TIME |
|----|--------|------------------|--------------------|------------|----------------------------|-----------------|---------------|------|--------|-----------------------|-----------------|---------------|
| *  | 1A     | -                | IIU                | 56         | 18                         | 1               | 1             | Υ    | Y      | Υ                     |                 | 15            |
| *  | 4A     | -                | I6U                | 41         | 3                          | 4               | 4             | Y    | Y      |                       |                 | 3             |
| *  | 5A     | -                | J1U                | 55         | 17                         | 5               | 5             | Y    | Y      | Y                     |                 | 15            |
| *  | 7A     | -                | J5U                | 57         | 19                         | 7               | 7             | Y    | Y      |                       |                 | 15            |
| *  | 8A     | -                | J6U                | 42         | 4                          | 8               | 8             | Y    | Y      |                       |                 | 3             |
| *  | 8B     | -                | J6L                | 46         | 8                          | 18              | 8             | Y    | Y      |                       |                 | 15            |

# \* WIRELESS DETECTION SYSTEM

- Install a Wireless Vehicle Detection System for vehicle detection. Perform installation according to manufacturer's directions and NCDOT Engineer-approved mounting locations to accomplish the detection schemes shown on the signal design plans.
- Ensure that the Wireless Vehicle Detection System is fully compatible with equipment manufactured in accordance with the specifications for the type 2070 controller.



# Detection - In Pavement Wireless

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STD. NO.

13.4

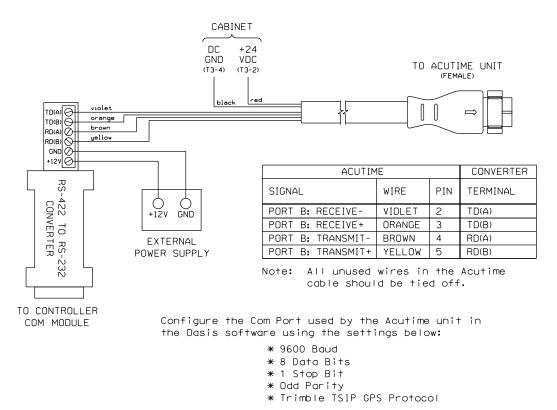
SHEET 1 OF 1

# Detection - GPS Clock Reference

Some systems must derive a clock reference from a satellite by using a GPS antenna. A typical GPS antenna wiring reference that would be shown on an electrical detail is shown below.

# CONNECTOR WIRING DETAIL FOR ACUTIME GPS ANTENNA WITH RS-422 INTERFACE

(make connections as shown)



# Detection - GPS Clock Reference

under D-1 (Set Clock) menu.

Be sure to enable the "GET TIME FROM GPS" option

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TRANSPORTATION MOBILITY AND SAFETY DIVISION
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

STD. NO.

13.5

# Timed Overlap

A timed overlap is a movement that has additional green time added and will continue to run after its parent phase(s) have timed out or gapped out. Timed overlaps are typically used where upstream traffic needs additional time to move downstream in order to clear out a zone between signals to make room for vehicles coming from side streets, or to give vehicles moving from freeway ramps time to clear a downstream signal before it clears. When the side street is served, the upstream signal(s) on the main street begin their clearance while the downstream signal(s) time their additional green time so the zone between the two signals can be cleared of vehicles before the side street begins moving. Once the extended green time has been served, the overlap clears to yellow and red.

# PHASING DIAGRAM (A) (B) TOL A PHASING DIAGRAM DETECTION LEGEND DETECTED MOVEMENT UNDETECTED MOVEMENT (OVERLAP) UNSIGNALIZED MOVEMENT

<--- PEDESTRIAN MOVEMENT

- (A) In the example shown to the left, overlap 'A' is a vehicle through move on the main street located between the upstream and downstream signal. Its parent phases are 2 and 4 and the overlap is active whenever parent phase 2 or 4 is the active phase.
- (B) Whenever either parent phase clears, the vehicle move that is "Timed Overlap A" (TOL A) will begin to time the extended green time as the parent phase(s) clear from yellow to red. After the extended green time is up, the overlap will time its programmed yellow and red clear times and the controller will proceed to the next phase in the sequence.

Programming details for Oasis and ASC/3-2070 for example shown to the left can be found on sheets 2 and 3 of STD. NO. 14.

# Timed Overlap

SIGNALS MANAGEMENT
TRANSPORTATION MOBILITY AND SAFETY DIVISION
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

STD. NO.

14.0

# <u>Timed Overlap Programming Detail - Oasis</u>

# OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

| PAGE 1: VEHICLE OVERLAP 'A' SETTINGS PHASE:  12345678910111213141516   |               |
|--|---------------|
| VEH OVL PARENTS:   X X   | — (A)         |
| VEH OVL NOT PED:   |               |
| VEH OVL GRN EXT:   X X  STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW _ GREEN               | — (B)         |
| SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?Y GREEN EXTENSION (0-255 SEC)5            | — (C)         |
| YELLOW CLEAR (0=PARENT,3-25.5 SEC)3.4 — RED CLEAR (0=PARENT,0.1-25.5 SEC)2.0 — OUTPUT AS PHASE # (0=NONE, 1-16)0 | — (Ö<br>— (E) |

OVERLAP PROGRAMMING COMPLETE

- A VEH OVL PARENTS will provide service when any of the selected parent phases are active. When the last active parent phase terminates, the overlap will terminate. Overlaps will time the Green extension, Yellow clearance, and Red clearance specified in the overlap programming detail. If one of these elements is not specified, the overlap will use the same clearances as the terminating parent as shown on the timing chart on the signal plan.
- (B) VEH OVL GRN EXT designates which phase(s) will start the extension timer when the parent phase(s) terminates.
- © GREEN EXTENSION designates the time the overlap will extend its green interval beyond the green termination of the parent phase(s) designated in the VEH OVL GRN EXT field.
- YELLOW CLEAR designates the yellow clearance interval for the overlap.
- RED CLEAR designates the red clearance interval for the overlap.

Timed Overlap Programming Detail – Oasis

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

14.0

SHEET 2 OF 3

# Timed Overlap Programming Detail - ASC/3-2070

# ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 2. CONTROLLER
- 2. From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

### OVFRIAP A

Select TMG VEH OVLP [A] and 'OTHER/ECONOLITE'

| ſ | TNC  | V/C1 |     | 21/1 | _   |     | ſ   | . 1 | Τ.  | <u> </u> | [   | 3 T I |            | - /r | - ~ . | ) N 1 C | ٦.  | 1 7 1 | -   | 1     |       |
|---|------|------|-----|------|-----|-----|-----|-----|-----|----------|-----|-------|------------|------|-------|---------|-----|-------|-----|-------|-------|
| ı | TMG  |      |     |      |     |     | •   | •   |     |          | L   |       |            |      |       |         |     |       | _   |       |       |
| l | PH   | IASI | ES  | 1    | 2   | 3   | 4   | 5   | 6   | 7        | 8   | 9     | 0          | 1    | 2     | 3       | 4   | 5     | 6   |       |       |
| l | INCL | .UDI | ΕD  |      | Χ   |     | Χ   | •   | •   | •        | •   | •     | •          | •    | •     | •       | •   | •     | •   |       | - (A) |
| l | PR01 | EC   | Т   |      |     |     |     |     |     |          |     |       |            |      |       |         |     |       |     |       |       |
| l | PED  | PR   | ТС  |      |     |     |     |     |     |          |     |       |            |      |       |         |     |       |     |       |       |
| l | NOT  | ٥٧١  | LΡ  |      |     |     |     |     |     |          |     |       |            |      |       |         |     |       |     |       |       |
| l | FLSH | l GI | RN  |      |     |     |     |     |     |          |     |       |            |      |       |         |     |       |     |       |       |
| l | LAG  | ΧΙ   | РΗ  |      | Χ   |     | Χ   | •   | •   | •        | •   | •     | •          | •    | •     | •       | •   | •     | •   |       | - B   |
| l | LAG  | 2    | РΗ  |      |     |     |     |     |     |          |     |       |            |      |       |         |     |       |     |       |       |
| l |      |      |     |      |     |     |     |     |     |          |     |       |            |      |       |         |     |       |     |       |       |
| l | LAG  | GRI  | N 5 | 5.(  | ) , | ΥEΙ | _ ; | 3.4 | 4 F | REI      | ) ; | 2.(   | ) <i>i</i> | ٩D١  | V (   | GRN     | ۱ ۱ | 0.0   | o — | - COE |       |
| L |      |      |     |      |     |     |     |     |     |          |     |       |            |      |       |         |     |       |     |       |       |

END PROGRAMMING

- (A) INCLUDED phases specify the phases whose timing state is used to derive the state of the overlap. Generally, when any included phase is timing its green interval or the controller is advancing from one included phase to another included phase, the overlap will be green.
- (E) LAG (Trailing) X PH identifies which phases are to time programmed trailing green, yellow, and red. If trailing phases are defined, only those phases will time trailing green, yellow, and red when they advance to yellow change and no other included phase is timing or a phase next selection. If no Trailing phases are defined, then trailing green, yellow, and red are disabled.
- ©DE Normally, if an included phase is terminating and no other included phase is timing or a phase next selection, the terminating included phase's yellow and red are also output to the overlap.

LAG (Trailing) Green, Yellow, Red times provide a means of extending the overlap's green and then timing a specified yellow and red. When the last timing overlap included phase begins its yellow change, the overlap's green interval is extended by the specified Trailing Green time. After Trailing Green has timed, Trailing Yellow and Trailing Red times are used to time the overlap's yellow change and red clearance intervals.

Timed Overlap Programming Detail - ASC/3-2070

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

14.0

SHEET 3 OF 3

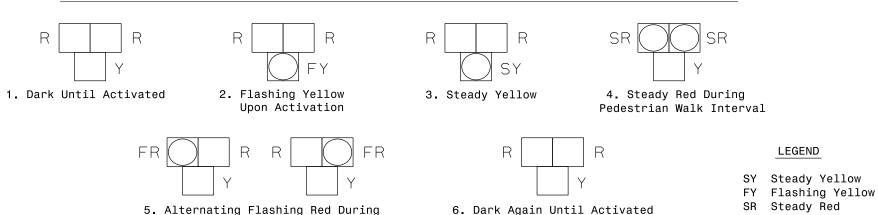
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|       |   |          |
|       |   | STD. NO. |
|       | SIGNALS MANAGEMENT  |          |
| 10-18 | TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION | SHEET OF |
|       |   |          |

# Pedestrian Hybrid Beacon

A Pedestrian Hybrid Beacon is a pedestrian crossing that utilizes a three section signal head to display a unique sequence to vehicle traffic when pedestrians are given right of way. When no peds are being served the ped signal heads display the don't walk ("hand") indication and the vehicle signal heads are dark. When a ped call is received by the controller and the sequence begins the yellow vehicle signal face will flash for a designated time, then the yellow signal face will display steady yellow for a designated time after which both of the red signal faces will display steady red. After the vehicle red clearance has finished timing, right of way transfers to the pedestrian movements and the controller turns on the ped signal head walk ("man") indication. When the ped clearance starts and begins to flash the don't walk ped signal head indication, the red vehicle signal faces change from steady red to alternating flashing red and will continue to do so until the end of the ped interval. At this time the ped signal heads will display the steady don't walk indication and the flashing red vehicle heads will go dark. Right of way at this point is returned to vehicle traffic until another ped call is received by the controller and the Pedestrian Hybrid Beacon sequence is initiated again.

The timing specifics for the Pedestrian Hybrid Beacon can be found in the Signal Design section of the Design Manual in STD. NO. 7.2. Timing for Oasis, ASC/3-2070, and SE-PAC2070 softwares are shown there but the Signals Management portion of the Design Manual will only discuss the details for Oasis and ASC/3-2070 Pedestrian Hybrid Beacon applications.

From MUTCD 2009 Edition Part 4 Figure 4F-3. Sequence for a Pedestrian Hybid Beacon



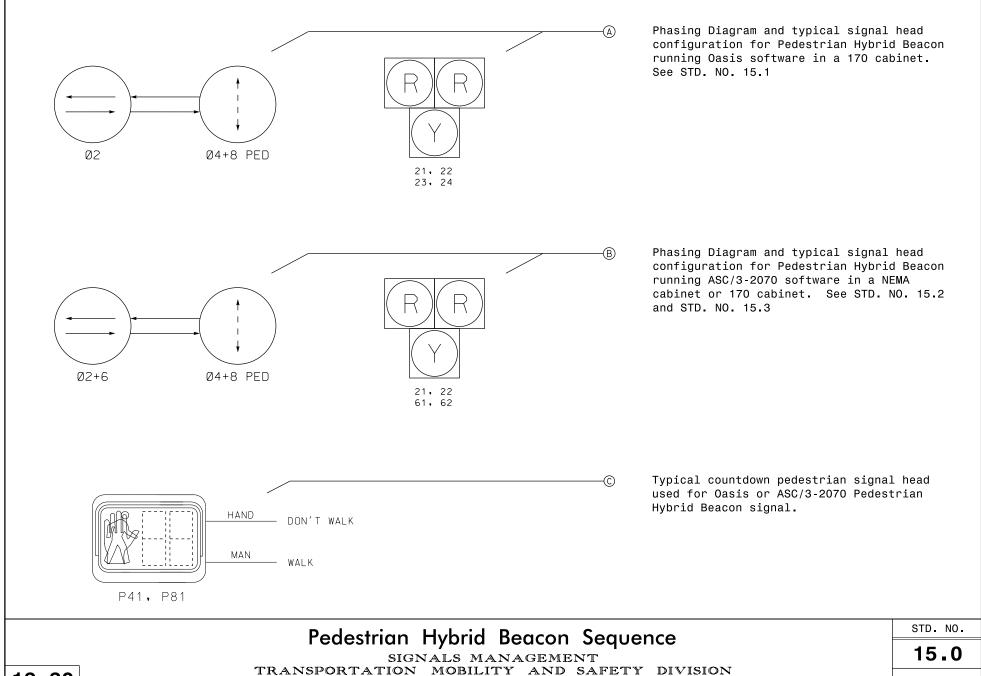
# Pedestrian Hybrid Beacon Sequence

Pedestrian Clearance Interval

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

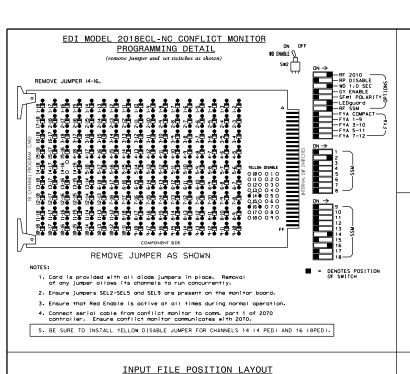
FR Flashing Red

15.0



NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

SHEET 2 OF 2



### NOTES

- Insert yellow flash program blocks for phases 1 and 2. Insert red flash program blocks for all remaining unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- 2. Program Phases 4 and 8 for Ped Yellow Clear.
- 3. Program phase 2 for Startup In Green.
- 4. Program phases 4 and 8 for Startup Ped Call.
- 5. Program Phase 2 for Yellow Flash.
- 6. The cabinet and controller are part of the (insert)

#### **EQUIPMENT INFORMATION**

CONTROLLER.....2070 CABINET......332 
 OVERLAP "A"
 2\*\*

 OVERLAP "B"
 NONE

 OVERLAP "C"
 NONE

 OVERLAP "D"
 NONE

- \* Phase used for timing purposes only.
- \*\* Used to control clearance intervals.

# PROJECT REFERENCE NO. SHEET NO. Sig.

|                       | SI          | GNA            | L   | ΗEΑ | D I | 100 | K-l        | JP | CHA | ٩RT |     |     |
|-----------------------|-------------|----------------|-----|-----|-----|-----|------------|----|-----|-----|-----|-----|
| LOAD<br>SWITCH NO.    | SI          | S2             | 53  | S4  | S5  | S6  | <b>S</b> 7 | S8 | 59  | S10 | S11 | Siz |
| CMU<br>CHANNEL<br>NO. | 1           | 2              | 13  | 3   | 4   | 14  | 5          | 6  | 15  | 7   | 8   | 16  |
| PHASE                 | OLA<br>DUP. | 2<br>OLA       | PED | 3   | 4   | PED | 5          | 6  | PÉD | 7   | 8   | PEI |
| SIGNAL<br>HEAD NO.    |             | 21,22<br>23,24 | N   | NU  | NC  | P41 | N          | NU | NU  | NU  | NC  | Р8  |
| RED                   | 125         | 128            |     |     |     |     |            |    |     |     |     |     |
| YELLOW                | *           | 129            |     |     |     |     |            |    |     |     |     |     |
| GREEN                 | *           | *              |     |     |     |     |            |    |     |     |     |     |
| RED<br>ARROW          |             |                |     |     |     |     |            |    |     |     |     |     |
| YELLOW<br>ARROW       |             |                |     |     |     |     |            |    |     |     |     |     |
| GREEN<br>ARROW        |             |                |     |     |     |     |            |    |     |     |     |     |
| ₩                     |             |                |     |     |     | 104 |            |    |     |     |     | 116 |
| Ķ                     |             |                |     |     |     | 106 |            |    |     |     |     | 112 |

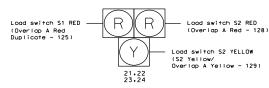
NU = Not Used

NC = Not Connected

\* Denotes install load resistor. See load resistor installation detail on sheet 3.

### SIGNAL HEAD WIRING DETAIL

(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: DESIGNED: SEALED: REVISED:

| LOOP NO.            | LOOP<br>TERMINAL | INPUT<br>FILE POS. | PIN<br>NO. | INPUT<br>ASSIGNMENT<br>NO. | DETECTOR<br>NO. | NEMA<br>PHASE | CALL EXTEND FULL STRETCH DELAY TIME |
|---------------------|------------------|--------------------|------------|----------------------------|-----------------|---------------|-------------------------------------|
| PED PUSH<br>BUTTONS |                  |                    |            |                            |                 |               | NOTE:                               |
| P41                 | TB8-5.6          | I12L               | 69         | 31                         | PED 4           | 4/8 PED       | * INSTALL DC ISOLATORS              |
| P81                 | TB8-8,9          | I13L               | 70         | 32                         | PED 8           | 8/4 PED       | * IN INPUT FILE SLOTS               |

INPUT FILE CONNECTION & PROGRAMMING CHART

INPUT FILE POSITION LEGEND: 12L FILE I LOWER

EX.: 1A, 2A, ETC. = LOOP NO.'S

\* See Pedestrian Detector Programming Detail on Sheet 3.

10 11 12 13

FS

NOT NOT USED USED

Ø4 PENØ8 PEN

FS = FLASH SENSE ST = STOP TIME

### **OPERATIONAL NOTES**

- 1. In order for the controller to perform the "Pedestrian Hybrid Beacon" (aka. HAWK signal) sequence, special logic and output programming is necessary. See programming details on sheet 2 of this electrical detail.
- 2. The modified Phase 2 Yellow output is used to produce the flashing yellow clearance. The Overlap 'A' yellow output has been remapped to the phase 2 yellow output to produce the steady yellow clearance interval and time for this interval shall be implemented in Overlap 'A' Yellow Clear timing. See the signal plan for timing values.
- 3. Phase 2 Yellow Clear and Overlap 'A' GREEN EXTENSION times <u>must</u> be equal. This is necessary so that when flashing yellow clear ends the steady yellow clear begins.
- 4. Phases 4 and 8 Red Clear times  $\underline{\text{must}}$  be set to 0.0 sec.
- 5. The Ped 4 push button is programmed to call Ped 4 and Ped 8. and

Electrical Detail - Sheet 1 of 3 REVIEWED BY: STD. NO.

# Oasis Typical Pedestrian Hybrid Beacon (170 Cabinet)

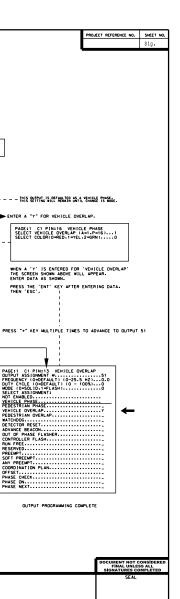
SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION 15.1

SHEET 1 OF 3

12-20

 $_{\mathsf{FILE}}\ ^{\mathsf{U}}$ 

FILE U



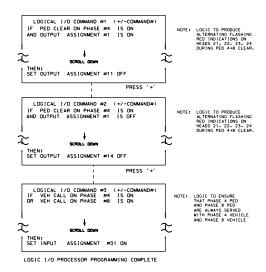
# OUTPUT REMAPPING DETAIL FOR SPECIAL PEDESTRIAN HYBRID BEACON SEQUENCE

PRODUCE SPECIAL PEDESTRIAN HYBRID BEACON SEQUENCE (program controller as shown below)

FROM MAIN MENU PRESS '2' (PHASE CONTROL). THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.

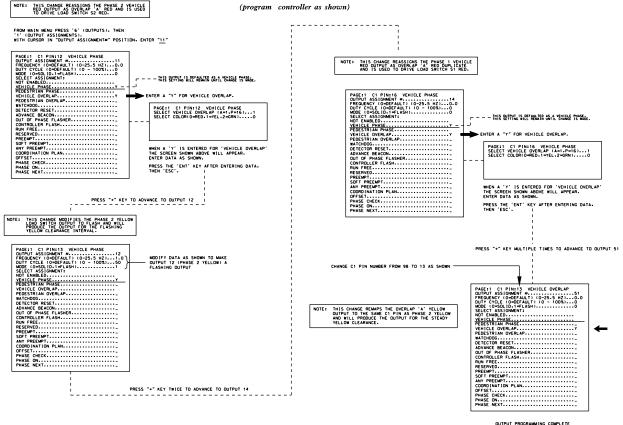
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO

2. FROM MAIN MENU PRESS '6' (OUTPUTS). THEN '3' (LOGICAL I/O PROCESSOR).



**NO REFERENCE SCHEDULE** 

OUTPUT 1 = PHASE 4 DON'T WALK OUTPUT 11 = OLA RED OUTPUT 14 = OLA RED (DUPLICATE) INPUT 31 = PHASE 4 & 8 PED CALL



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: DESIGNED. SEALED: REVISED

Electrical Detail - Sheet 2 of 3 REVIEWED BY: REVISIONS SIGNATURE

# Oasis Typical Pedestrian Hybrid Beacon (170 Cabinet)

SIGNALS MANAGEMENT

TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

15.1

SHEET 2 OF 3

# OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).

OVERLAP PROGRAMMING COMPLETE

# TIMING INTERVAL

NOTICE TIMING INTERVALS THESE COME FROM THE SIGNAL PLAN

PHASE 2 GREEN REST = Dark Display

PHASE 2 YELLOW CLEAR TIME = Flashing Yellow Display

OVERLAP 'A' YELLOW CLEAR TIME = Steady Yellow Display

OVERLAP 'A' RED CLEAR TIME = Steady Red Display

PHASE 4+8 WALK = Steady Red Display

PHASE 4+8 PED CLEAR = Alternating Flashing Red Display

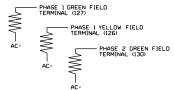
PHASE 4+8 VEH YEL CLR = Alternating Flashing Red Display

PHASE 4+8 VEH RED CLR = Alternating Flashing Red Display

### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)





#### PEDESTRIAN DETECTOR ASSIGNMENT PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '7' (DETECTORS). THEN '2' (PEDESTRIAN DETECTOR ASSIGNMENTS). PRESS '+' UNTIL PED DETECTOR #4 IS REACHED.

PED DETECTOR #4 SETTINGS (+/- DET)
PHASE# ASSIGNED : X X
SETTING: (Y/N)
ENABLE DETECTOR. (Y/N)
ENABLE DIAGNOSTICS N
RECALL IF FAILED N
MAX CALLS/DIAG PERIOD (-255). 255
MAX CALLS/DIAG PERIOD (-255). 0
MAX DECUPANCY % (0-100%). 100

PRESS '+' UNTIL PED DETECTOR #8 IS REACHED

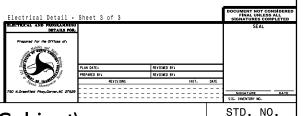
PED DETECTOR #8 SETTINGS (+/- DET)
PHASE# 112345678910111213141516
PHASES ASSIGNED | X X
SETTING: (Y/N)
ENABLE DETECTOR. Y
ENABLE DIGGING. Y
ENABLE DIAGNOSTICS. N
RECALL IF FAILED. Y
MAX CALLS/DIAG PERIOD (0-255). 00
MAX OCCUPANCY X (0-100%). 100

PROGRAMMING COMPLETE

### COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: DESIGNED: SEALED: REVISED:



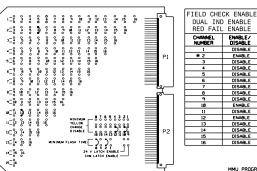
# Oasis Typical Pedestrian Hybrid Beacon (170 Cabinet)

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION 15.1

SHEET 3 OF 3

### EDI MODEL MMU2-16LEip MALFUNCTION MANAGEMENT UNIT PROGRAMMING DETAIL

(program card and tables as shown below)



MMU PROGRAMMING CARD

| OPTION          | SETTING |
|-----------------|---------|
| RECURRENT PULSE | ON      |
| WALK DISABLE    | 0FF     |
| LOG CVM FAULTS  | ON      |
| EXTERN WATCHOOG | OFF     |
| 24V-2=12VDC     | OFF     |
| PGM CARD MEMORY | ON      |
| LEDguard        | ON      |
| FORCE TYPE 16   | OFF     |
| TYPE12-SOLC     | OFF     |
| VM 3x/Day Latch | ON      |

| 1 3x/Day Laten | UN           |  |  |  |  |  |  |  |
|----------------|--------------|--|--|--|--|--|--|--|
|                |              |  |  |  |  |  |  |  |
| FLASHING YE    | LLOW ARROW   |  |  |  |  |  |  |  |
| CONFIG MODE    | В            |  |  |  |  |  |  |  |
| ENABLE CHANN   | EL PAIR, FYA |  |  |  |  |  |  |  |
| CH 1-13        | OFF          |  |  |  |  |  |  |  |
| CH 3-14        | OFF          |  |  |  |  |  |  |  |
| CH 5-15        | OFF          |  |  |  |  |  |  |  |
| CH 7-16        | OFF          |  |  |  |  |  |  |  |
| RED/YEL IN     | OT ENABLE    |  |  |  |  |  |  |  |
| CH 1           | OFF          |  |  |  |  |  |  |  |
| CH 3           | OFF          |  |  |  |  |  |  |  |
| CH 5           | OFF          |  |  |  |  |  |  |  |
| CH 7           | OFF          |  |  |  |  |  |  |  |
| ASH RATE FAULT | OFF          |  |  |  |  |  |  |  |
| YA TRAP DETECT | OFF          |  |  |  |  |  |  |  |
|                |              |  |  |  |  |  |  |  |

#### MMU PROGRAMMING NOTE

ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

\* Only enable R-Y Qual Indication for this channel. During the Flashing Yellow Claerones interval, channel 2 Green will be active at the same time the logic is instructed to turn on channel 2 Yellow 16 G-Y Qual Indication). Enabling G-Y Qual Indication vould result in a self-made

#### NOTES

- To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- To prevent red failures on unused monitor channels, the unused load switch red outputs 1.3.4.5.7.8.9.11.13.14.15, and 16 to load switch AC+ by inserting a jumper plug in the unused load switch socket from pin 1 (LS AC+) to pin 3 (RED out). Make sure all I losh transfer relays are in place.
- 3. Program controller to start up in phase 2 Walk and 6 Walk.
- Set power-up flash time to 10 seconds and implement on the Malfunction Management Unit. Set controller power-up flash time to 0 seconds.
- 5. Enable simultaneous gap-out feature for all phases.
- 6. Program phases 2 and 6 for Rest In Walk.
- 7. Program phase 2 and 6 for Ped Recall.
- 8. Program phases 4 and 8 for PED>CLR RED.

### **EQUIPMENT INFORMATION**

OVERLAPS.....NONE

\* Used for timing purposes only.

#### SIGNAL HEAD HOOK-UP CHART 3 4 5 6 7 8 2 4 6 8 OLA OLB OLC OLD PHASE 2 21,22 61,62 21,22 61,62 NU NC NC NU P41 NC P81 NU NU 2R 6R RED 2Y \* GREEN YELLOW FLASHIN YELLOW ARROW ₩ 10R 12R Ķ 10G 12G

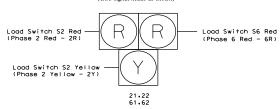
NU = Not Used

NC = Not Connected

\* Denotes install load resistor. See Load Resistor Installation Detail on this sheet.

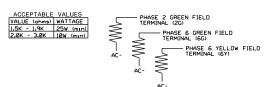
### SIGNAL HEAD WIRING DETAIL

(wire signal heads as shown)



### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)



| lectrical Detail                          | Sheet 1 of 2 |              |       |      | DOCUMENT NOT CON<br>FINAL UNLESS<br>SIGNATURES COM | ALL  |
|---|--------------|--------------|-------|------|--|------|
| LECTRICAL AND PROGRAMMING<br>DETAILS FOR: |              |              |       |      | SEAL   |      |
| Prepared for the Offices of:              |              |              |       |      |  |      |
|   |              |              |       |      |  |      |
|   | PLAN DATE:   | REVIEWED BY: |       |      |  |      |
| V (200 )                                  | PREPARED BY: | REVIEWED BY: |       |      |  |      |
| Simula Management                         | REVISIONS    |              | INIT. | DATE |  |      |
| 50 N.Greenfield Phey.Garner.NC 27529      |              |              |       |      |  |      |
| No more and a mylour and an area          | <b>.</b>     |              |       |      | SIGNATURE<br>SIG. INVENTORY NO.                    | DATE |
|   |              |              | 1     |      | SIG. INVENTURY NO.                                 |      |

# ECONOLITE ASC/3-2070 PEDESTRIAN DETECTOR PHASE ASSIGNMENT PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select 6. DETECTORS
- 2. From DETECTOR Submenu select 3. PED DETECTOR INPUT ASSIGNMENT
- 3. Press the TOGGLE key to select ECONOLITE MODE and press ENTER.

- "." = No assignment, disabled
- X = Assigns Pedestrian Push Button (PPB) to call the phase or phases
- 2 = Call for Ped timing 2
- B = Allows for the PPB to call for Min Green 2 (BIKE GREEN)

### OPERATIONAL NOTES

- In order for the controller to perform the Pedestrian Hybrid Beacon (HAWK signal) sequence, special logic programming is necessary. Refer to sheet 2 for the Econolite ASC/3-2070 Logic Processor Programming Detail.
- For operational purposes, Phase 2 and Phase 6 both run dummy pedestrian phases that are required to produce the correct HAWK signal sequence. There are no Phase 2 or Phase 6 pedestrian heads.
- The only Phase 6 load switch output that is being used drives one of the red signal faces of each signal head.
- 4. The Logic Processor flashes Phase 2 Yellow during the Phase 2 pedestrian clearance phase, and Phase 2 Yellow drives the solid Yellow signal faces during Phase 2 vehicle Yellow clear.
- 5. The Phase 2 and Phase 6 Red outputs drive the solid Red displays during Phase 2 and 6 Red. The Logic Processor flashes the Phase 2 and Phase 6 Red outputs in a wig-wag pattern during Phase 4+8 Ped Clear and thru Phase 4+8 vehicle Yellow and Red clear.
- 6. The controller must be programmed for Ped Clear Thru Red for Pedestrian Phases 4 and 8 so that the Red displays continue to flash during Phases 4 and 8 Yellow Clear and Red clear.
- 7. Make sure that all Phase 2 and Phase 6 timings match each other, and that all Phase 4 and Phase 8 timings match each other.
- 8. The Ped 4 push button is programmed to call Ped 4 and Ped 8, and the Ped 8 push button is programmed to call Ped 8 and Ped 4.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: DESIGNED: SEALED: REVISED:

# ASC/3-2070 Typical Pedestrian Hybrid Beacon (NEMA Cabinet)

SIGNALS MANAGEMENT
TRANSPORTATION MOBILITY AND SAFETY DIVISION
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

STD. NO.

PROJECT REFERENCE NO. SHEET NO.

15.2

SHEET 1 OF 2

# ECONOLITE ASC/3 LOGIC PROCESSOR PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 1. CONFIGURATION

2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR

3. From LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

ENTER A "1" IN THE LP# FIELD. PRESS 'ENTER'. AND

LP#: 1 COPY FROM: 1 ACTIVE: M (T/F) PED ON PH PED CLR LP COB CODE ON 2 IS ON 546 THEN SIG SET PH YELLOW 2

ELSE

ENTER A "2" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#: 2 COPY FROM: 1 ACTIVE: M (T/F) IF PED ON PH PED CLR AND LP COB CODE ON 4 IS ON 546 THEN SIG SET PHASE RED 2 OFF ELSE

ENTER A "3" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

3 COPY FROM: 1 ACTIVE: M (T/F) THEN SIG SET PHASE RED 6 OFF ELSE

OGIC TO PRODUCE ALTERNATING FLASHING RED INDICATIONS ON PED 4+8 CLEAR (FORCES PHASE 6

LOGIC TO FLASH YELLOW SIGNAL FACES AFTER A PED CALL IS PLACED.

LOGIC TO PRODUCE ALTERNATING

FLASHING RED INDICATIONS ON HEADS 21, 22, 61, 62 DURING

PED 4+8 CLEAR (FORCES PHASE 2

END PROGRAMMING

NOTE: COB CODE 546 is a 1Hz 50% Duty Cycle internal logic processor reference.

1. From Main Menu select 1. CONFIGURATION

2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR

3. From LOGIC PROCESSOR Submenu select 1. LOGIC STATEMENT CONTROL

ENABLE LOGIC PROCESSOR STATEMENTS 1-3 BY POSITIONING THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE THEM.

| LOGIC STA | CC | CONTROL |   |   |   |   |   |   |   |   |   |   |   |   |   |   |
|-----------|----|---------|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
|           | 1  | 2       | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 | 1 | 2 | 3 | 4 | 5 | 6 |
| LP 1-15   | Ε  | Ε       | Ε |   |   |   |   |   |   |   |   |   |   |   |   |   |
| LP 16-30  |    |         |   |   |   |   | ٠ |   | ٠ |   |   |   |   |   |   |   |
| LP 31-45  |    |         |   |   |   |   |   |   |   |   |   |   |   |   |   |   |
| LP 46-60  |    |         |   |   |   |   |   |   |   |   |   |   |   |   |   |   |
| LP 61-75  |    |         |   |   |   |   | ٠ |   | ٠ |   |   |   |   |   |   |   |
| LP 76-90  |    |         |   |   |   |   |   |   |   |   |   |   |   |   |   |   |
|           |    |         |   |   |   |   |   |   |   |   |   |   |   |   |   |   |

END PROGRAMMING

# ECONOLITE ASC/3-2070 LOAD SWITCH ASSIGNMENT DETAIL

(program controller as shown)

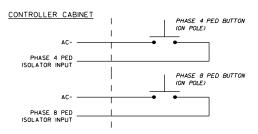
1. From Main Menu select 1. CONFIGURATION

2. From CONFIGURATION Submenu select 3. LOAD SW ASSIGN

LD SWITCH ASSIGN DIMMING ---FLASH---PHASE /OVLP TYPE R Y G D PWR AUT TGR ... + A R X 12

### PEDESTRIAN PUSH BUTTON WIRING DETAIL

(wire push buttons as shown)



### COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

### TIMING INTERVAL

PHASE 2+6 WALK = Dark Display

PHASE 2+6 PED CLEAR = Flashing Yellow Display

PHASE 2+6 VEH YEL CLR = Steady Yellow Display

PHASE 2+6 RED CLEAR = Steady Red Display

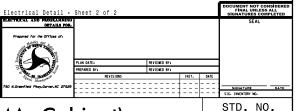
PHASE 4+8 WALK = Steady Red Display

PHASE 4+8 PED CLEAR = Alternating Flashing Red Display

PHASE 4+8 VEH YEL CLR = Alternating Flashing Red Display

PHASE 4+8 VEH RED CLR = Alternating Flashing Red Display

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: DESIGNED: REVISED:



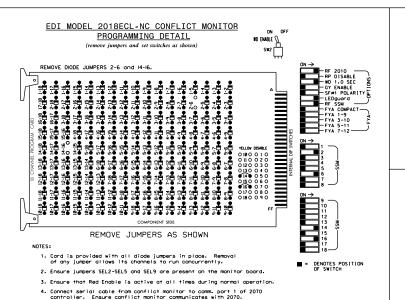
ASC/3-2070 Typical Pedestrian Hybrid Beacon (NEMA Cabinet)

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION 15.2

PROJECT REFERENCE NO. SHEET NO.

Sig

SHEET 2 OF 2



INPUT FILE POSITION LAYOUT

E M M

E MPT

FS

NOT NOT USED

Ø4 PEDØ8 PED

(front view)

# NOTES

- To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans. Insert yellow flash program blocks for load switches S2 and S8.
- 2. Program controller to start up in phases 2 Walk and 6 Walk.
- 3. Enable simultaneous gap-out for all phases.
- 4. Program phases 2 and 6 for Rest In Walk.
- 5. Program phases 2 and 6 for Ped Recall.
- 6. Program phases 4 and 8 for PED>CLR RED.

### EQUIPMENT INFORMATION

CONTROLLER......2070 CABINET.....332

CABINET MOUNT.....BASE

OUTPUT FILE POSITIONS...12

LOAD SWITCHES USED.....\$2.53.58.59

PHASES USED......2,2PED\*,4\*,4PED,6,6PED\*,8\*,8PED

OVERLAPS.....NONE

\* Used for timing purposes only.

# INPUT FILE CONNECTION & PROGRAMMING CHART

| LOOP NO.            | LOOP<br>TERMINAL | INPUT<br>FILE POS. | PIN<br>NO. | DETECTOR<br>NO. | NEMA<br>PHASE | CALL | EXTEND<br>TIME       | DELAY<br>TIME | ADDED<br>INITIAL | DETECTO<br>TYPE |  |  |
|---------------------|------------------|--------------------|------------|-----------------|---------------|------|----------------------|---------------|------------------|-----------------|--|--|
| PED PUSH<br>BUTTONS |                  |                    |            |                 |               | NOTE | :                    |               |                  |                 |  |  |
| P41,P42             | TB8-5,6          | I12L               | 69         | PED 4           | 4/8 PED       | 1 11 | INSTALL DC ISOLATORS |               |                  |                 |  |  |
| P81.P82             | TB8-8,9          | 113L               | 70         | PED 8           | 4/8 PED       | l in | INPUT                | FILE          | SLOTS            |                 |  |  |
| 112 AND 113.        |                  |                    |            |                 |               |      |                      |               |                  |                 |  |  |
|                     | INPUT            | FILE PO            | SITIO      | ON LEGENI       | ): J2L        |      |                      |               |                  |                 |  |  |

#### SIGNAL HEAD HOOK-UP CHART LOAD SWITCH NO. S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 15 PED PED PED. 6 PED PED 2 3 4 6 PHASE NC P81 NC NC RED 128 134 129 GREEN YELLOW 110 104 於 112 106

PROJECT REFERENCE NO. SHEET NO.

Sig.

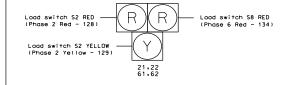
NU = Not Used

NC = Not Connected

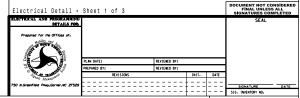
\* Denotes install load resistor. See load resistor installation detail this sheet.

### SIGNAL HEAD WIRING DETAIL

(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: DESIGNED: SEALED: REVISED:



# LOAD RESISTOR INSTALLATION DETAIL (install resistors as shown below)

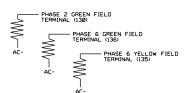
(install resistors as shown below

| ACCEPTABLE VALUES | VALUE (ohms) | WATTAGE | 1.5K - 1.9K | 25W (min) | 2.0K - 3.0K | 10W (min) |

EX.: 1A. 2A. ETC. = LOOP NO.'S

FILE U

FILE



# ASC/3-2070 Typical Pedestrian Hybrid Beacon (170 Cabinet)

SIGNALS MANAGEMENT
TRANSPORTATION MOBILITY AND SAFETY DIVISION
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

STD. NO.

15.3

SHEET 1 OF 3

| OJECT | REFERENCE | NO. | SHEET | NO. |
|-------|-----------|-----|-------|-----|
|       |           |     | 0.5 a |     |

### ECONOLITE ASC/3 LOGIC PROCESSOR

### PROGRAMMING DETAIL

(program controller as shown)

1. From Main Menu select 1. CONFIGURATION

2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR

3. From LOGIC PROCESSOR Submenu select 2. LOGIC STATEMENTS

1. From Main Menu select 1. CONFIGURATION

2. From CONFIGURATION Submenu select 8. LOGIC PROCESSOR

3. From LOGIC PROCESSOR Submenu select 1. LOGIC STATEMENT CONTROL

ENTER A "1" IN THE LP# FIELD. PRESS 'ENTER'. AND PROGRAM AS SHOWN.

LP#: 1 COPY FROM: 1 ACTIVE: M (T/F) IF PED ON PH PED CLR 2 IS ON AND LP COB CODE ON 546 THEN SIG SET PH YELLOW 2 ON ELSE

LOGIC TO FLASH YELLOW SIGNAL FACES AFTER A PED CALL IS PLACED.

ENTER A "2" IN THE LP# FIELD. PRESS 'ENTER'. AND PROGRAM AS SHOWN.

LP#: 2 COPY FROM: 2 ACTIVE: M (T/F) 1F PED ON PH PED CLR 4 IS ON AND LP COB CODE ON 546 THEN SIG SET PHASE RED 2 OFF ELSE

LOGIC TO PRODUCE ALTERNATING FLASHING RED INDICATIONS ON HEADS 21, 22, 61, 62 DURING

ENTER A "3" IN THE LP# FIELD, PRESS 'ENTER', AND PROGRAM AS SHOWN.

LP#: 3 COPY FROM: 3 ACTIVE: M (T/F) IF PED ON PH PED CLR 4 IS ON AND LP COB CODE OFF 546 THEN SIG SET PHASE RED 6 ELSE

LOGIC TO PRODUCE ALTERNATING FLASHING RED INDICATIONS ON PED 4+8 CLEAR (FORCES PHASE 6

ENTER A "4" IN THE LP# FIELD. PRESS 'ENTER'. AND

LP#: 4 COPY FROM: 4 ACTIVE: M (T/F) IF PED ON PH PED CLR 2 IS ON THEN SIG SET PH GREEN 2 ELSE

END PROGRAMMING

OFF DURING PHASE 2 PED CLEAR TO AVOID A G/Y DUAL INDICATION.

TURNS LOAD SWITCH 2 GREEN

ENABLE LOGIC PROCESSOR STATEMENTS 1-4 BY POSITIONING THE CURSOR OVER THE FIELDS SHOWN BELOW AND USING THE TOGGLE KEY TO ENABLE THEM.

| LOC | IC STA | TEM | ENT | СО | INTR | OL |   |   |   |   |   |   |   |   |   |   |   |
|-----|--------|-----|-----|----|------|----|---|---|---|---|---|---|---|---|---|---|---|
|     |        | 1   | 2   | 3  | 4    | 5  | 6 | 7 | 8 | 9 | 0 | 1 | 2 | 3 | 4 | 5 | 6 |
| LP  | 1-15   | Ε   | Ε   | Ε  | Ε    |    |   |   |   |   |   |   |   |   |   |   |   |
| LP  | 16-30  |     |     |    |      |    |   |   |   |   |   |   |   |   |   |   |   |
| LP  | 31-45  |     |     |    |      |    |   | ٠ |   | ٠ |   |   |   | ٠ |   |   |   |
| LP  | 46-60  |     |     |    |      |    |   |   |   |   |   |   |   |   |   |   |   |
| LP  | 61-75  |     |     |    |      |    |   | ٠ |   | ٠ |   |   |   |   |   |   |   |
| LP  | 76-90  |     |     |    |      |    |   |   |   |   |   |   |   |   |   |   |   |
|     |        |     |     |    |      |    |   |   |   |   |   |   |   |   |   |   |   |

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: DESIGNED: SEALED: REVISED:

# ECONOLITE ASC/3-2070 PEDESTRIAN DETECTOR

COUNTDOWN PEDESTRIAN SIGNAL OPERATION Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual

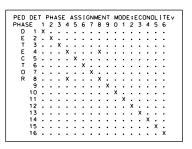
PHASE ASSIGNMENT PROGRAMMING DETAIL (program controller as shown)

1. From Main Menu select 6. DETECTORS

for instructions on selecting this feature.

2. From DETECTOR Submenu select 3. PED DETECTOR INPUT ASSIGNMENT

3. Press the TOGGLE key to select ECONOLITE MODE and press ENTER.

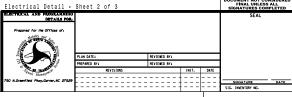


"." = No assignment, disabled

X = Assigns Pedestrian Push Button (PPB) to call the phase or

2 = Call for Ped timing 2

B = Allows for the PPB to call for Min Green 2 (BIKE GREEN)



NOTE: COB CODE 546 is a 1Hz 50% Duty Cycle internal logic processor reference.

# ASC/3-2070 Typical Pedestrian Hybrid Beacon (170 Cabinet)

SIGNALS MANAGEMENT TRANSPORTATION MOBILITY AND SAFETY DIVISION NORTH CAROLINA DEPARTMENT OF TRANSPORTATION STD. NO.

15.3

SHEET 2 OF 3

| PROJECT REFERENCE NO. | SHEET NO. |
|-----------------------|-----------|
|                       | Sig.      |

### TIMING INTERVAL

PHASE 2+6 WALK = Dark Display

PHASE 2+6 PED CLEAR = Flashing Yellow Display

PHASE 2+6 VEH YEL CLR = Steady Yellow Display

PHASE 2+6 RED CLEAR = Steady Red Display

PHASE 4+8 WALK = Steady Red Display

PHASE 4+8 PED CLEAR = Alternating Flashing Red Display

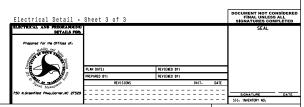
PHASE 4+8 VEH YEL CLR = Alternating Flashing Red Display

PHASE 4+8 VEH RED CLR = Alternating Flashing Red Display

### OPERATIONAL NOTES

- In order for the controller to perform the Pedestrian Hybrid Beacon (HAWK signal) sequence, special logic programming is necessary. Refer to sheet 2 for the Econolite ASC/3-2070 Logic Processor Programming Detail.
- For operational purposes. Phase 2 and Phase 6 both run dummy pedestrian phases that are required to produce the correct HAWK signal sequence. There are no Phase 2 or Phase 6 pedestrian heads.
- The only Phase 6 load switch output that is being used drives one of the red signal faces of each signal head.
- 4. The Logic Processor floshes Phase 2 Yellow during the Phase 2 pedestrian clearance phase, and Phase 2 Yellow drives the solid Yellow signal faces during Phase 2 vehicle Yellow clear.
- 5. The Phase 2 and Phase 6 Red outputs drive the solid Red displays during Phase 2 and 6 Red. The Logic Processor flashes the Phase 2 and Phase 6 Red outputs in a wig-wag pattern during Phase 4+8 Ped Clear and thru Phase 4+8 vehicle Yellow and Red clear.
- 6. The controller must be programmed for Ped Clear Thru Red for Pedestrian Phases 4 and 8 so that the Red displays continue to flash during Phases 4 and 8 Yellow Clear and Red clear.
- 7. Make sure that all Phase 2 and Phase 6 timings match each other, and that all Phase 4 and Phase 8 timings match each other.
- 8. The Ped 4 push button is programmed to call Ped 4 and Ped 8, and the Ped 8 push button is programmed to call Ped 8 and Ped 4.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: DESIGNED: SEALED: REVISED:



ASC/3-2070 Typical Pedestrian Hybrid Beacon (170 Cabinet)

SIGNALS MANAGEMENT
TRANSPORTATION MOBILITY AND SAFETY DIVISION
NORTH CAROLINA DEPARTMENT OF TRANSPORTATION

SHEET 3 OF 3

STD. NO.

15.3